

**isc Silicon NPN Power Transistor**

**TIPL760C**

**FEATURES**

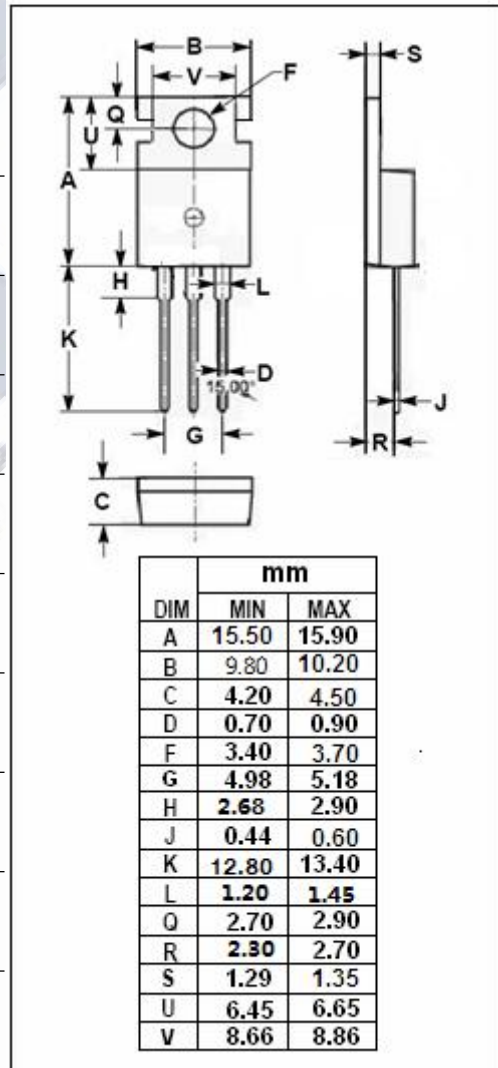
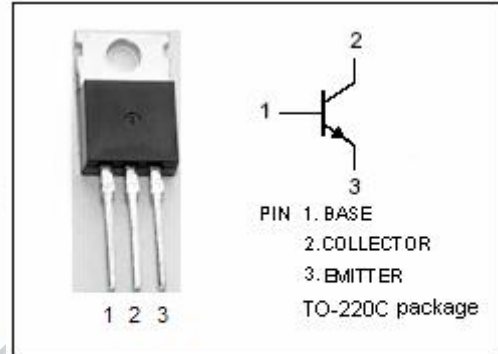
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = 550V(\text{Min.})$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**DESCRIPTION**

- Rugged Triple-diffused planar construction

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	1200	V
$V_{CEO}$	Collector-Emitter Voltage	550	V
$V_{EBO}$	Emitter-Base Voltage	10	V
$I_c$	Collector Current-Continuous	4	A
$I_{CM}$	Peak collector current	8	A
$P_c$	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	75	W
$T_j$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-65~150	$^\circ\text{C}$



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**ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA	550			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 2A, I <sub>B</sub> = 0.4A			1.0	V
		I <sub>C</sub> = 3A, I <sub>B</sub> = 0.6A			2.5	V
		I <sub>C</sub> = 3A, I <sub>B</sub> = 0.6A, T <sub>C</sub> =100°C			5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 2A, I <sub>B</sub> = 0.4A			1.2	V
		I <sub>C</sub> = 3A, I <sub>B</sub> = 0.6A			1.4	V
		I <sub>C</sub> = 3A, I <sub>B</sub> = 0.6A, T <sub>C</sub> =100°C			1.3	V
I <sub>CEO</sub>	Collector Cutoff current	V <sub>CE</sub> = 550V, I <sub>E</sub> = 0			50	μA
I <sub>EBO</sub>	Emitter Cutoff current	V <sub>EB</sub> = 10V, I <sub>C</sub> = 0			1	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 0.5A; V <sub>CE</sub> = 5V	20		60	