

DESCRIPTION

The fundamental of SP6013A synchronous rectifier (SR) driver IC is based on our U.S. patented methods that utilize the principle of "prediction" logic circuit. The IC deliberates previous cycle timing to control the SR in present cycle by "predictive" algorithm that makes adjustments to the turn-off time, in order to achieve maximum efficiency and avoid cross-conduction at the same time. It also maintains the MOSFET's body diode conduction at minimum level. The SP6013A is capable to adapt in almost all existing flyback converters with few adjustments considered necessary.

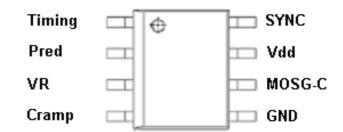
FEATURES

- Offers efficiency improvement over Schottky Diode (depends on drive configuration of the SR).
- Drives all logic level Power MOSFET.
- Prediction gate timing control.
- Minimum MOSFET body diode conduction.
- Operating frequency up to 650 KHz.
- Synchronize to transformer secondary voltage waveform.

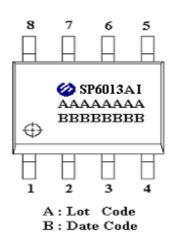
APPLICATIONS

- Servers & workstations
- Storage area network power supplies
- Telecommunication converters
- Embedded systems
- Industrial & commercial systems using high current processors

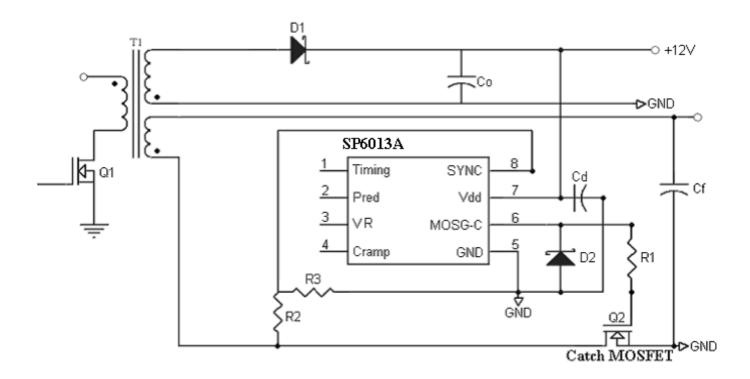
PIN CONFIGURATION (SOP-8)



PART MARKING



TYPICAL APPLCATION CIRCUIT

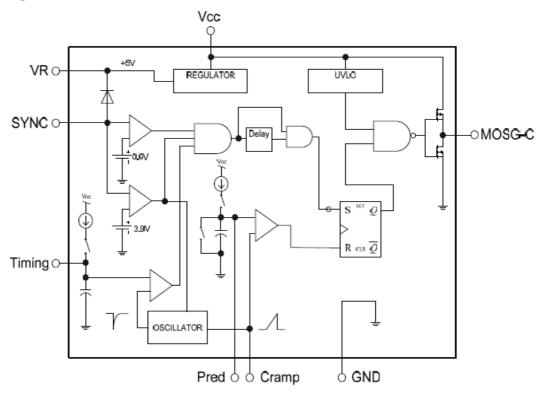


PIN DESCRIPTION

Pin	Symbol	Description	
1	Timing	Discontinuous current filter timing adjustment resistor connection.	
2	Pred	Capacitor to store previous cycle timing for Catch MOSFET	
3	VR	Voltage Regulator	
4	Cramp	Ramp capacitor adjustment to extend MOSFET's gate timing.	
5	GND	Ground connection.	
6	MOSG-C	Catch MOSFET gate drive.	
7	Vdd	DC supply voltage.	
8	SYNC	Synchronized signal from transformer's output.	



BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Package	Part Marking
SP6013AS8RG	SOP-8	SP6013A I
SP6013AS8TG	SOP-8	SP6013A I
SP6013AS8RGB	SOP-8	SP6013AI
SP6013AS8TGB	SOP-8	SP6013AI

※ SP6013AS8RG: 7" Tape Reel; Pb − Free

※ SP6013AS8TG: Tube; Pb − Free

※ SP6013AS8RGB: 7" Tape Reel; Pb − Free; Halogen - Free

※ SP6013AS8TGB: Tube; Pb − Free; Halogen - Free

ABSOULTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit
V_{dd}	DC Supply Voltage	17	V
SYNC	SYNC Voltage	7	V
I_{OUT}	Peak Source Current (Pulsed)	3	A
	Peak Sink Current (Pulsed)	3	A
P_{D}	Power Dissipation @ $T_A=85^{\circ}C$ (*)	0.25	W
$T_{ m J}$	Operating Junction Temperature Range	-40 to125	$^{\circ}\mathbb{C}$
T_{STG}	Storage Temperature Range	-40 to 150	$^{\circ}\mathbb{C}$
$T_{ m LEAD}$	Lead Soldering Temperature for 5 sec.	260	$^{\circ}\mathbb{C}$

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
Rөjc	Thermal Resistance Junction – Case (*)	45	°C/W

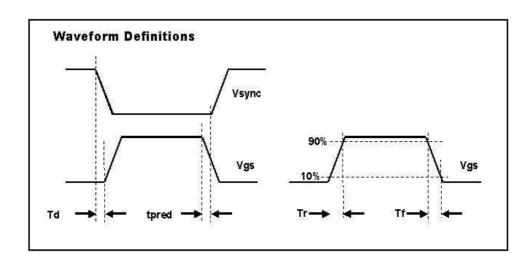
^(*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.

ELECTRICAL CHARACTERISTICS

 $(T_A=25^{\circ}\text{C}, V_{dd}=12\text{V}, \text{Freq.}=300 \text{ KHz}, \text{Duty Cycle}=50\%, \text{unless otherwise specified.})$

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit	
SUPPLY IN	SUPPLY INPUT						
Idd	Supply ourrant	No load		10	16	mA	
100	Supply current	V _{SYNC} =0V, No load		7.5	10	mA	
Vonth	Vdd turn on threshold			9.5	10	V	
Voffth	Vdd turn off threshold		8	8.5		V	
SYNC REFI	ERENCE (SYNC)		·				
Vshth	SYNC high threshold		3.9	5.0		V	
Vslth	SYNC low threshold			0.9	1.2	V	
Isync	SYNC input current				3	mA	
Voltage Regu	ulator REFERENCE (VR)						
Ivr	VR Output Current				25	mA	
ON TIME D	UTY SETUP (PIN 6)		·				
Ton-time			10			us	
MOSFET G	ATE DRIVER (MOSG-C)						
Voh	Output high voltage	Io = -200 mA	11.5	11.8		V	
Vol	Output low voltage	Io = 200mA		0.1	0.2	V	
Td	Propagation delay	No load	15	35		ns	
Tpred		No load		120		ns	
Tr	Rise time	Load = 1nF(*)		10	25	ns	
Tf	Fall time	Load = 1nF(*)		10	25	ns	

^(*) Tr & Tf are measured among 10% and 90% of starting and final voltage.



PERFORMANCE CHARACTERISTICS (T_A=25°C, unless otherwise specified.)

Figure 1: Supply Current vs Supply Voltage

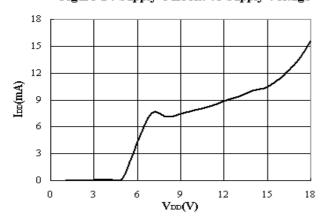
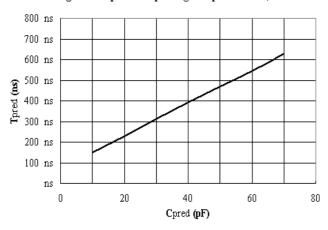
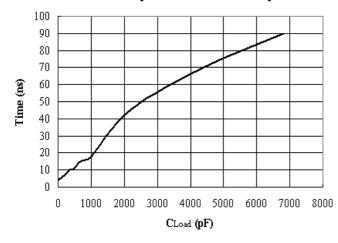


Figure 3 : Tpred vs Cpred @ Freq = 70 KHz ; V_{DD} =10V

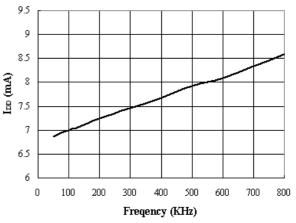


Fihure 5: Output Fall Time vs Load Capacitor



*Fig. 1 : No Load ; No SYNC *Fig. 4~5 : Frequency = 65 kHz.

Figure 2 : Supply Current vs Freq. @ No Load



Fihure 4: Output Rise Time vs Load Capacitor

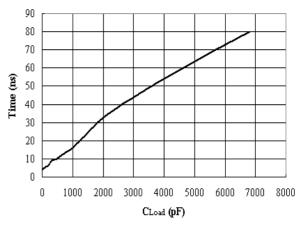
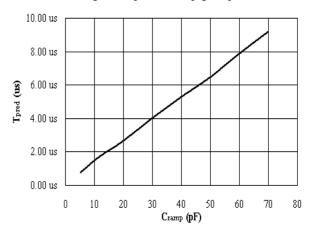
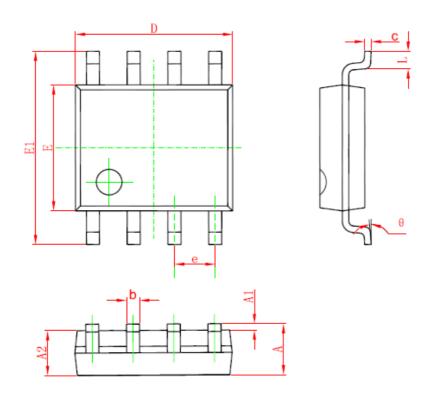


Figure 6: Tperd vs Cramp @ Freq = 20 KHz





SOP- 8 PACKAGE OUTLINE



Comb a l	Dimensions I	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
А	1. 350	1. 750	0.053	0.069
A1	0. 100	0. 250	0.004	0.010
A2	1. 350	1. 550	0.053	0.061
b	0. 330	0. 510	0.013	0.020
С	0. 170	0. 250	0.006	0.010
D	4. 700	5. 100	0. 185	0. 200
Е	3. 800	4. 000	0.150	0. 157
E1	5. 800	6. 200	0. 228	0. 244
е	1. 270 (BSC)		0.050	(BSC)
L	0. 400	1. 270	0.016	0.050
θ	0°	8°	0°	8°



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