



## Transistors

## ●Absolute maximum ratings (Ta=25°C)

Di1

Parameter	Symbol	Limits	Unit
Average rectified forward current	I <sub>o</sub>	200	mA
Forward current surge peak (60Hz, 1∞)	I <sub>FSM</sub>	1	A
Reverse voltage (DC)	V <sub>R</sub>	30	V
Junction temperature	T <sub>j</sub>	125	°C

Tr2

Parameter	Symbol	Limits	Unit
Collector-base voltage	V <sub>CBO</sub>	-15	V
Collector-emitter voltage	V <sub>CEO</sub>	-12	V
Emitter-base voltage	V <sub>EBO</sub>	-6	V
Collector current	I <sub>c</sub>	-500	mA
	I <sub>CP</sub>	-1	A
Power dissipation	P <sub>d</sub>	120	mW *
Junction temperature	T <sub>j</sub>	150	°C

\* Each terminal mounted on a recommended.

Di1 / DTr2

Parameter	Symbol	Limits	Unit
Power dissipation	P <sub>d</sub>	150	mW *
Storage temperature	T <sub>stg</sub>	-55 to +125	°C

\* Each terminal mounted on a recommended.

## ●Electrical characteristics (Ta=25°C)

Di1

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>F</sub>	-	0.40	0.50	V	I <sub>F</sub> =200mA
Reverse current	I <sub>R</sub>	-	4.0	30	μA	V <sub>R</sub> =10V

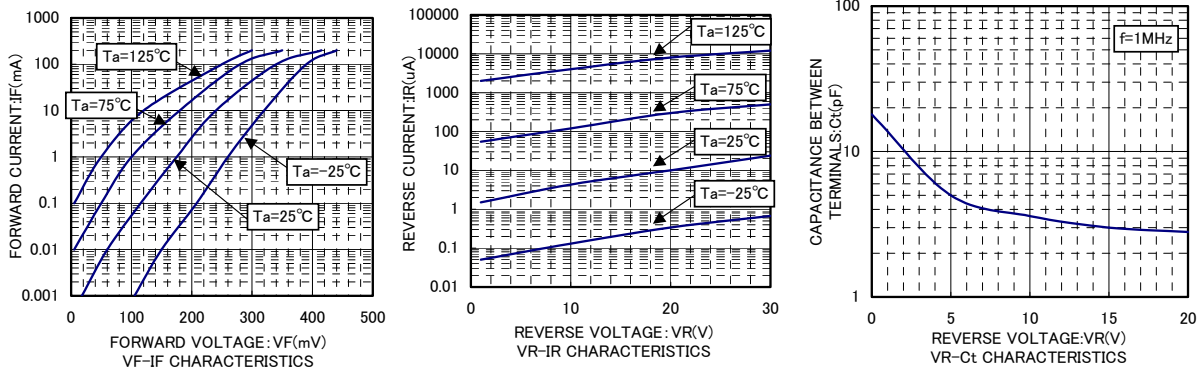
Tr2

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	-12	-	-	V	I <sub>c</sub> =-1mA
Collector-base breakdown voltage	BV <sub>CBO</sub>	-15	-	-	V	I <sub>c</sub> =-10μA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	-6	-	-	V	I <sub>E</sub> =-10μA
Collector cut-off current	I <sub>CBO</sub>	-	-	-100	nA	V <sub>CB</sub> =-15V
Emitter cut-off current	I <sub>EBO</sub>	-	-	-100	nA	V <sub>EB</sub> =-6V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	-	-100	-250	mV	I <sub>c</sub> =-200mA, I <sub>B</sub> =-10mA
DC current gain	h <sub>FE</sub>	270	-	680	-	V <sub>CE</sub> =-2V, I <sub>c</sub> =-10mA
Transition frequency	f <sub>T</sub>	-	260	-	MHz	V <sub>CE</sub> =-2V, I <sub>E</sub> =10mA, f=100MHz
Collector output capacitance	C <sub>ob</sub>	-	6.5	-	pF	V <sub>CB</sub> =-10V, I <sub>E</sub> =0mA, f=1MHz

Transistors

●Electrical characteristic curves

Di1



Tr2

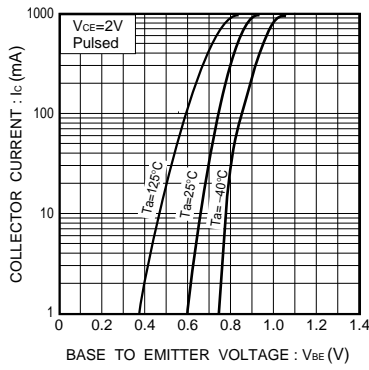


Fig.3 Grounded emitter propagation characteristics

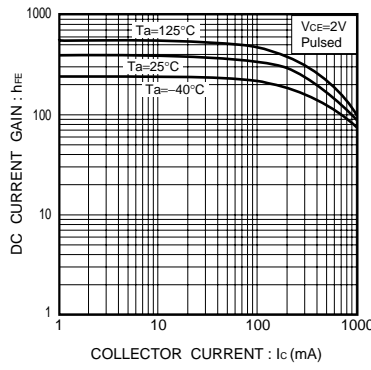


Fig.4 DC current gain vs. collector current

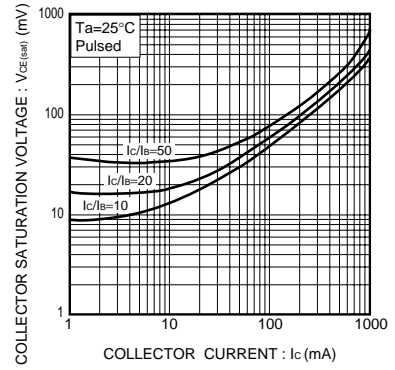


Fig.5 Collector-emitter saturation voltage vs. collector current ( I )

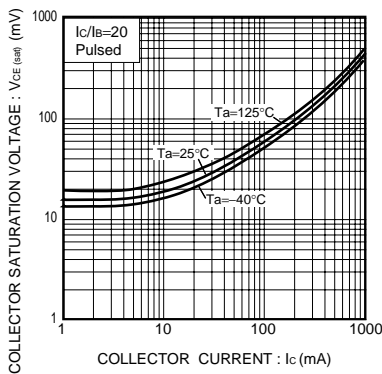


Fig.6 Collector-emitter saturation voltage vs. collector current ( II )

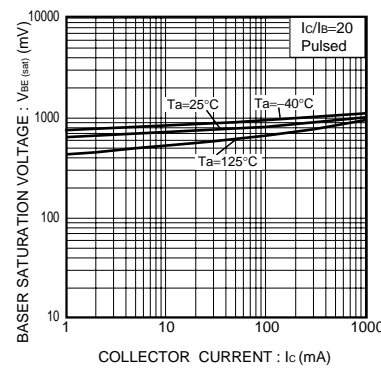


Fig.7 Base-emitter saturation voltage vs. collector current

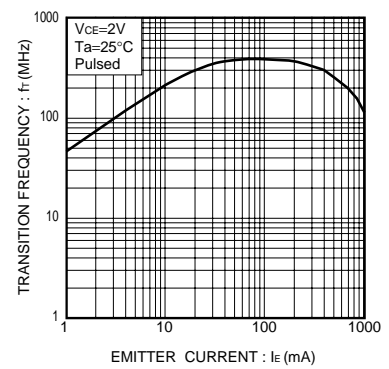


Fig.8 Gain bandwidth product vs. emitter current

Transistors

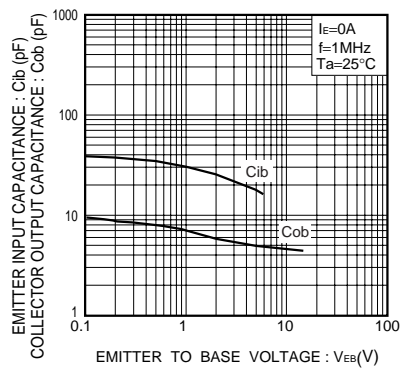


Fig.9 Collector output capacitance vs. collector-base voltage  
Emitter input capacitance vs. emitter-base voltage

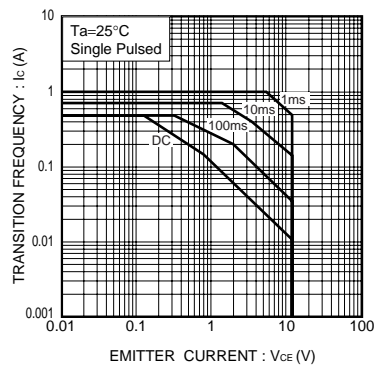


Fig.10 Safe operation area

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