

## Multilayer Ceramic Chip Capacitors

## C0402C0G1C101J020BC



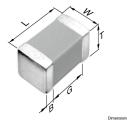






#### TDK item description C0402C0G1C101JT\*\*\*\*\*

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C0402 [EIA 01005]	
Status	Production (Not Recommended for New Design)	



	Size	
Length(L)	0.40mm ±0.02mm	
Width(W)	0.20mm ±0.02mm	
Thickness(T)	0.20mm ±0.02mm	
Terminal Width(B)	0.07mm Min.	
Terminal Spacing(G)	0.14mm Min.	
Recommended Land Pattern (PA)	0.15mm to 0.25mm	
Recommended Land Pattern (PB)	0.15mm to 0.25mm	
Recommended Land Pattern (PC)	0.15mm to 0.25mm	

Electrical Characteristics		
Capacitance	100pF ±5%	
Rated Voltage	16VDC	
Temperature Characteristic	C0G(0±30ppm/°C)	
Q (Min.)	1000	
Insulation Resistance (Min.)	10000ΜΩ	

Other		
Soldering Method	Reflow	
AEC-Q200	No	
Packing	Punched (Paper)Taping [180mm Reel]	
Package Quantity	20000pcs	

<sup>!</sup> Images are for reference only and show exemplary products. ! This PDF document was created based on the data listed on the TDK Corporation website.

<sup>!</sup> All specifications are subject to change without notice.

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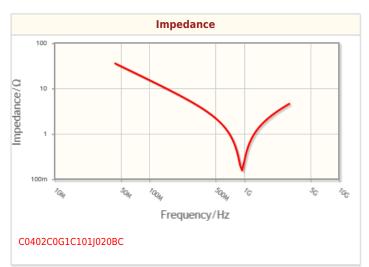


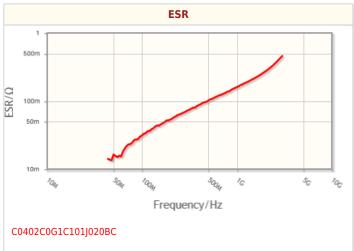


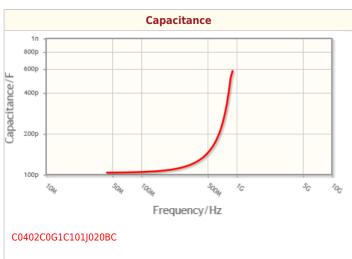


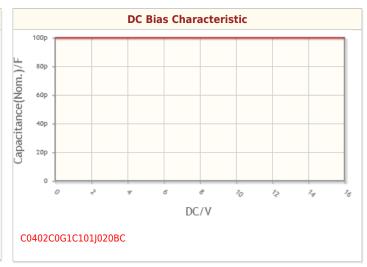


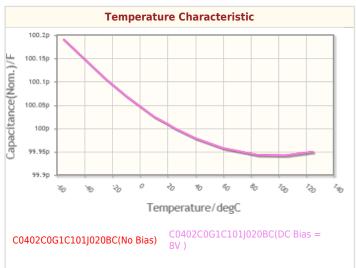
# $Characteristic\ Graphs (This\ is\ reference\ data,\ and\ does\ not\ guarantee\ the\ products\ characteristics.)$











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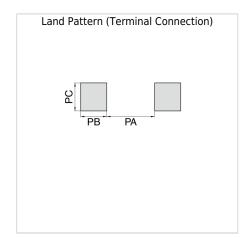








## **Associated Images**



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