

## N-Channel 650-V (D-S) MOSFET

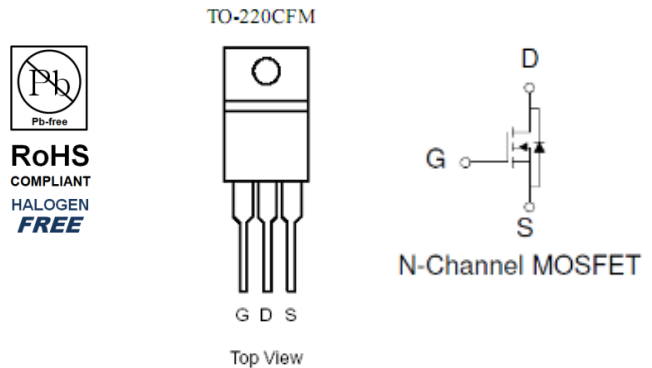
### Key Features:

- Low  $r_{DS(on)}$  trench technology
- Low thermal impedance
- Fast switching speed

### Typical Applications:

- Power Supplies
- Motor Drives
- Consumer Electronics

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ (m $\Omega$ )	$I_D$ (A)
650	800 @ $V_{GS} = 10V$	7
	850 @ $V_{GS} = 6V$	6.5



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		$V_{DS}$	650	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>a</sup>	$T_C = 25^\circ\text{C}$	$I_D$	7	A
Pulsed Drain Current <sup>b</sup>		$I_{DM}$	30	
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	7	A
Power Dissipation <sup>a</sup>	$T_C = 25^\circ\text{C}$	$P_D$	60	W
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient <sup>c</sup>		$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Case		$R_{\theta JC}$	2.5	

### Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board.

## Electrical Characteristics

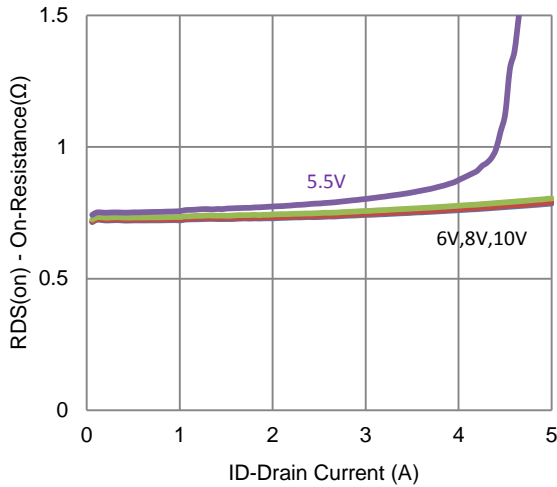
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static</b>						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 520 V, V_{GS} = 0 V$			1	uA
		$V_{DS} = 520 V, V_{GS} = 0 V, T_J = 55^\circ C$			25	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	15			A
Drain-Source On-Resistance <sup>a</sup>	$r_{DS(on)}$	$V_{GS} = 10 V, I_D = 2 A$			800	mΩ
		$V_{GS} = 6 V, I_D = 1.6 A$			850	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15 V, I_D = 2 A$		22		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 6 A, V_{GS} = 0 V$		0.82		V
<b>Dynamic <sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 100 V, V_{GS} = 6 V,$ $I_D = 2 A$		23		nC
Gate-Source Charge	$Q_{gs}$			11		
Gate-Drain Charge	$Q_{gd}$			11		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 100 V, R_L = 50 \Omega,$ $I_D = 2 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 \Omega$		24		ns
Rise Time	$t_r$			10		
Turn-Off Delay Time	$t_{d(off)}$			60		
Fall Time	$t_f$			11		
Input Capacitance	$C_{iss}$	$V_{DS} = 15 V, V_{GS} = 0 V, f = 1 \text{ Mhz}$		2691		pF
Output Capacitance	$C_{oss}$			165		
Reverse Transfer Capacitance	$C_{rss}$			33		

## Notes

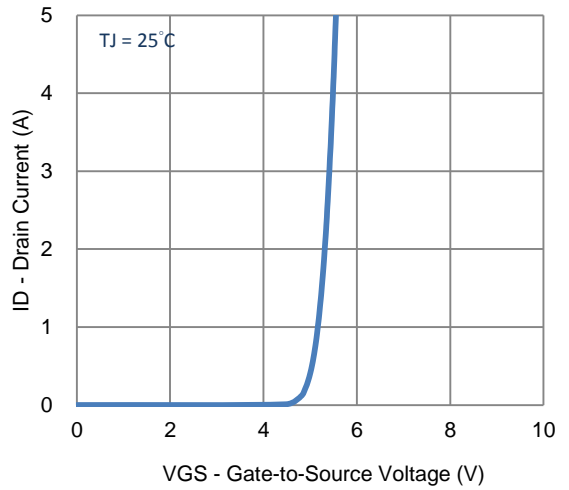
- Pulse test:  $PW \leq 300 \mu s$  duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.

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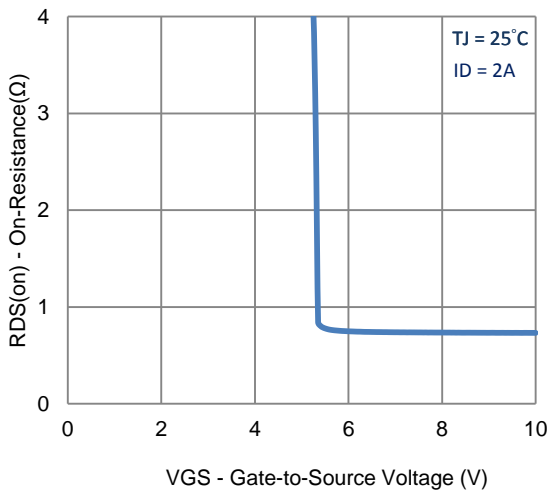
Typical Electrical Characteristics



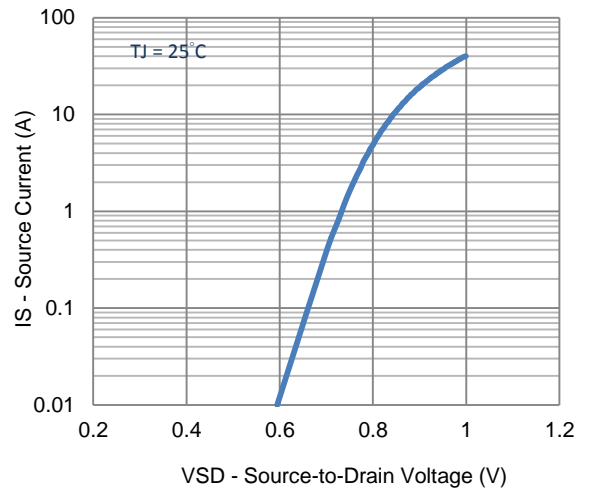
1. On-Resistance vs. Drain Current



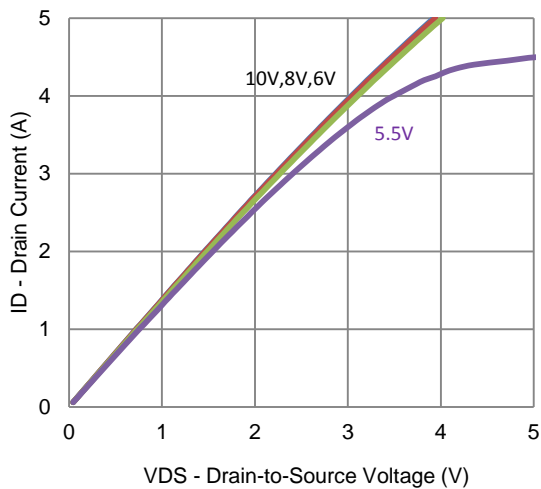
2. Transfer Characteristics



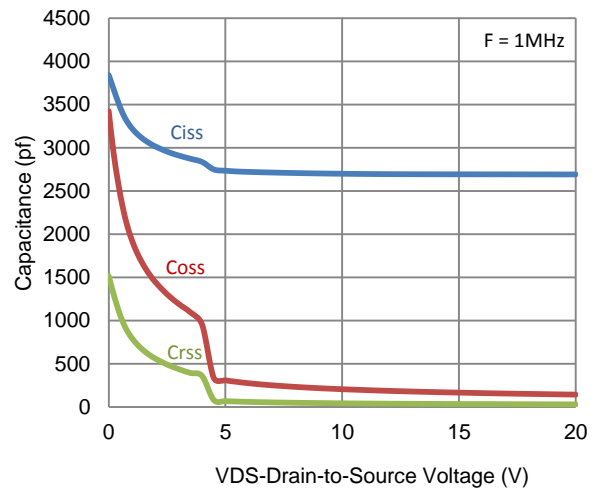
3. On-Resistance vs. Gate-to-Source Voltage



4. Drain-to-Source Forward Voltage

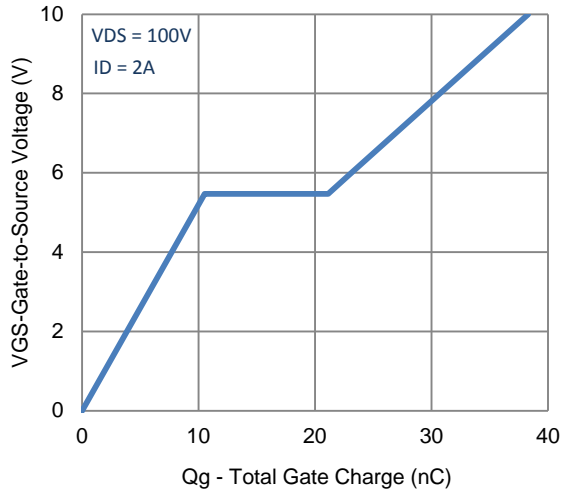


5. Output Characteristics

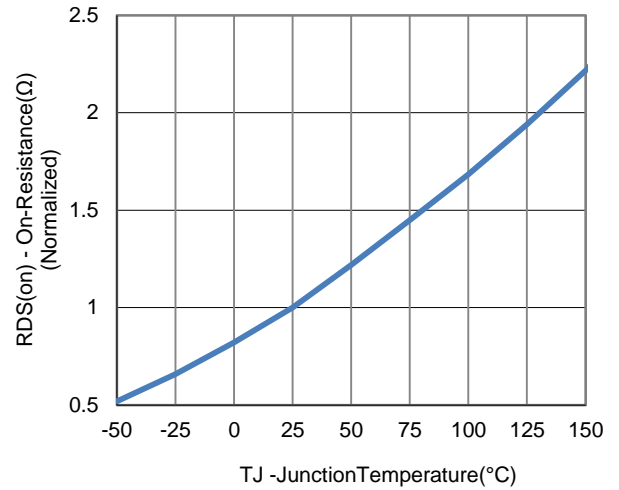


6. Capacitance

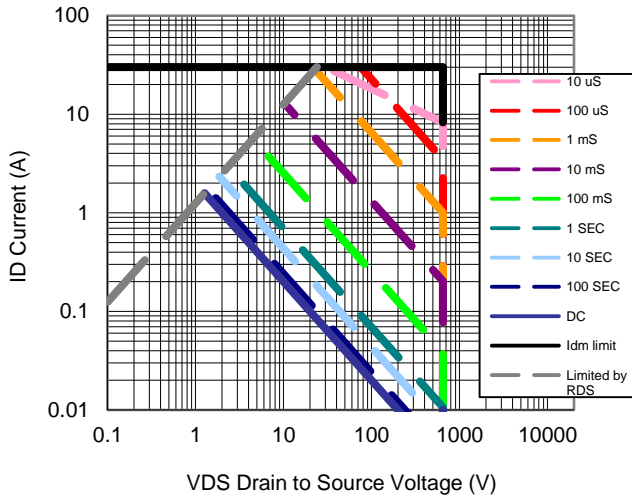
Typical Electrical Characteristics



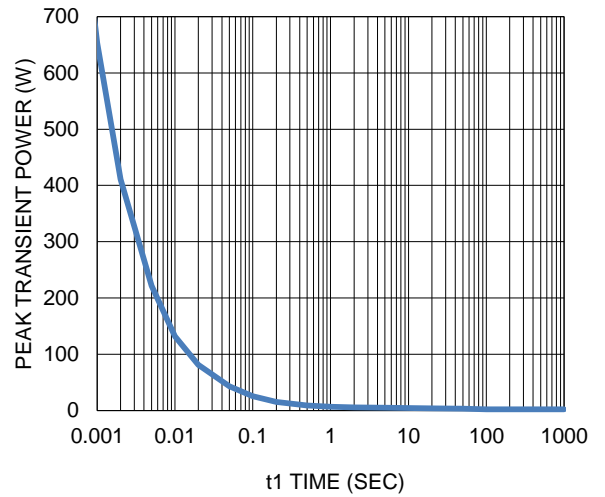
7. Gate Charge



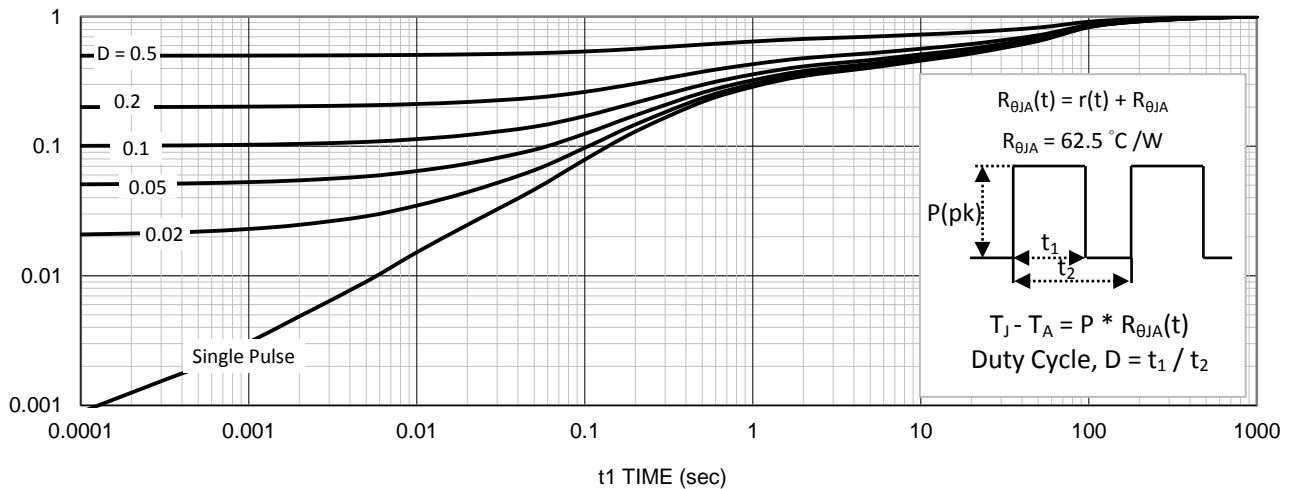
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

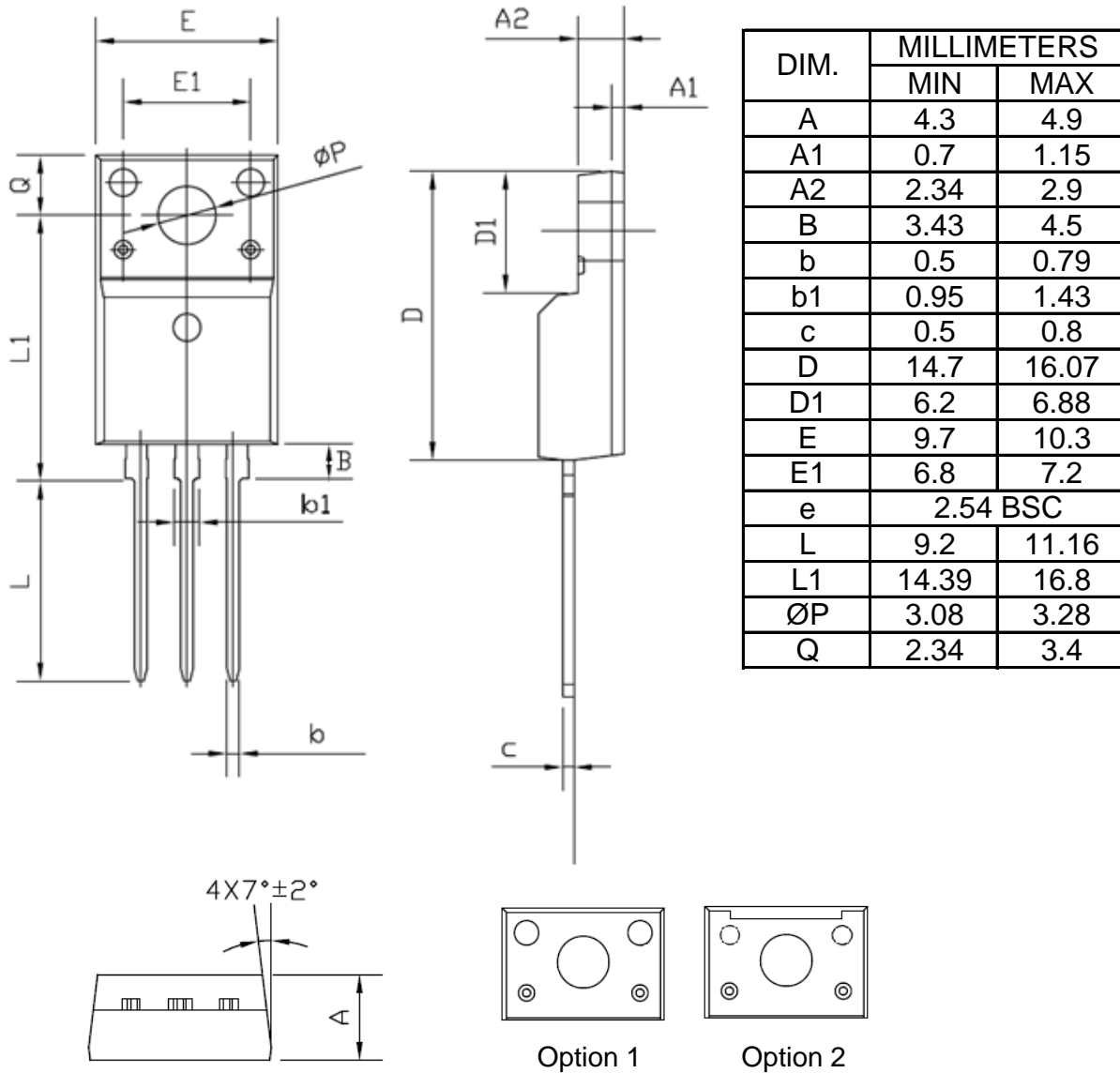


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



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