RoHS



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Vishay General Semiconductor

High Voltage Surface Mount Schottky Rectifier

High Barrier Technology for Improved High Temperature Performance



DO-214AA (SMB)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	2.0 A				
V_{RRM}	90 V, 100 V				
I _{FSM}	75 A				
V _F	0.65 V				
I _R	10 μΑ				
T _J max.	175 °C				
Package	DO-214AA				
Diode variations	Single				

FEATURES

- · Low profile package
- · Guardring for overvoltage protection
- · Ideal for automated placement
- Low power losses, high efficiency
- Low forward voltage drop
- · Low leakage current
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: DO-214AA (SMB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3 - RoHS-compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes the cathode end

PARAMETER	SYMBOL	SS2H9	SS2H10	UNIT
Device marking code		MS9	MS10	
Maximum repetitive peak reverse voltage	V _{RRM}	90	100	V
orking peak reverse voltage V _{RWN}		90	100	V
Maximum DC blocking voltage	V _{DC}	90	100	V
Maximum average forward rectified current at: T _L = 130 °C	I _{F(AV)}	2.0		Α
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	75		А
Peak repetitive reverse surge current at t _p = 2.0 μs, 1 kHz	I _{RRM}	1.0		А
Voltage rate of change (rated V _R)	dV/dt	10 000		V/µs
Operating junction and storage temperature range	T _J , T _{STG}	-65 to +175		°C



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	SS2H9	SS2H10	UNIT
Maximum instantaneous forward voltage (1)	I _F = 2.0 A	T _J = 25 °C T _J = 125 °C	V _F	0.79 0.65		V
		T _J = 125 °C				
Maximum reverse current at rated V _B ⁽²⁾		T _J = 25 °C	- I _R	10		μΑ
waximum reverse current at rated $v_R \leftarrow$		T _J = 125 °C		4	4	mA

Notes

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SS2H9	SS2H10	UNIT	
Maximum thermal resistance junction to lead $T_L = 25 ^{\circ}C^{(1)}$	$R_{\theta JA}$	80		°C/W	
Maximum thermal resistance junction to lead T _L = 25° C ***	$R_{ heta JL}$	25		C/VV	

Note

(1) Units mounted on PCB with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SS2H9-E3/52T	0.096	52T	750	7" diameter plastic tape and reel	
SS2H9-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel	
SS2H9HE3/52T (1)	0.096	52T	750	7" diameter plastic tape and reel	
SS2H9HE3/5BT (1)	0.096	5BT	3200	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

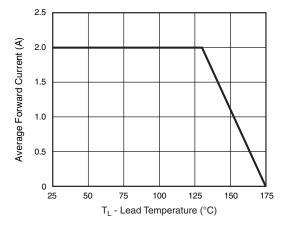


Fig. 1 - Forward Current Derating Curve

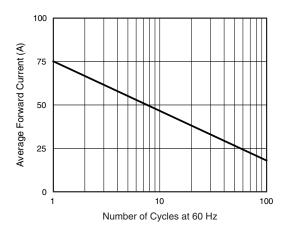


Fig. 2 - Max Non-Repetitive Peak Forward Surge Current



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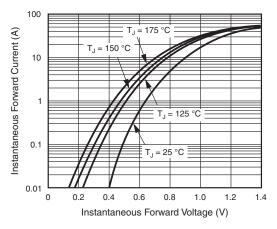


Fig. 3 - Typical Instanteous Forward Characteristics

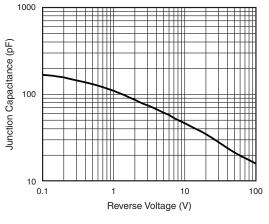


Fig. 5 - Typical Junction Capacitance

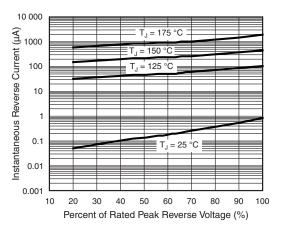


Fig. 4 - Typical Reverse Characteristics

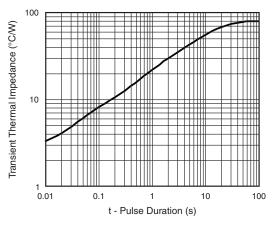
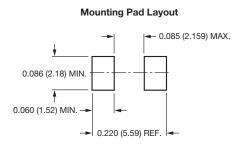


Fig. 6 - Typical Transient Thermal Impedance Per Leg

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

0.086 (2.20) 0.077 (1.95) 0.180 (4.57) 0.160 (4.06) 0.096 (2.44) 0.084 (2.13) 0.096 (0.152) 0.008 (0.2) 0.008 (0.2) 0.008 (0.2) 0.008 (0.2) 0.008 (0.2) 0.008 (0.2)





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Mouser Electronics

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