

**ECH8602** 

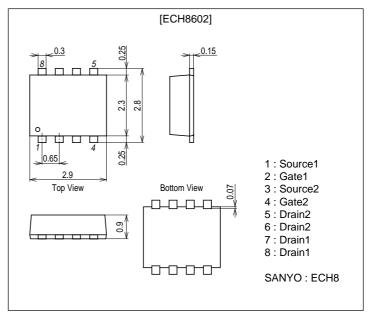
# **LIB Applications**

### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- 2.5V drive.
- Composite type, facilitating high-density mounting.
- · Drain common specitications.

## **Package Dimensions**

unit : mm 2206A



# **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±12	V
Drain Current (DC)	ID		5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)1unit	1.4	W
Total Dissipation	PT	Mounted on a ceramic board (900mm <sup>2</sup> X0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.5		1.3	V

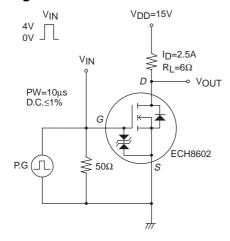
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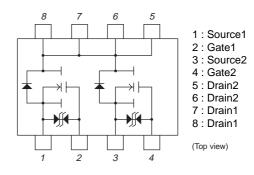
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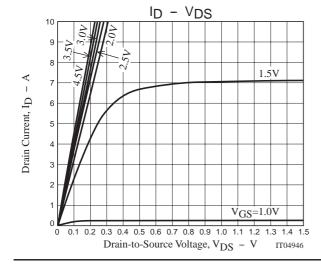
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =2.5A	7	10		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =4A, V <sub>GS</sub> =4.5V		22	30	mΩ
	RDS(on)2	ID=4A, VGS=4.0V		23	31	$m\Omega$
	R <sub>DS</sub> (on)3	I <sub>D</sub> =2A, V <sub>GS</sub> =2.5V		30	44	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		850		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		240		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		110		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		20		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		70		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		90		ns
Fall Time	tf	See specified Test Circuit.		80		ns
Total Gate Charge	Qg	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		21		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		1.4		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		2.1		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =5A, V <sub>G</sub> S=0		0.82	1.2	V

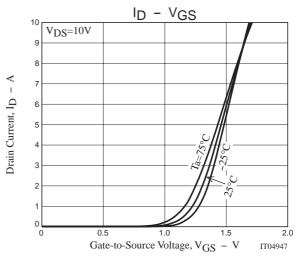
# **Switching Time Test Circuit**

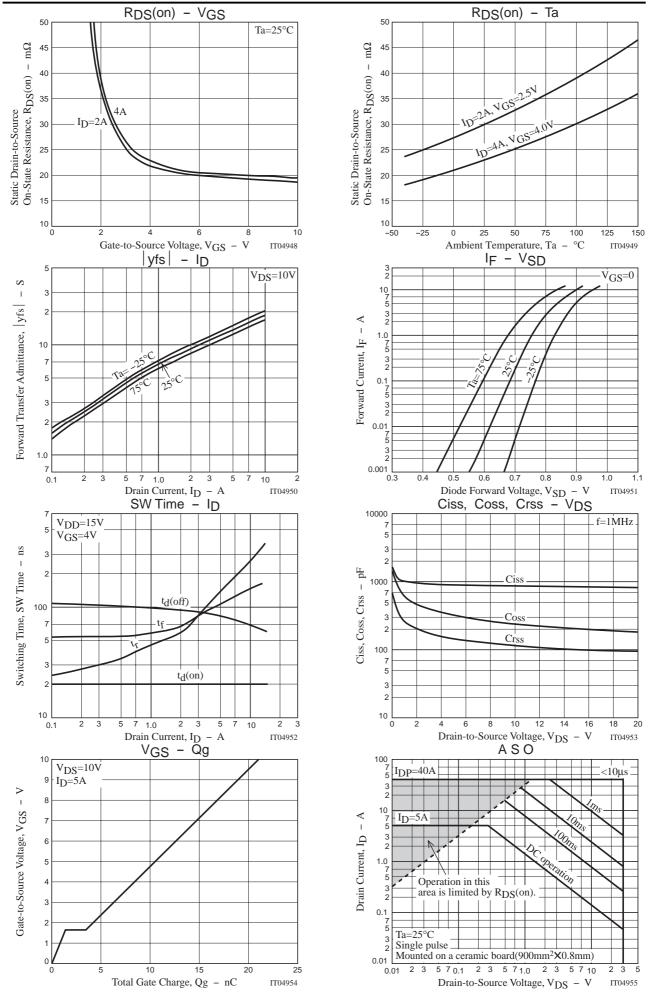


# **Electrical Connection**

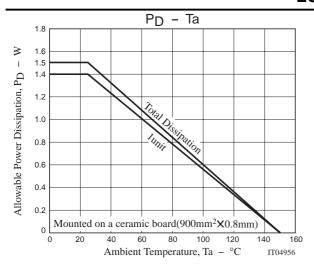








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