RoHS Compliant
HALOGEN-FREE

MagnaChip

MDHT4N25

N-Channel MOSFET 250V, 0.83A, 1.75Ω

General Description

The MDHT4N25 uses advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

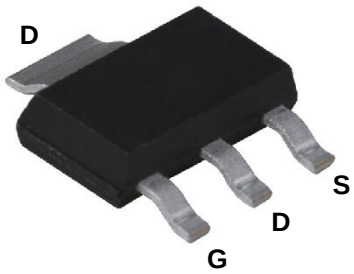
MDHT4N25 is suitable device for SMPS, HID and general purpose applications.

Features

- V_{DS} = 250V
- I_D = 0.83A
- $R_{DS(ON)}$ ≤ 1.75Ω @V_{GS} = 10V

Applications

- Power Supply
- PFC
- LED TV



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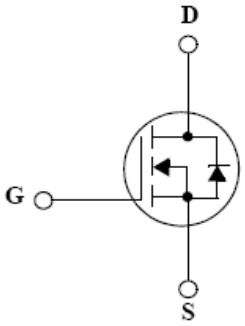

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	250	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current	T_C=25°C	I_D	0.83	A
	T_C=100°C		0.52	A
Pulsed Drain Current[1]		I_{DM}	3.3	A
Power Dissipation	T_C=25°C	P_D	2.5	W
	Derate above 25 °C		0.02	W/ °C
Peak Diode Recovery dv/dt[3]		dv/dt	5.5	V/ns
Repetitive Pulse Avalanche Energy[4]		E_{AR}	0.25	mJ
Avalanche current[1]		I_{AR}	0.83	A
Single Pulse Avalanche Energy[4]		E_{AS}	52	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient[1]	$R_{\theta JA}$	50	°C/W

*When mounted on the minimum pad size recommended (PCB Mount)

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDHT4N25URH	-55~150°C	SOT-223	Reel and Tape	Halogen Free

Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D = 250μA, V_{GS} = 0V	250	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	3.0	-	5.0	
Drain Cut-Off Current	I_{DSS}	V_{DS} = 250V, V_{GS} = 0V	-	-	1	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±30V, V_{DS} = 0V	-	-	100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 0.415A		1.38	1.75	Ω
Forward Transconductance	g_{fs}	V_{DS} = 30V, I_D = 0.415A	-	0.91	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	V_{DS} = 200V, I_D = 3.6A, V_{GS} = 10V	-	4.2	-	nC
Gate-Source Charge	Q_{gs}		-	1.35	-	
Gate-Drain Charge	Q_{gd}		-	1.95	-	
Input Capacitance	C_{iss}	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	-	146	-	pF
Reverse Transfer Capacitance	C_{rss}		-	3	-	
Output Capacitance	C_{oss}		-	32	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = 5V, V_{DS} = 125V, I_D = 3.6A, R_G = 25Ω	-	8	-	ns
Rise Time	t_r		-	21	-	
Turn-Off Delay Time	$t_{d(off)}$		-	5	-	
Fall Time	t_f		-	16	-	
Drain-Source Body Diode Characteristics						
Maximum Continuous Drain to Source Diode Forward Current	I_S		-	0.83	-	A
Source-Drain Diode Forward Voltage	V_{SD}	I_S = 0.83A, V_{GS} = 0V	-	-	1.5	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 3.6A, dI/dt = 100A/μs(3)	-	110	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	0.34	-	μC

Note :

1. Pulse width is based on $R_{\theta JC}$ & $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
3. I_{SD} ≤3.6A, di/dt≤300A/us, V_{DD}≤BVdss, R_g =25Ω, Starting T_J=25°C
4. L=120mH, I_{AS}=0.83A, V_{DD}=50V, R_g =25Ω, Starting T_J=25°C

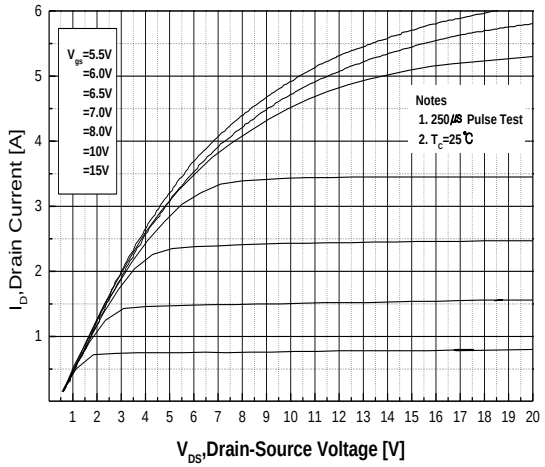

Fig.1 On-Region Characteristics

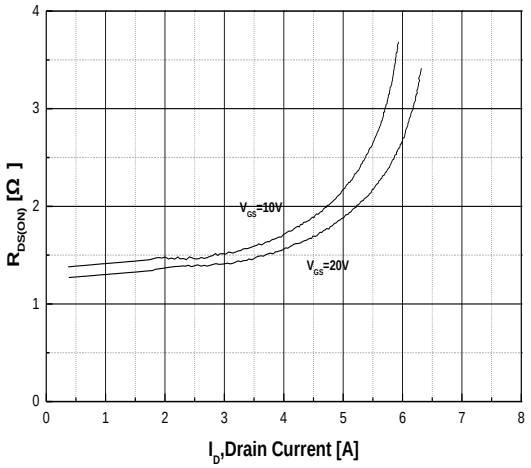

Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

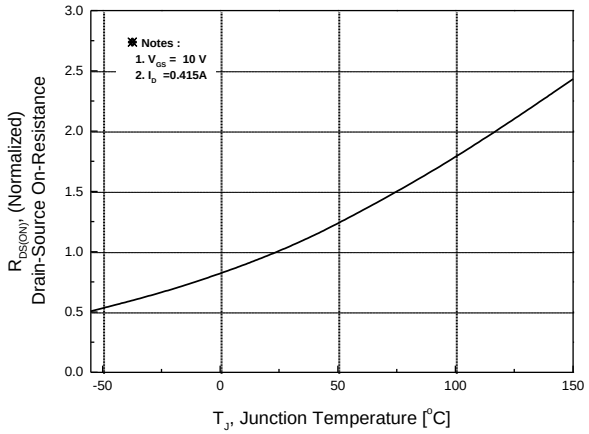

Fig.3 On-Resistance Variation with Temperature

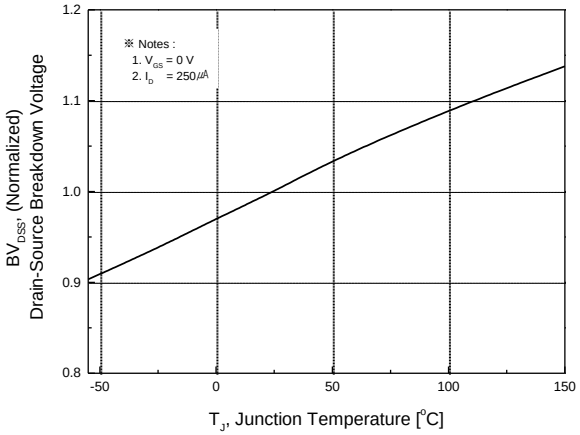

Fig.4 Breakdown Voltage Variation vs. Temperature

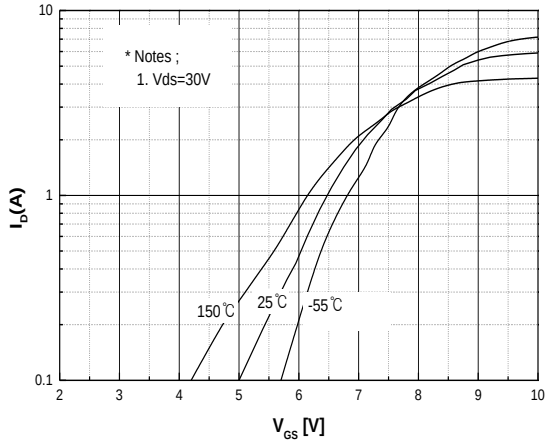

Fig.5 Transfer Characteristics

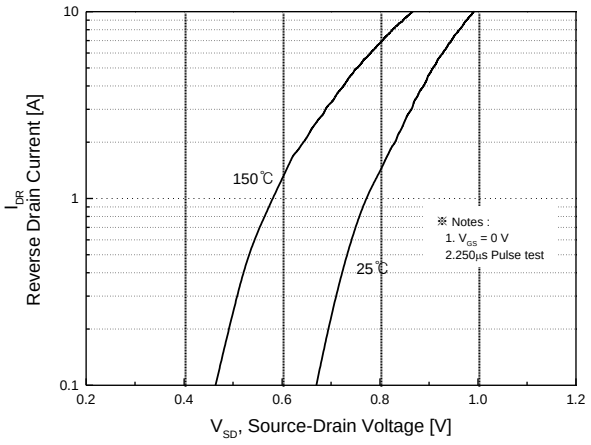

Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

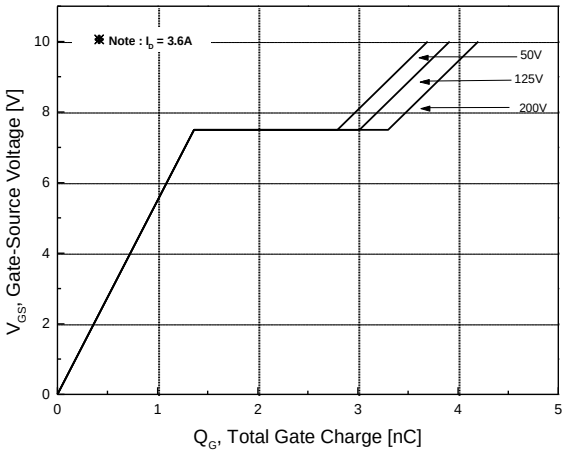

Fig.7 Gate Charge Characteristics

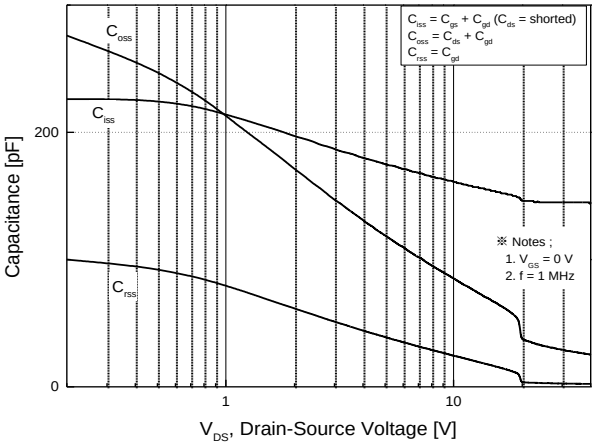

Fig.8 Capacitance Characteristics

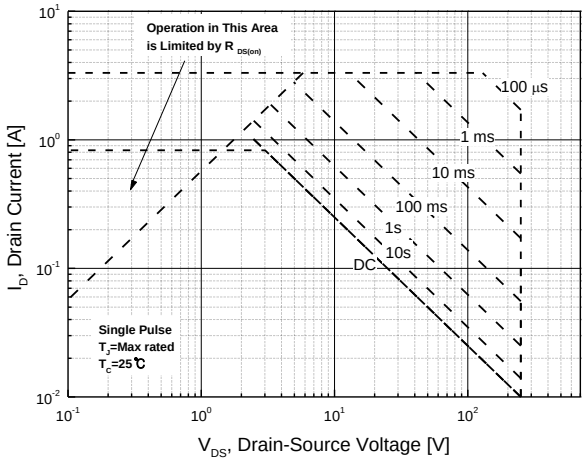

Fig.9 Maximum Safe Operating Area

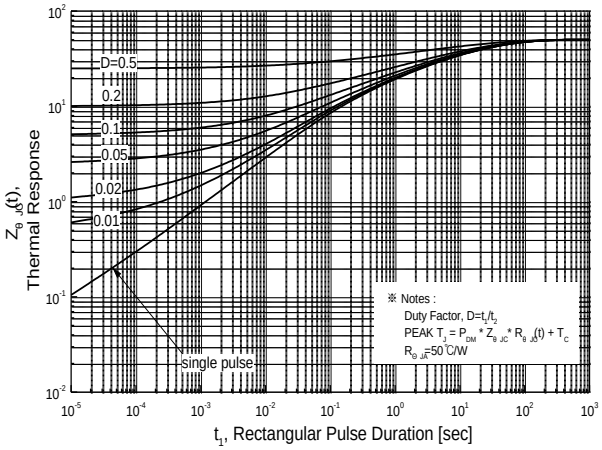

Fig.10 Transient Thermal Response Curve

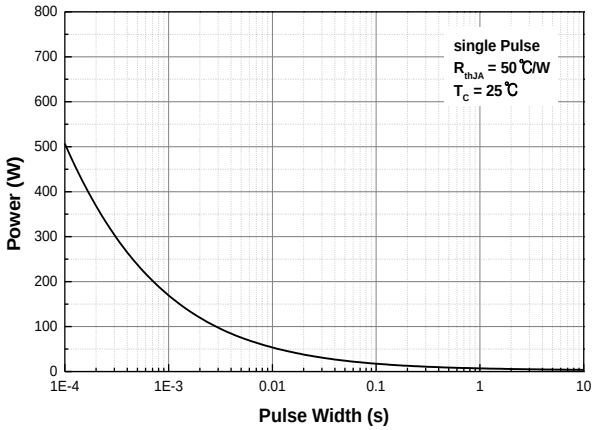

Fig.11 Single Pulse Maximum Power Dissipation

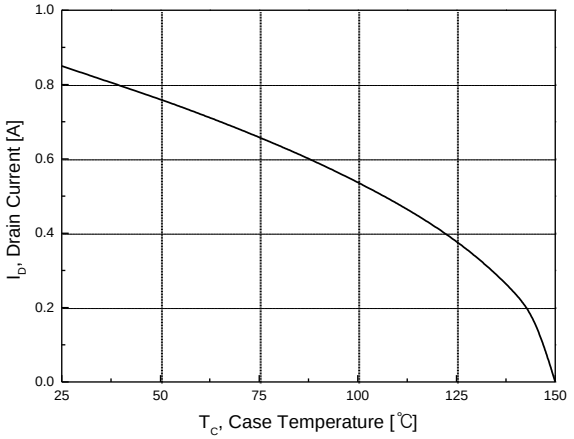

Fig.12 Maximum Drain Current vs. Case Temperature

Physical Dimension

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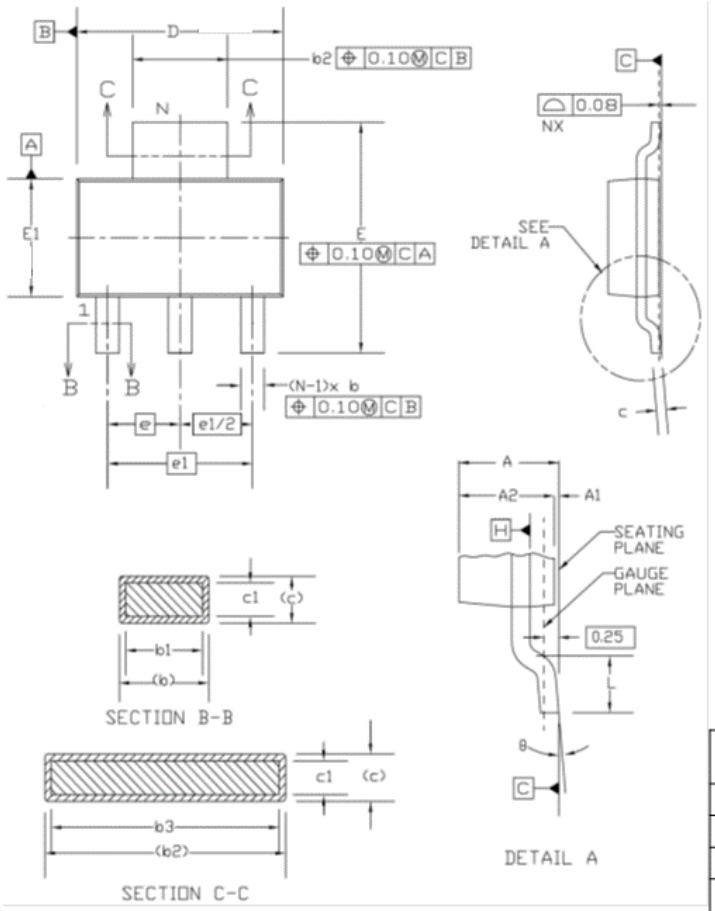
Dimensions are in millimeters, unless otherwise specified



Symbol	Min	Nom	Max
A	-	-	1.80
A1	0.00	-	0.10
A2	1.50	-	1.70
b	0.60	-	0.84
b1	0.60	-	0.79
b2	2.90	-	3.10
b3	2.84	-	3.05
c	0.23	-	0.35
c1	0.23	-	0.33
D	6.20	-	6.70
E	6.70	-	7.30
E1	3.30	-	3.70
e	2.30 BASIC		
e1	4.60 BASIC		
L	0.75	-	-
θ	0°	-	10°

DIV'D	NAME	DATE	TITLE	SOT-223-3LD, PACKAGE DRAWING
DES.BY	Jungsul Kim	'11.07.27	DWG.NO	MBKD-D0780
CH.BY				
APR.BY			REV.NO	1 SHEET 1/2
SCALE	NA	UNIT	mm	MagnaChip

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