

CPW4-1200S020B-Silicon Carbide Schottky Diode Chip

| Z - Rec^{TM} Rectifier | | V _{RRM} = 1200 V |
|----------------------------|--------------|-----------------------------------|
| | | I _{F(AVG)} = 20 A |
| Features | Chip Outline | Q _c = 130 nC |
| | | |

- 1200-Volt Schottky Rectifier •
- Zero Reverse Recovery ٠
- Zero Forward Recovery •
- •
- High-Frequency Operation Temperature-Independent Switching Behavior •
- Extremely Fast Swtitching •
- Positive Temperature Coefficient on V_{F} •



| Part Number | Anode | Cathode | Package | Marking |
|----------------|-------|---------|--------------|-----------------|
| CPW4-1200S020B | AI | Ni/Ag | Sawn on Foil | Wafer # on Foil |

Maximum Ratings

| Symbol | Parameter | Value | Unit | Test Conditions | Note |
|-----------------------------------|--|----------------|------|---|------|
| V _{RRM} | Repetitive Peak Reverse Voltage | 1200 | V | | |
| V _{RSM} | Surge Peak Reverse Voltage | 1300 | V | | |
| V _{DC} | DC Blocking Voltage | 1200 | V | | |
| I _{F(AVG)} | Average Forward Current | 20 | А | T _j =175°C | |
| I _{FRM} | Repetitive Peak Forward Surge Current | 91 | А | $T_c = 25^{\circ}C$, $t_p = 10$ ms, Half Sine Wave | 1 |
| I _{FSM} | Non-Repetitive Peak Forward Surge Current | 130 | А | $T_c=25^{\circ}C$, $t_p=10$ ms, Half Sine Wave | 1 |
| T _J , T _{stg} | Operating Junction and Storage Temperature | -55 to +175 | °C | | |



Electrical Characteristics

| Symbol | Parameter | Тур. | Max. | Unit | Test Conditions | Note |
|----------------|-------------------------|------------------|------------|------|---|------|
| V _F | Forward Voltage | 1.5 2.2 | 1.8 3 | V | $I_F = 20 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 20 \text{ A } T_J = 175^{\circ}\text{C}$ | |
| I _R | Reverse Current | 35 65 | 200 400 | μA | $V_{R} = 1200 V T_{J} = 25^{\circ}C$ $V_{R} = 1200 V T_{J} = 175^{\circ}C$ | |
| Q _c | Total Capacitive Charge | 130 | | nC | $V_{R} = 1200 \text{ V}, I_{F} = 20 \text{ A}$ $di/dt = 200 \text{ A}/\mu\text{s}$ $T_{J} = 25^{\circ}\text{C}$ | |
| с | Total Capacitance | 1500 93 67 | | pF | $V_{R} = 0 V, T_{J} = 25^{\circ}C, f = 1 MHz$ $V_{R} = 400 V, T_{J} = 25^{\circ}C, f = 1 MHz$ $V_{R} = 800 V, T_{J} = 25^{\circ}C, f = 1 MHz$ | |

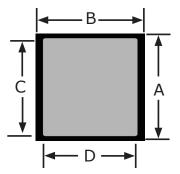
Note: 1. Assumes $\theta {\sf JC}$ Thermal Resistance of 0.62°C/W or less

Mechanical Parameters

| Parameter | Тур. | Unit |
|------------------------------|-------------|------|
| Die Size | 3.08 x 3.08 | mm |
| Anode Pad Size | 2.79 x 2.79 | mm |
| Anode Pad Opening | 2.51 x 2.51 | mm |
| Thickness | 377 ± 10% | μm |
| Wafer Size | 100 | mm |
| Anode Metalization (Al) | 4 | μm |
| Cathode Metalization (Ni/Ag) | 1.4 | μm |
| Frontside Passivation | Polyimide | |



Chip Dimensions



| symbol | dimension | | | |
|--------|-----------|-------|--|--|
| | mm | inch | | |
| А | 3.08 | 0.121 | | |
| В | 3.08 | 0.121 | | |
| С | 2.51 | 0.099 | | |
| D | 2.51 | 0.099 | | |

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|----------------|-------|---------|--------------|-----------------|
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The die-on-tape method of delivering these SiC die may be considered a means of temporary storage only. Due to an increase in adhesion over time, die stored for an extended period may affix too strongly to the tape. These die should be stored in a temperature-controlled nitrogen dry box soon after receipt. Cree will further recommend that all die be removed from tape to a waffle pack, to a similar storage medium, or used in production within 2 – 3 weeks of delivery to assure 100% release of all die without issues.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.

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