# StaticGuard AVX Multilayer Ceramic Transient Voltage Suppressors ESD Protection for CMOS, Bi Polar and SiGe Based Systems

#### **GENERAL INFORMATION**

- Typical ESD failure voltage for CMOS and/or Bi Polar is  $\geq$  200V.
- 15kV ESD pulse (air discharge) per IEC 1000-4-2, Level 4, generates < 20 millijoules of energy.
- Low capacitance (<200pF) is required for high-speed data transmission.
- Low leakage current (I<sub>L</sub>) is necessary for battery operated equipment.

#### **StaticGuard**

| AVX<br>Part Number | Working<br>Voltage<br>(DC) | Working<br>Voltage<br>(AC) | Clamping<br>Voltage | Test<br>Current<br>For V <sub>c</sub> | Maximum<br>Leakage<br>Current | Transient<br>Energy<br>Rating | Peak<br>Current<br>Rating | Typical<br>Cap | Case<br>Size | Elements |
|--------------------|----------------------------|----------------------------|---------------------|---------------------------------------|-------------------------------|-------------------------------|---------------------------|----------------|--------------|----------|
| VC04LC18V500       | ≤18.0                      | ≤14.0                      | 50                  | 1                                     | 10                            | 0.02                          | 15                        | 40             | 0402         | 1        |
| VC06LC18X500       | ≤18.0                      | ≤14.0                      | 50                  | 1                                     | 10                            | 0.05                          | 30                        | 50             | 0603         | 1        |
| VC08LC18A500       | ≤18.0                      | ≤14.0                      | 50                  | 1                                     | 10                            | 0.10                          | 30                        | 80             | 0805         | 1        |
| VC12LC18A500       | ≤18.0                      | ≤14.0                      | 50                  | 1                                     | 10                            | 0.10                          | 30                        | 200            | 1206         | 1        |
| VA10LC18A500       | ≤18.0                      | ≤14.0                      | 50                  | 1                                     | 10                            | 0.10                          | 30                        | 200            | Axial        | 1        |

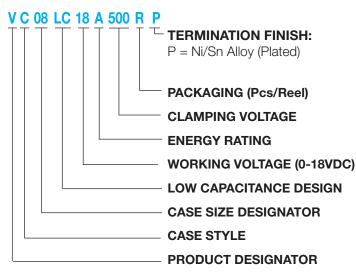
Termination/Lead Finish Code Packaging Code

- V<sub>w</sub>(DC) DC Working Voltage (V)
- V<sub>w</sub>(AC) AC Working Voltage (V)
- V<sub>c</sub> Clamping Voltage (V @ I<sub>vc</sub>)
- $I_{vc}$  Test Current for V<sub>c</sub> (A, 8x20µS)
- I<sub>L</sub> Maximum Leakage Current at the Working Voltage (μΑ)
- $E_{\tau}$  Transient Energy Rating (J, 10x1000µS)
- I<sub>P</sub> Peak Current Rating (A, 8x20µS)
- Cap Typical Capacitance (pF) @ frequency specified and 0.5 V

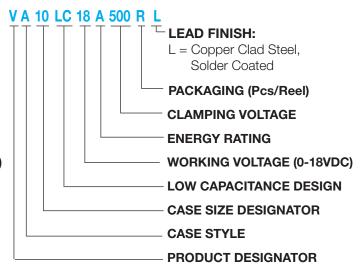
### PART NUMBER IDENTIFICATION

#### **Chips**

40



#### **Axials**





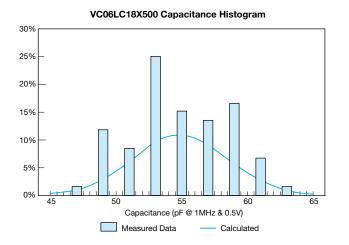
#### Not RoHS Compliant



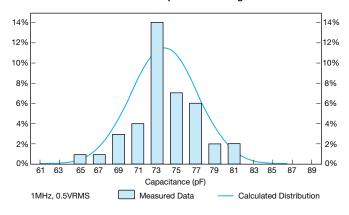
For RoHS compliant products, please select correct termination style.

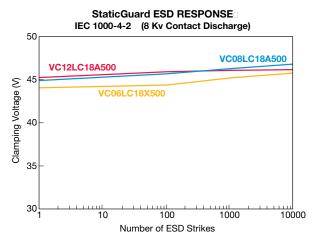
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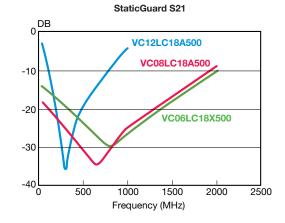
#### **TYPICAL PERFORMANCE DATA**

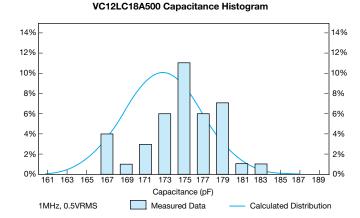


VC08LC18A500 Capacitance Histogram

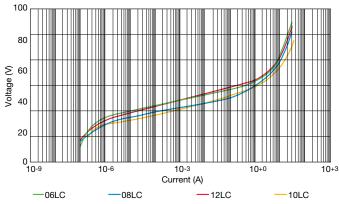








VI Curves - StaticGuard Products



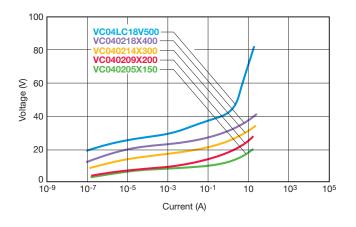
# **StaticGuard**



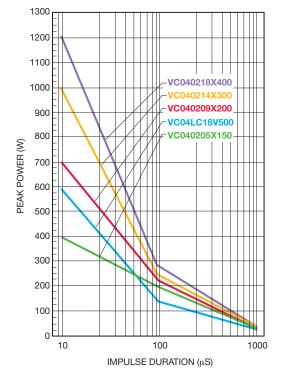
## **AVX Multilayer Ceramic Transient Voltage Suppressors**

## **TYPICAL PERFORMANCE CURVES (0402 CHIP SIZE) VOLTAGE/CURRENT CHARACTERISTICS**

Multilayer construction and improved grain structure result in excellent transient clamping characteristics up to 20 amps peak current, while maintaining very low leakage currents under DC operating conditions. The VI curves below show the voltage/current characteristics for the 5.6V, 9V, 14V, 18V and low capacitance StaticGuard parts with currents ranging from parts of a micro amp to tens of amps.



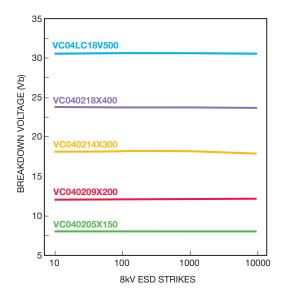
#### PEAK POWER VS PULSE DURATION



#### **PULSE DEGRADATION**

Traditionally varistors have suffered degradation of electrical performance with repeated high current pulses resulting in decreased breakdown voltage and increased leakage current. It has been suggested that irregular intergranular boundaries and bulk material result in restricted current paths and other non-Schottky barrier paralleled conduction paths in the ceramic. Repeated pulsing of TransGuard® transient voltage suppressors with 150Amp peak 8 x 20µS waveforms shows negligible degradation in breakdown voltage and minimal increases in leakage current. This does not mean that TransGuard® suppressors do not suffer degradation, but it occurs at much higher current.

#### ESD TEST OF 0402 PARTS



#### **INSERTION LOSS CHARACTERISTICS**

