

# AN8146FBP

## High Speed 10-Bit 3-Ch. D/A Converter

### Overview

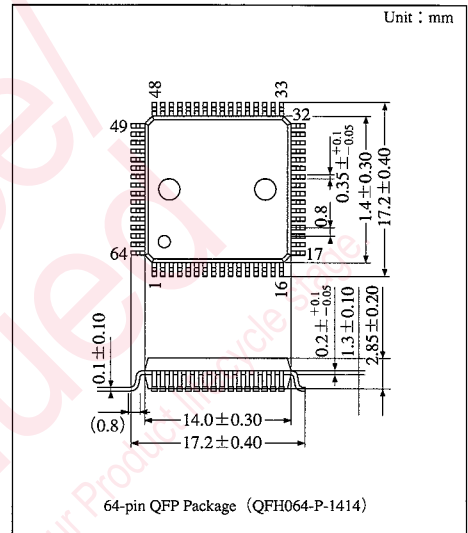
The AN8146FBP is a D/A converter for video band with input/output of three channels. It is suitable for the various image equipments such as hi-vision device or monitor.

### Features

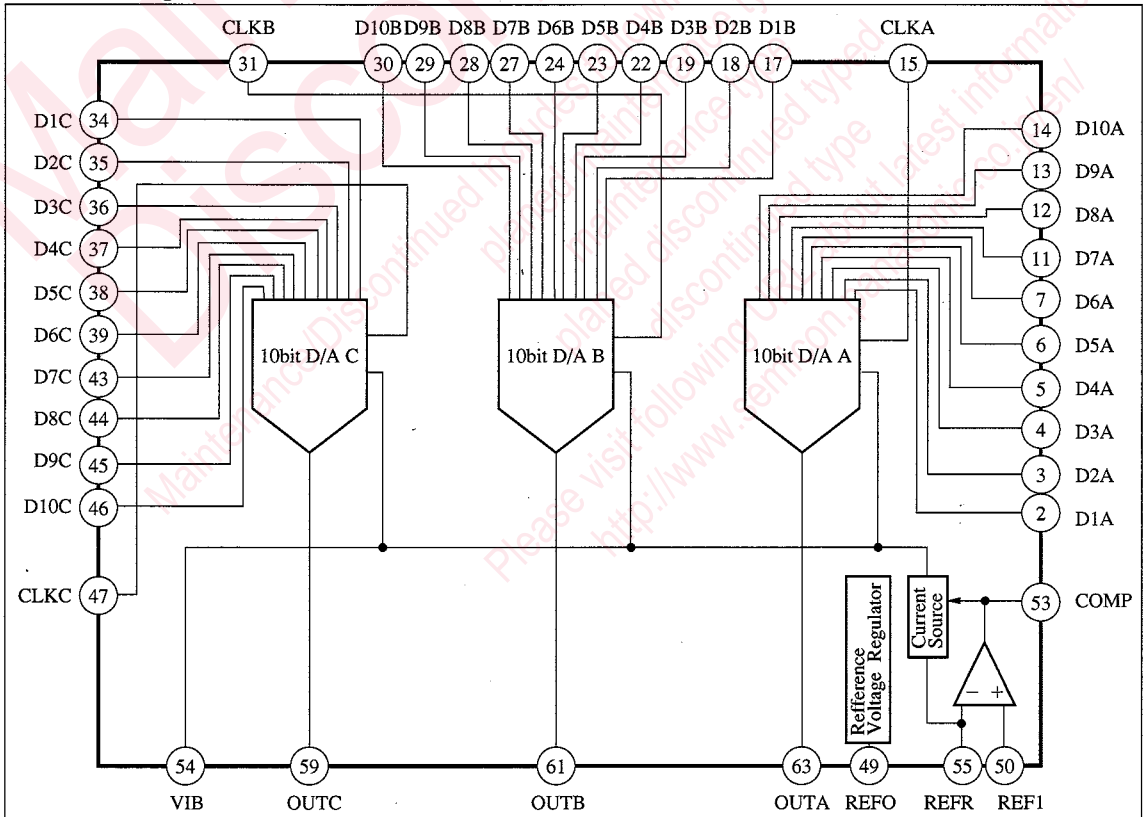
- 10-bit resolution
- High speed : maximum conversion rate of 50 MSPS
- Low consumption power : 450 mW
- Input level : TTL level
- Single power supply : 5V
- Reference power supply built-in

### Application Field

- Image equipment such as hi-vision device
- OA equipment such as image scanner
- Measuring equipment such as digital oscilloscope



### Block Diagram



A/D and D/A Converters

## Pin Descriptions

Pin No.	Symbol	Pin name	Pin No.	Symbol	Pin name
1	NC	NC	33	NC	NC
2	D1A	Digital input 1-bit (MSB) A	34	D1C	Digital input 1-bit (MSB) C
3	D2A	Digital input 2-bit A	35	D2C	Digital input 2-bit C
4	D3A	Digital input 3-bit A	36	D3C	Digital input 3-bit C
5	D4A	Digital input 4-bit A	37	D4C	Digital input 4-bit C
6	D5A	Digital input 5-bit A	38	D5C	Digital input 5-bit C
7	D6A	Digital input 6-bit A	39	D6C	Digital input 6-bit C
8	NC	NC	40	NC	NC
9	NC	NC	41	NC	NC
10	NC	NC	42	NC	NC
11	D7A	Digital input 7-bit A	43	D7C	Digital input 7-bit C
12	D8A	Digital input 8-bit A	44	D8C	Digital input 8-bit C
13	D9A	Digital input 9-bit A	45	D9C	Digital input 9-bit C
14	D10A	Digital input 10-bit (LSB) A	46	D10C	Digital input 10-bit C
15	CLKA	Clock input A	47	CLKC	Clock input C
16	DGND	Digital ground	48	AGND	Analogue ground
17	D1B	Digital input 1-bit (MSB) B	49	REFO	Reference voltage output
18	D2B	Digital input 2-bit B	50	REFI	Reference voltage input
19	D3B	Digital input 3-bit B	51	NC	NC
20	NC	NC	52	NC	NC
21	NC	NC	53	COMP	Compensation capacitance
22	D4B	Digital input 4-bit B	54	VIB	Stabilized pin
23	D5B	Digital input 5-bit B	55	REFR	Reference resistance
24	D6B	Digital input 6-bit B	56	NC	NC
25	NC	NC	57	NC	NC
26	NC	NC	58	AGND	Analogue ground
27	D7B	Digital input 7-bit B	59	OUTC	Analogue output C
28	D8B	Digital input 8-bit B	60	AVCC	Analogue power supply
29	D9B	Digital input 9-bit B	61	OUTB	Analogue output B
30	D10B	Digital input 10-bit B	62	AVCC	Analogue power supply
31	CLKB	Clock input B	63	OUTA	Analogue output A
32	DVCC	Digital power supply	64	AGND	Analogue ground

### Major Characteristics ( $V_{CC}=5V$ , $T_a=25^\circ C$ )

Parameter	Symbol	Rating	Unit
Resolution	RES	10	bit
Linearity error	$E_L$	$\pm 1.0$	LSB
Differential linearity error	$E_D$	$\pm 1.0$	LSB
Maximum conversion rate	$F_{C_{MAX}}$	50	MSPS

### Absolute Maximum Ratings ( $T_a=25^\circ C$ )

Parameter	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	-0.3 to +7.0	V
Digital input voltage	$V_{IN}$	-0.3 to $DV_{CC}$	V
Analogue output current	$I_O$	30	mA
Reference input voltage	$V_{REF1}$	-0.3 to $AV_{CC}$	V
Power dissipation	$P_D$	585	mW
Operating ambient temperature	$T_{opr}$	-20 to +70	$^\circ C$
Storage temperature	$T_{stg}$	-55 to +125	$^\circ C$

### Recommended Operating Conditions ( $T_a=25^\circ C$ )

Parameter	Symbol	Condition	min	typ	max	Unit
Supply voltage	$V_{CC}$		4.75	5.0	5.25	V
Reference voltage	$V_{REF1}$		1.2	1.25	—	V
Output load resistance	$R_O$		63	75	86	$\Omega$
Digital input voltage	$V_{IH}$		2.4	—	—	V
	$V_{IL}$		0	—	0.8	V
Clock input pulse width	$t_{WH}$	*1	8 *2	—	—	ns
	$t_{WL}$	*1	8 *2	—	—	ns
Setting-up time	$t_S$	*1	5	—	—	ns
Holding time	$t_H$	*1	1	—	—	ns

\* 1 : Refer to the timing chart.

\* 2 :  $t_{WH} + t_{WL} \geq 20ns$

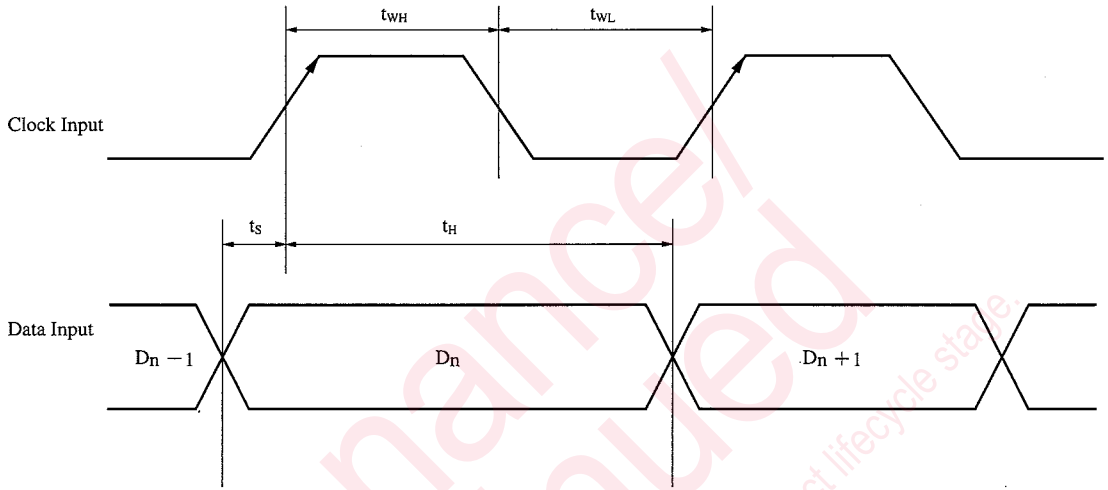
A/D  
and D/A  
Converters

### Electrical Characteristics ( $V_{CC}=5V$ , $T_a=25^\circ C$ )

Parameter	Symbol	Condition	min	typ	max	Unit
Supply current	$I_{CC}$		—	90	150	mA
Digital input leak current	$I_{DI}$	$V_{IN}=0V, 5V$	—	—	$\pm 30$	$\mu A$
REF1 input leak current	$I_{REF1}$	$V_{IN}=0V, 5V$	—	—	$\pm 30$	$\mu A$
Reference output voltage	$V_{REF0}$	$I_{REF0}=3mA$	2.16	2.7	3.24	V
Linearty error	$E_L$		—	$\pm 0.5$	$\pm 1.0$	LSB
Differential linearty error	$E_D$		—	$\pm 0.5$	$\pm 1.0$	LSB
Maximum conversion rate	$F_{C_{MAX}}$	$R_{REF}=470\Omega$	50	—	—	MSPS
Full-scale output voltage	$V_{FS}$	$R_O=75\Omega$	4.9	5.0	5.1	V
Zero-scale output voltage	$V_{ZS}$	$V_{REF0}-V_{REF1}=1.4V$	3.8	4.0	4.2	V

■ Timing Chart

(Operation Timing)



Maintenance/Discontinued includes following four Product lifecycle stage:  
planned maintenance type  
maintenance type  
planned discontinued type  
discontinued type  
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