

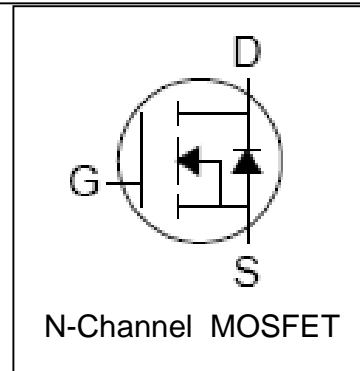
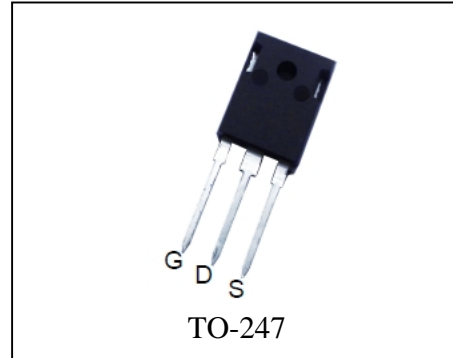
Features

- 75V/260A,
 $R_{DS(ON)}=2.8m$ (Typ.)@ $V_{GS}=10V$
- Super High Dense Cell Design
- Ultra Low On-Resistance
- Reliable and Rugged
- 100% avalanche tested
- Lead Free and Green Devices Available
 (RoHS Compliant)

Applications

- High Speed Power Switching
- UPS

Pin Description



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings ($T_C=25^\circ C$ Unless Otherwise Noted)			
V_{DSS}	Drain-Source Voltage	75	V
V_{GSS}	Gate-Source Voltage	± 25	
T_J	Maximum Junction Temperature	175	$^\circ C$
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
I_S	Diode Continuous Forward Current	$T_C=25^\circ C$ 260 ^①	A
Mounted on Large Heat Sink			
I_{DP}	300 μs Pulse Drain Current Tested	$T_C=25^\circ C$ 1040 ^②	A
I_D	Continuous Drain Current($V_{GS}=10V$)	$T_C=25^\circ C$ 260 ^①	A
		$T_C=100^\circ C$ 189 ^①	
P_D	Maximum Power Dissipation	$T_C=25^\circ C$ 428	W
		$T_C=100^\circ C$ 214	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.35	$^\circ C/W$
Drain-Source Avalanche Ratings			
E_{AS} ^③	Avalanche Energy, Single Pulsed	900	mJ

Electrical Characteristics ($T_C=25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Condition	RU75260Q			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	75			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=75V, V_{GS}=0V$ $T_J=85^\circ\text{C}$			1	μA
					30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	2	3	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$			± 100	nA
$R_{DS(ON)}^{(4)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=75A$		2.8	3.5	m Ω
Diode Characteristics						
$V_{SD}^{(4)}$	Diode Forward Voltage	$I_{SD}=75A, V_{GS}=0V$			1.2	V
t_{rr}	Reverse Recovery Time	$I_{SD}=75A, di_{SD}/dt=100A/\mu s$		45		ns
Q_{rr}	Reverse Recovery Charge			82		nC
Dynamic Characteristics ⁽⁵⁾						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$		1.2		Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=37.5V,$ Frequency=1.0MHz		9800		pF
C_{oss}	Output Capacitance			860		
C_{rss}	Reverse Transfer Capacitance			540		
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=37.5V, R_L=0.5\Omega,$ $I_{DS}=75A, V_{GEN}=10V,$ $R_G=6\Omega$		26		ns
t_r	Turn-on Rise Time			98		
$t_{d(OFF)}$	Turn-off Delay Time			87		
t_f	Turn-off Fall Time			72		
Gate Charge Characteristics ⁽⁵⁾						
Q_g	Total Gate Charge	$V_{DS}=60V, V_{GS}=10V,$ $I_{DS}=75A$		170		nC
Q_{gs}	Gate-Source Charge			52		
Q_{gd}	Gate-Drain Charge			64		

Notes: ① Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 90A.

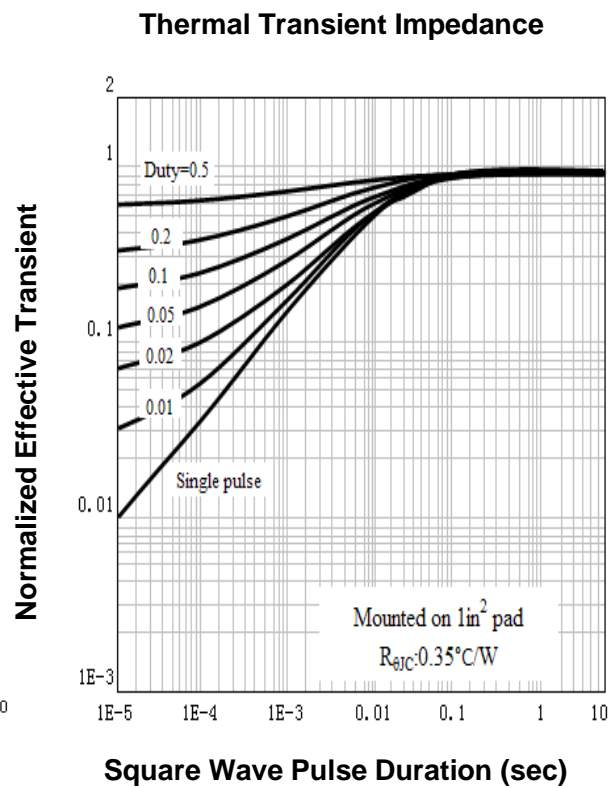
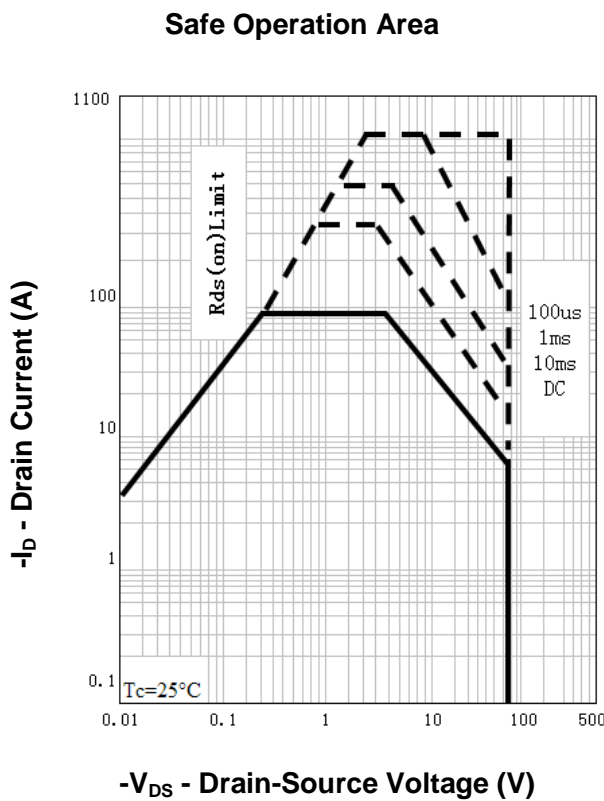
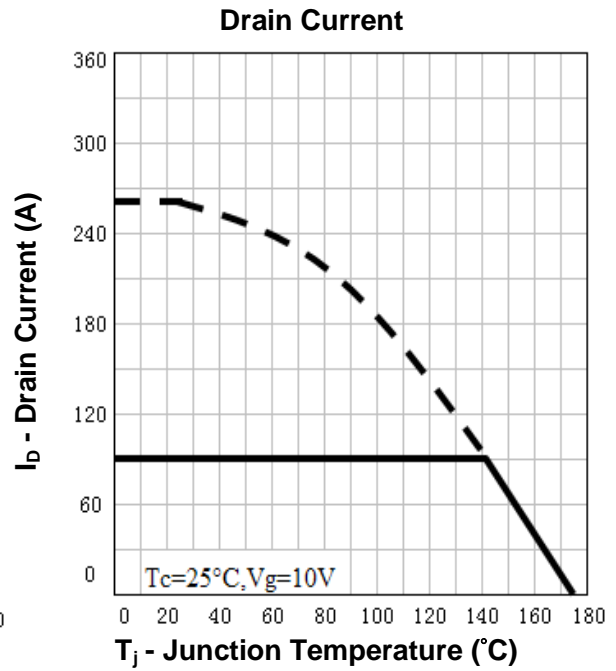
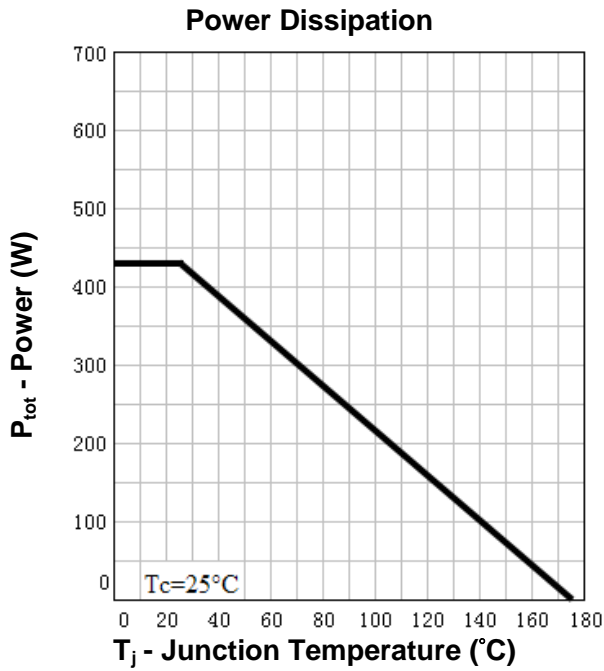
② Pulse width limited by safe operating area.

③ Limited by $T_{Jmax}, I_{AS}=60A, V_{DD}=48V, R_G=50\Omega$, Starting $T_J=25^\circ\text{C}$.

④ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

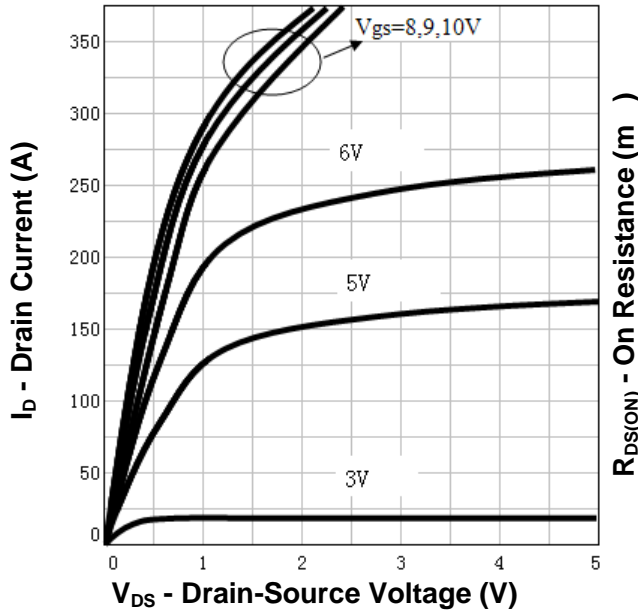
⑤ Guaranteed by design, not subject to production testing.

Typical Characteristics

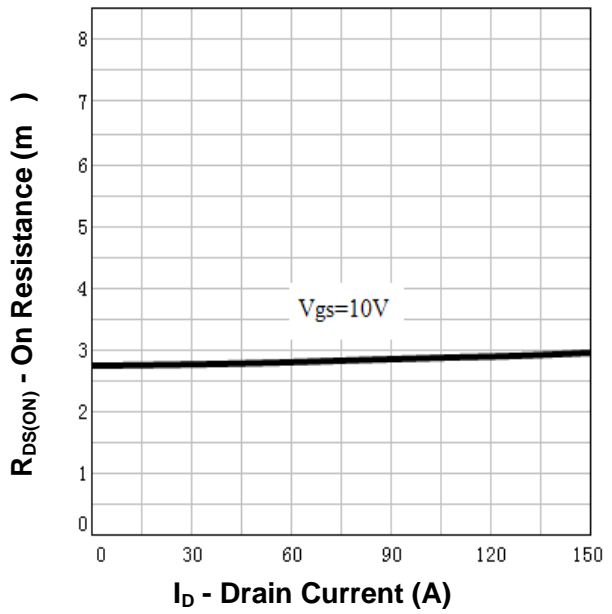


Typical Characteristics

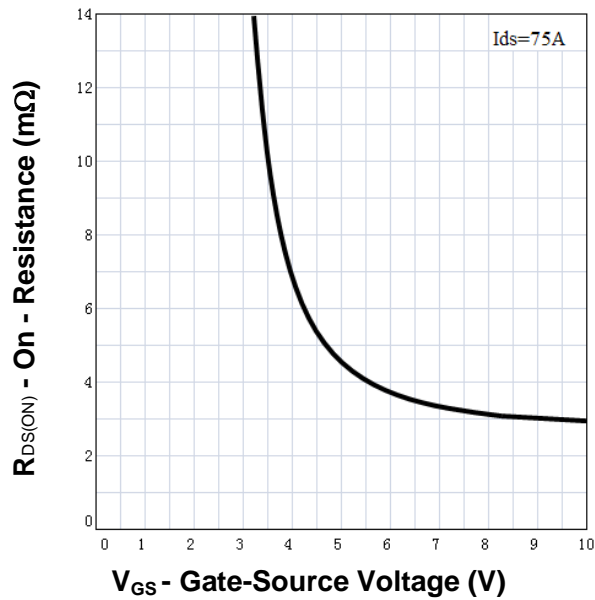
Output Characteristics



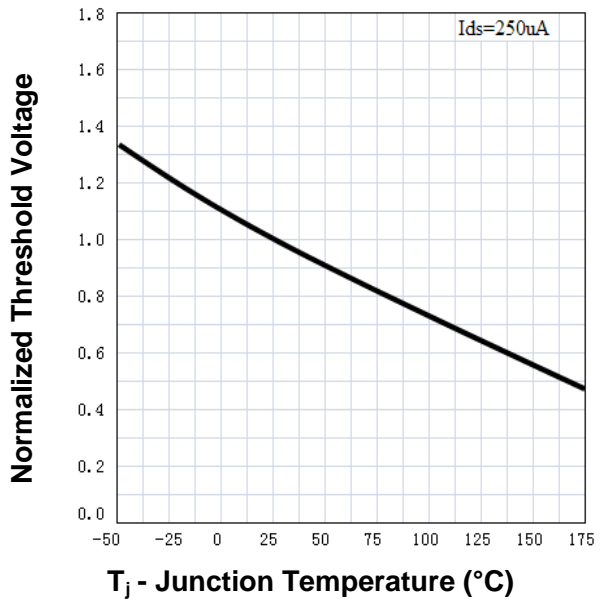
Drain-Source On Resistance



Drain-Source On Resistance

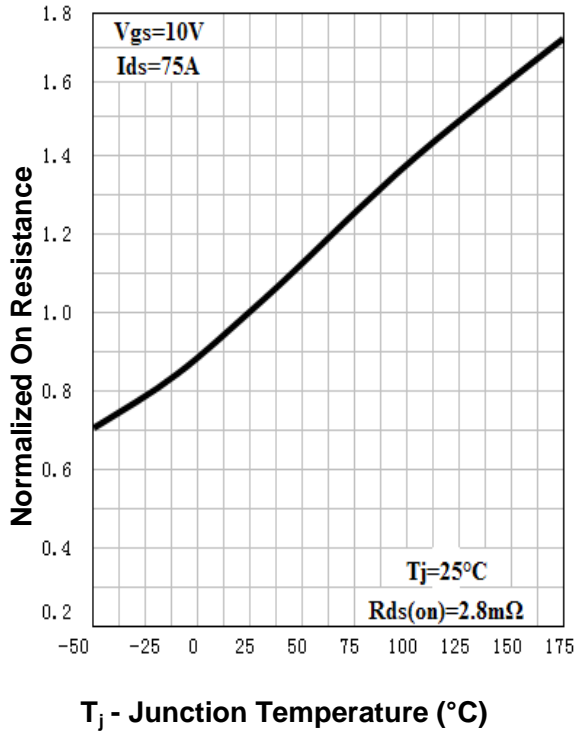


Gate Threshold Voltage

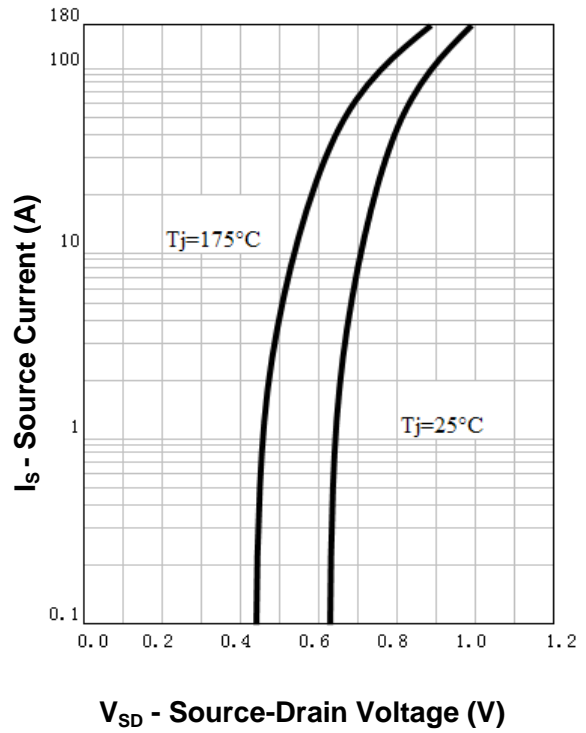


Typical Characteristics

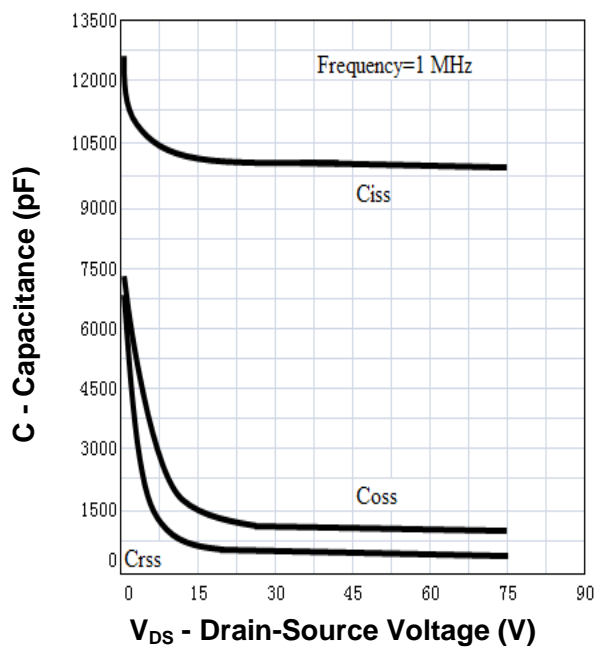
Drain-Source On Resistance



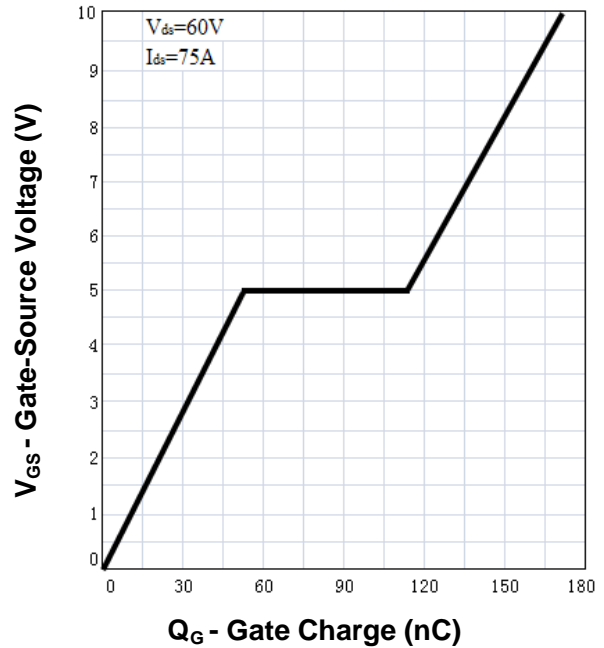
Source-Drain Diode Forward



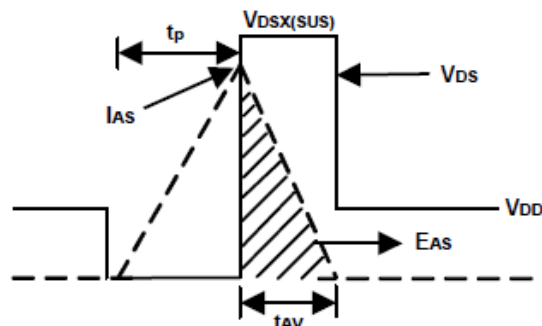
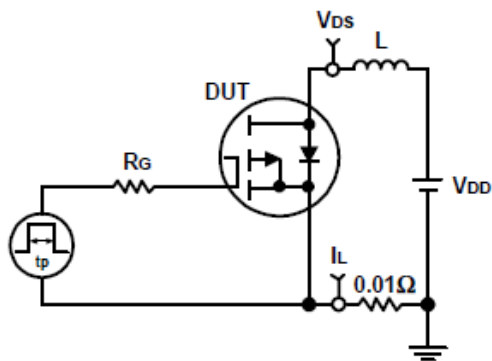
Capacitance



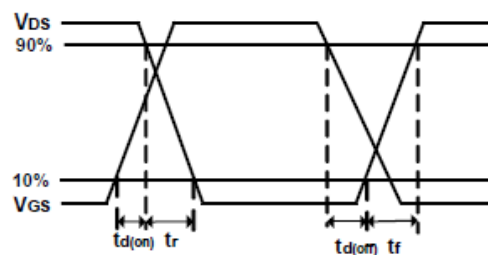
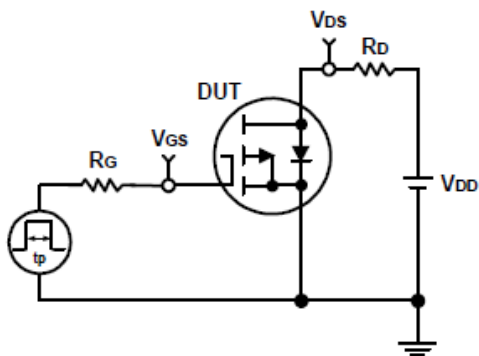
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms

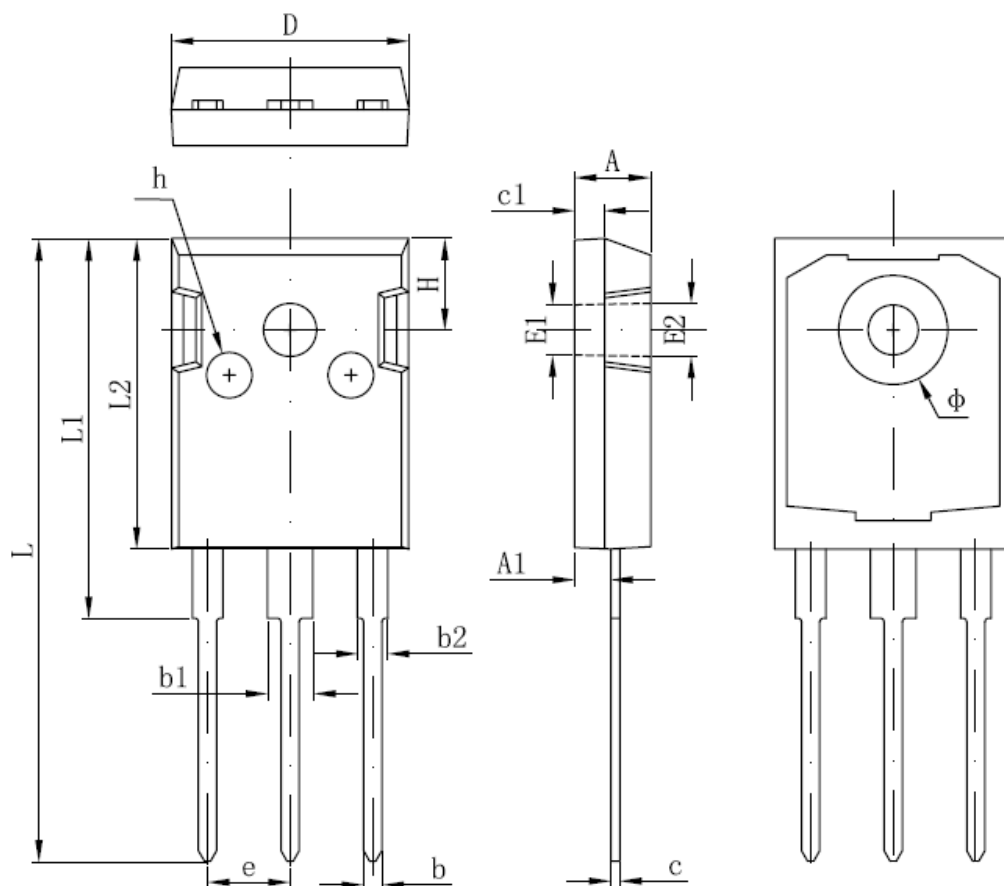


Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity	Reel Size	Tape width
RU75260Q	RU75260Q	TO-247	Tube	30	-	-

Package Information

TO-247



SYMBOL	MM		INCH		SYMBOL	MM		INCH	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
A	4.850	5.150	0,191	0.200	E2	3.600 REF		0.142 REF	
A1	2.200	2.600	0.087	0.102	L	40.900	41.300	1.610	1.626
B	1.000	1.400	0.039	0.055	L1	24.800	25.100	0.976	0.988
b1	2.800	3.200	0.110	0.126	L2	20.300	20.600	0.799	0.811
b2	1.800	2.200	0.071	0.087	Φ	7.100	7.300	0.280	0.287
c	0.500	0.700	0.020	0.028	e	5.450 TYP		0.215 TYP	
c1	1.900	2.100	0.075	0.083	H	5.980 REF.		0.235 REF.	
D	15.450	15.750	0.608	0.620	h	0.000	0.300	0.000	0.012
E1	3.500 REF.		0.138 REF.						

ALL DIMENSIONS REFER TO JEDEC STANDARD
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS

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