

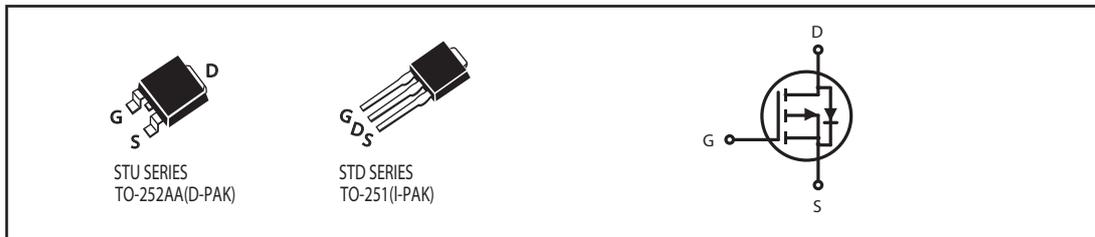


## P-Channel Logic Level Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> (mΩ) Max
-40V	-10A	45 @ V <sub>GS</sub> = -10V
		60 @ V <sub>GS</sub> = -4.5V

### FEATURES

- Super high dense cell design for low R<sub>DS(ON)</sub>.
- Rugged and reliable.
- TO-252 and TO-251 Package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25 C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-40	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous <sup>a</sup> @T <sub>a</sub>	I <sub>D</sub>	25 °C	-10	A
		70 °C	-8.3	A
-Pulsed <sup>b</sup>	I <sub>DM</sub>	-50	A	
Drain-Source Diode Forward Current <sup>a</sup>	I <sub>S</sub>	-10	A	
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>a</sub> =25 °C	50	W
		T <sub>a</sub> =70 °C	35	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 175	°C	

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	R <sub>θC</sub>	3	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θA</sub>	50	°C/W

# STU/D421S

P-Channel ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250uA	-40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -32V, V <sub>GS</sub> = 0V			1	uA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
<b>ON CHARACTERISTICS<sup>b</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250uA	-1.0	-1.7	-3.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -10A		34	45	m ohm
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -6A		47	60	m ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> = -5V, V <sub>GS</sub> = -10V	30			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -10A		11		S
<b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V f = 1.0MHz		900		pF
Output Capacitance	C <sub>OSS</sub>			135		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			85		pF
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1.0MHz		3.5		ohm
<b>SWITCHING CHARACTERISTICS<sup>c</sup></b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = -24V I <sub>D</sub> = -10 A V <sub>GS</sub> = -10V R <sub>GEN</sub> = 3.3 ohm		12		ns
Rise Time	t <sub>r</sub>			15		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			45		ns
Fall Time	t <sub>f</sub>			26		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -24V, I <sub>D</sub> = -10A, V <sub>GS</sub> = -10V		17		nC
		V <sub>DS</sub> = -24V, I <sub>D</sub> = -10A, V <sub>GS</sub> = -4.5V		8		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = -24V, I <sub>D</sub> = -16A		1.8		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> = -10V		5		nC

# STU/D421S

## ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>DRAIN-SOURCE DIODE CHARACTERISTICS <sup>a</sup></b>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = -10A$		-0.91	-1.3	V

### Notes

a. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

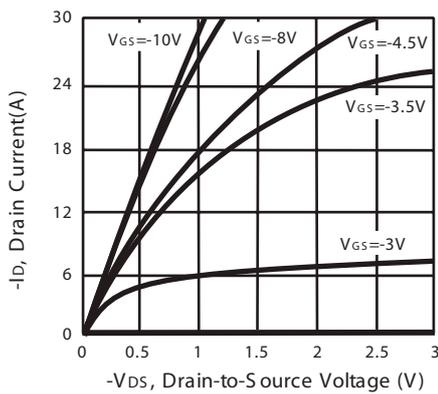


Figure 1. Output C characteristics

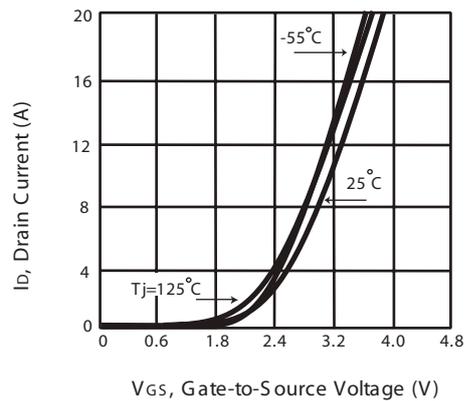


Figure 2. Transfer C characteristics

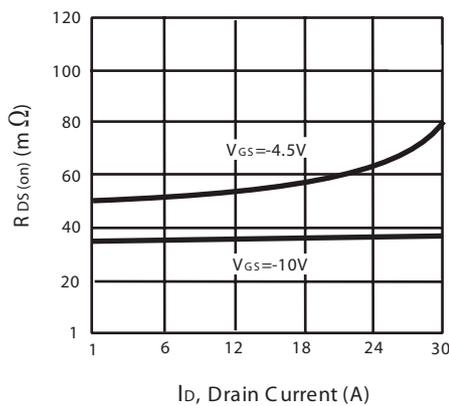


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

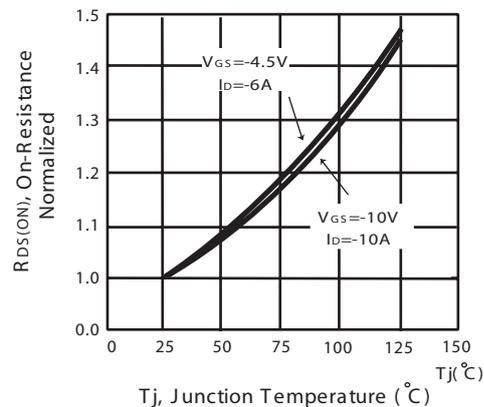


Figure 4. On-Resistance Variation with Drain Current and Temperature

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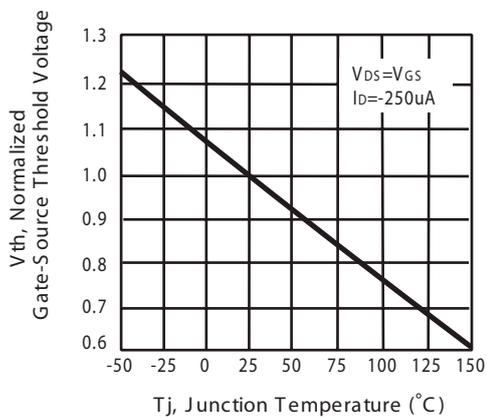


Figure 5. Gate Threshold Variation with Temperature

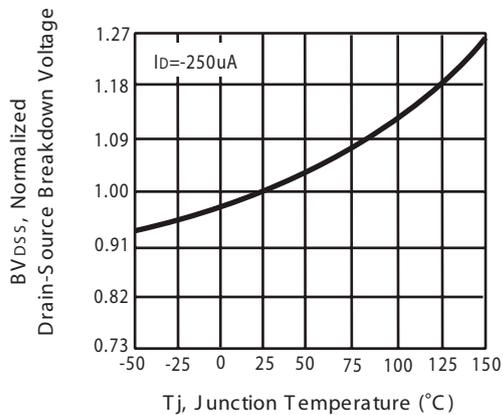


Figure 6. Breakdown Voltage Variation with Temperature

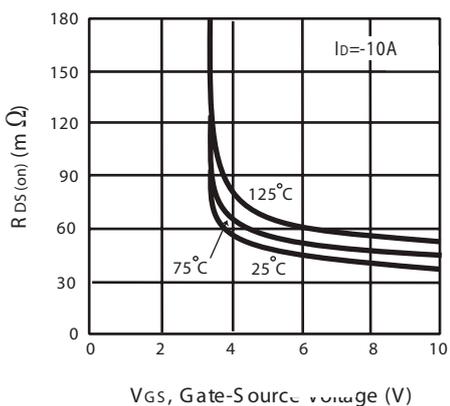


Figure 7. On-Resistance vs. Gate-Source Voltage

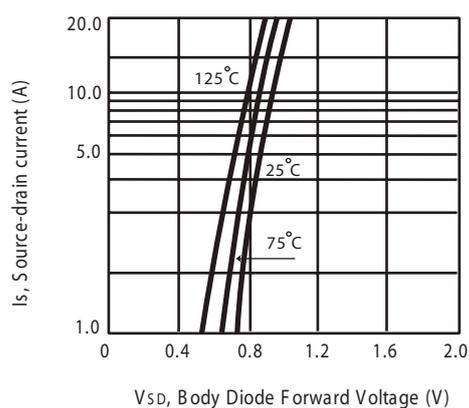


Figure 8. Body Diode Forward Voltage Variation with Source Current

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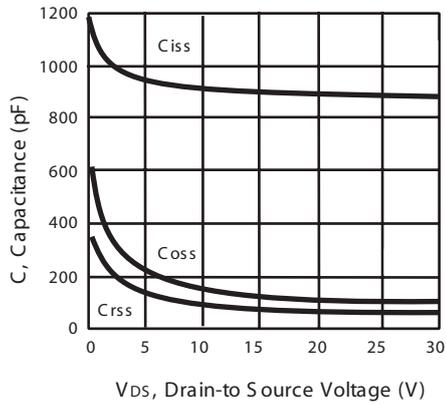


Figure 9. Capacitance

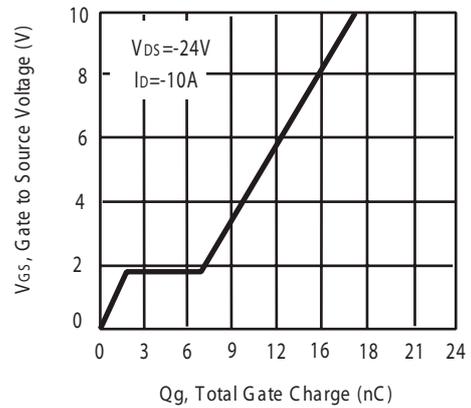


Figure 10. Gate Charge

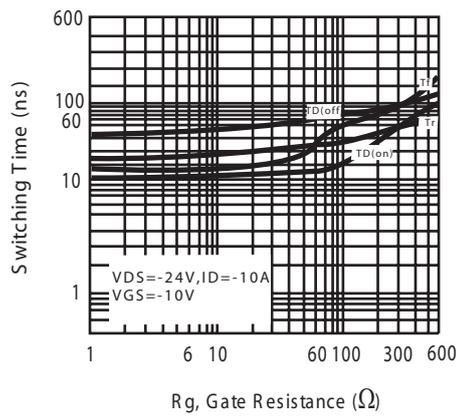


Figure 11. switching characteristics

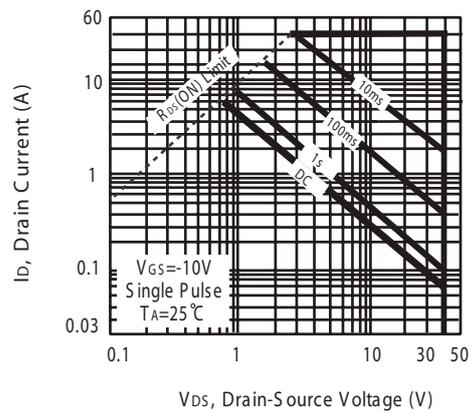


Figure 12. Maximum Safe Operating Area

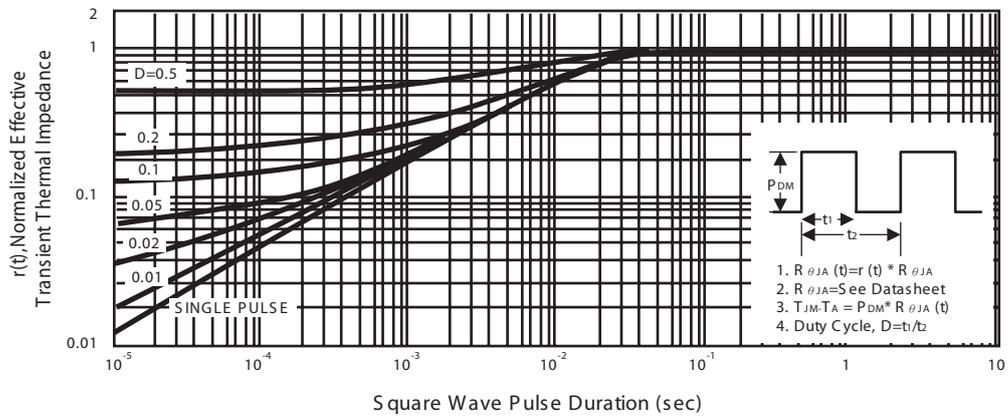
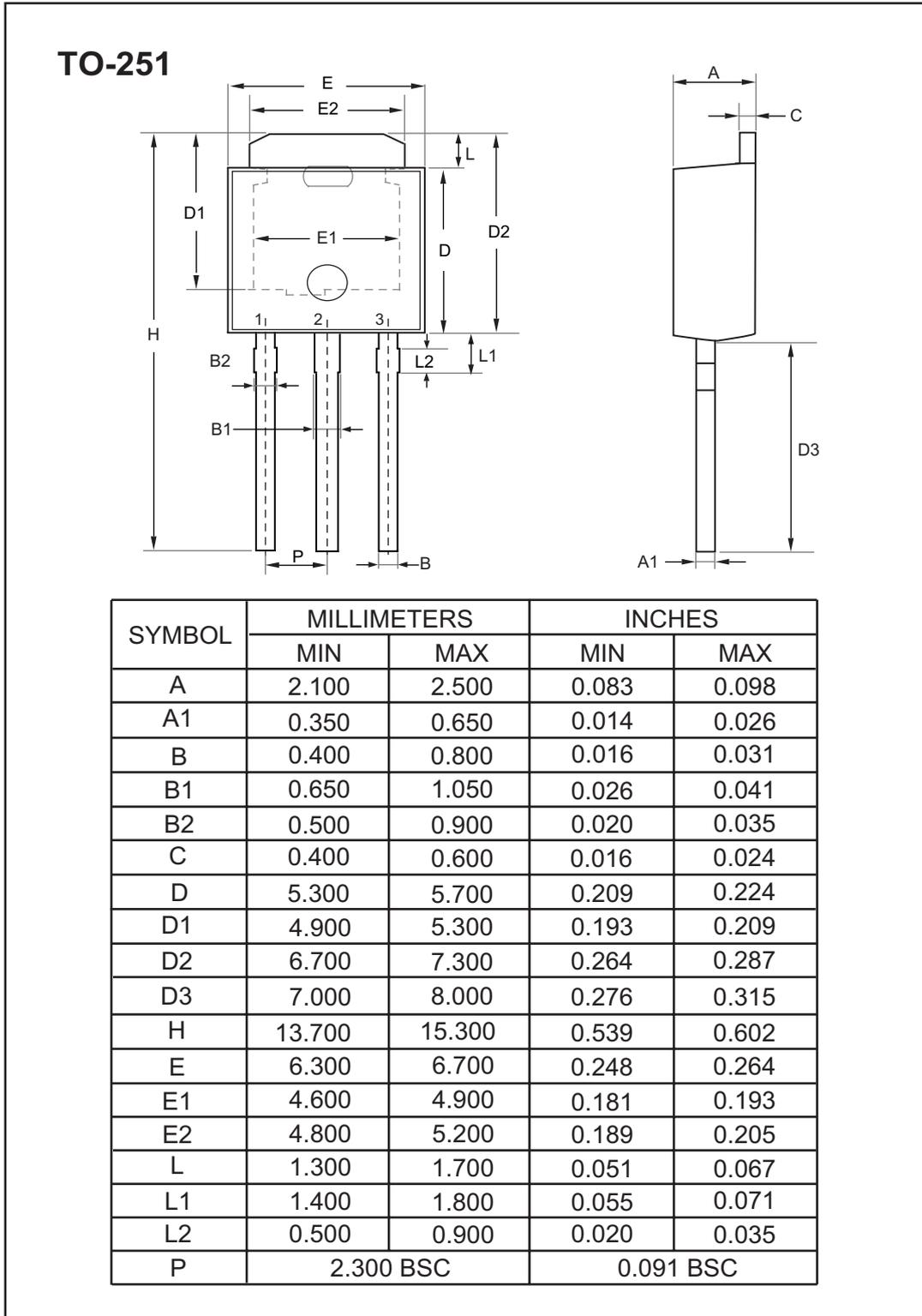


Figure 13. Normalized Thermal Transient Impedance Curve

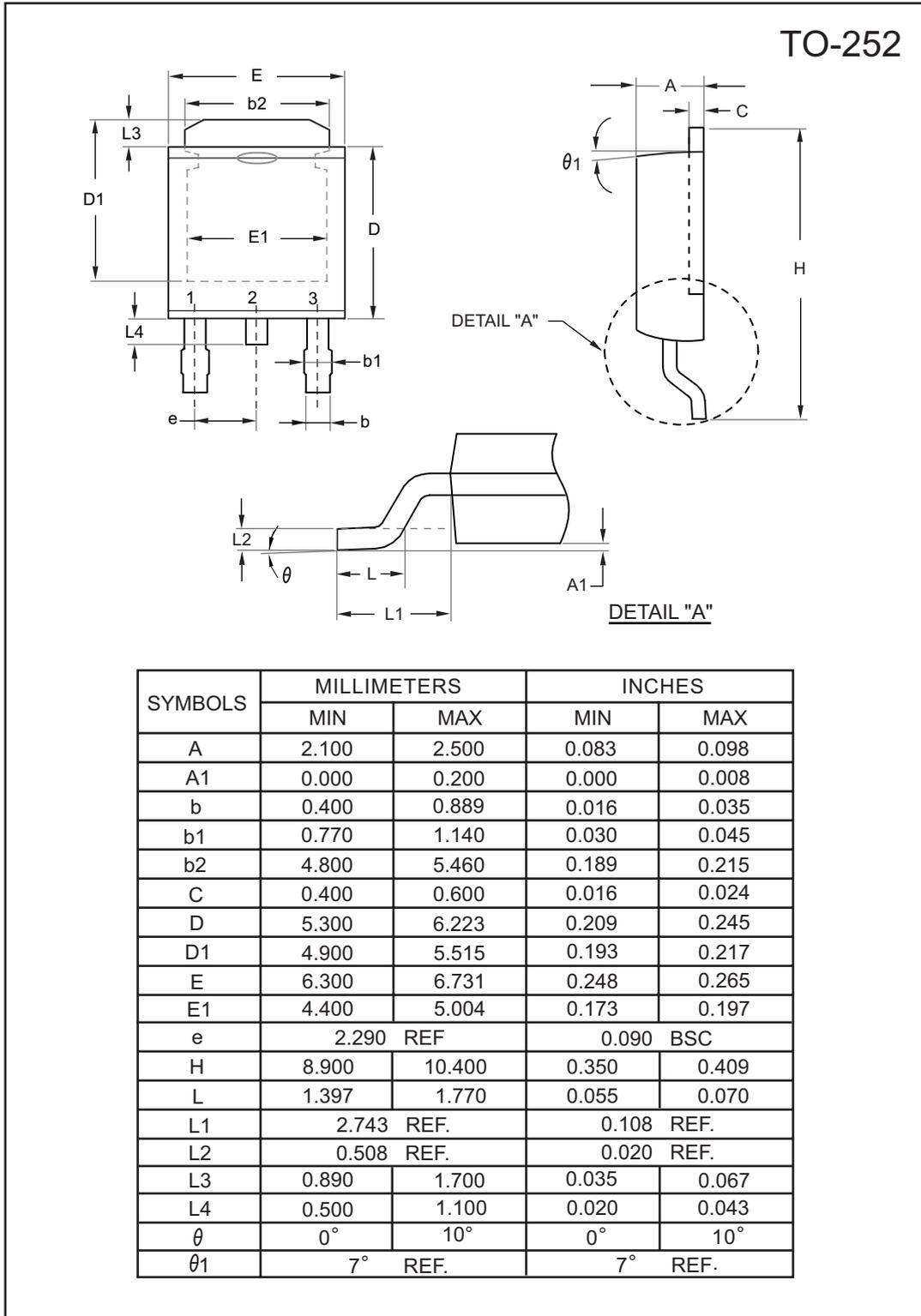
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## PACKAGE OUTLINE DIMENSIONS



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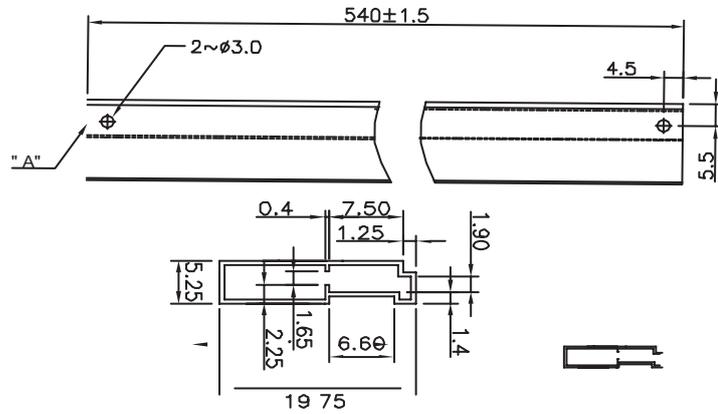
## PACKAGE OUTLINE DIMENSIONS



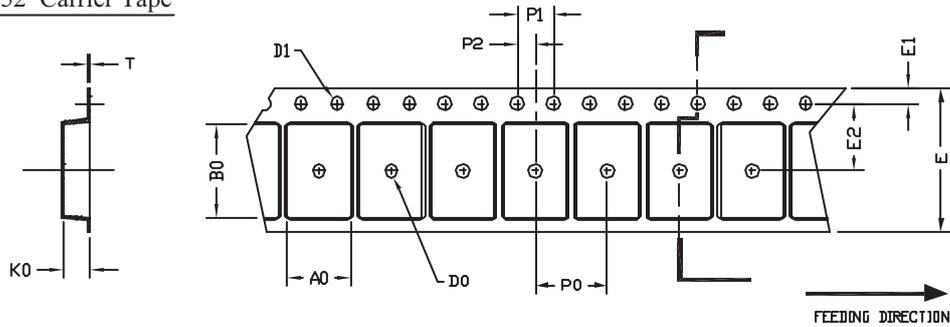
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## TO251 Tube/TO-252 Tape and Reel Data

### TO-251 Tube



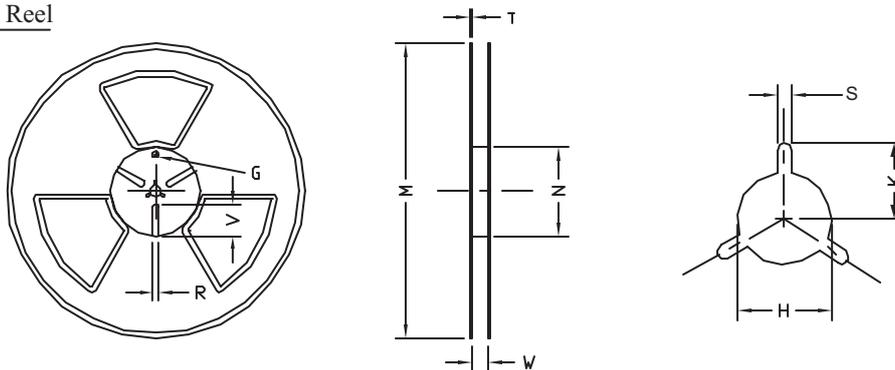
### TO-252 Carrier Tape



UNIT:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
TO-252 (16 mm)	6.80 ±0.1	10.3 ±0.1	2.50 ±0.1	φ 2	φ 1.5 + 0.1 - 0	16.0 0.3±	1.75 0.1±	7.5 ±0.15	8.0 ±0.1	4.0 ±0.1	2.0 ±0.15	0.3 ±0.05

### TO-252 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	T	H	K	S	G	R	V
16 mm	φ 330	φ 330 ± 0.5	φ 97 ± 1.0	17.0 + 1.5 - 0	2.2	φ 13.0 + 0.5 - 0.2	10.6	2.0 ±0.5	---	---	---