



SamHop Microelectronics Corp.

**STM6967**

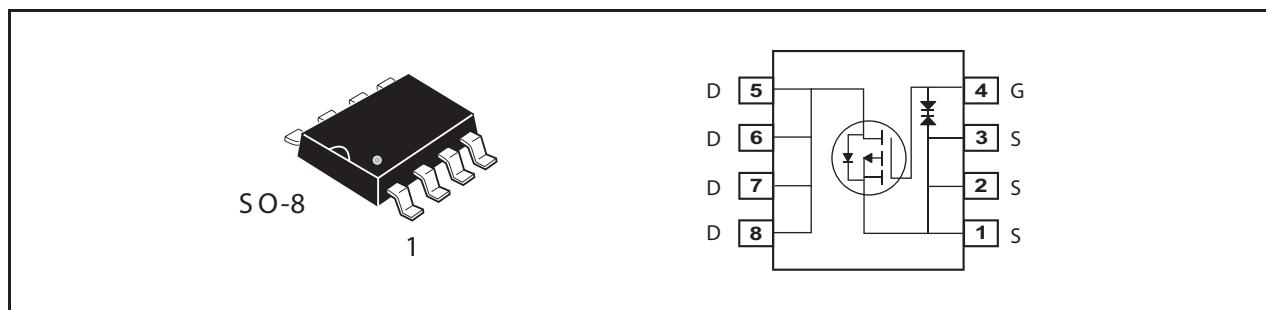
Ver 1.0

P-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
VDSS	ID	RDS(ON) (mΩ) Max
-60V	-4A	86 @ VGS=-10V
		125 @ VGS=-4.5V

FEATURES

- Super high dense cell design for low RDS(ON).
- Rugged and reliable.
- Surface Mount Package.



ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Limit	Units
V_{DS}	Drain-Source Voltage		-60	V
V_{GS}	Gate-Source Voltage		± 20	V
I_D	Drain Current-Continuous ^a	$T_A=25^\circ\text{C}$	-4	A
		$T_A=70^\circ\text{C}$	-3.2	A
I_{DM}	-Pulsed ^b		-22	A
E_{AS}	Single Pulse Avalanche Energy ^d		49	mJ
P_D	Maximum Power Dissipation ^a	$T_A=25^\circ\text{C}$	2.5	W
		$T_A=70^\circ\text{C}$	1.6	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range		-55 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ^a	50	$^\circ\text{C/W}$
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V , ID=-250uA	-60			V
IBSS	Zero Gate Voltage Drain Current	VDS=-48V , VGS=0V			-1	uA
IGSS	Gate-Body Leakage Current	VGS= ±20V , VDS=0V			±10	uA
ON CHARACTERISTICS						
VGS(th)	Gate Threshold Voltage	VDS=VGS , ID=-250uA	-2.0	-2.4	-4.0	V
RDS(ON)	Drain-Source On-State Resistance	VGS=-10V , ID=-4A		69	86	m ohm
		VGS=-4.5V , ID=-3.3A		93	125	m ohm
gFS	Forward Transconductance	VDS=-5V , ID=-4A		15		S
DYNAMIC CHARACTERISTICS						
Ciss	Input Capacitance	VDS=-15V,VGS=0V f=1.0MHz		1110		pF
Coss	Output Capacitance			79		pF
CRSS	Reverse Transfer Capacitance			56		pF
SWITCHING CHARACTERISTICS						
tD(ON)	Turn-On Delay Time	VDD=-15V ID=-1A VGS=-10V RGEN= 6 ohm		23		ns
tr	Rise Time			19		ns
tD(OFF)	Turn-Off Delay Time			71		ns
tf	Fall Time			37		ns
Qg	Total Gate Charge	VDS=-15V, ID=-4A, VGS=-10V		24		nC
		VDS=-15V, ID=-4A, VGS=-4.5V		11.5		nC
Qgs	Gate-Source Charge	VDS=-15V, ID=-4A, VGS=-10V		2.9		nC
Qgd	Gate-Drain Charge			6.2		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
VSD	Diode Forward Voltage	VGS=0V, Is=-1A		-0.79	-1.2	V
Notes						
a.Surface Mounted on FR4 Board,t ≤ 10sec.						
b.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.						
c.Guaranteed by design, not subject to production testing.						
d.Starting TJ=25°C,L=0.5mH,VDD = 30V.(See Figure13)						

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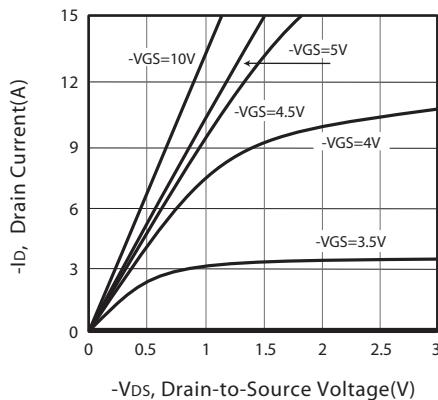


Figure 1. Output Characteristics

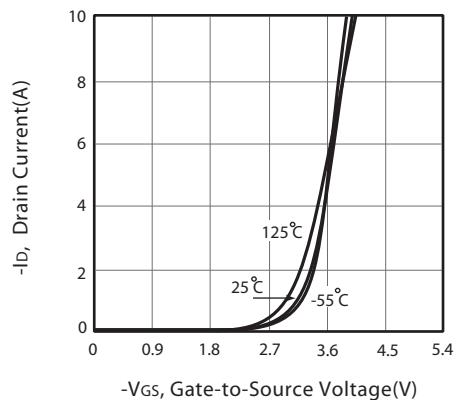


Figure 2. Transfer Characteristics

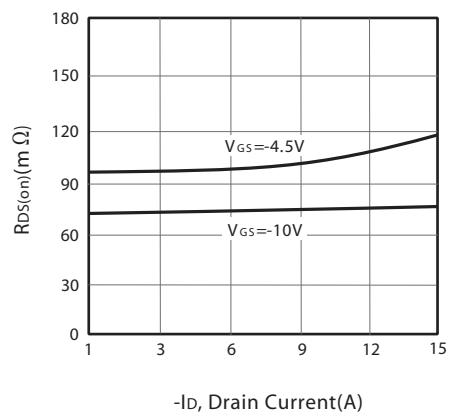


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

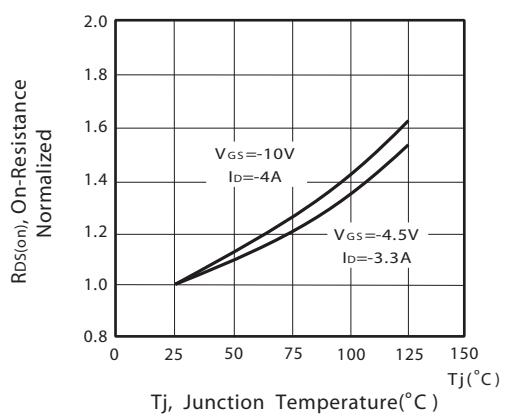


Figure 4. On-Resistance Variation with Drain Current and Temperature

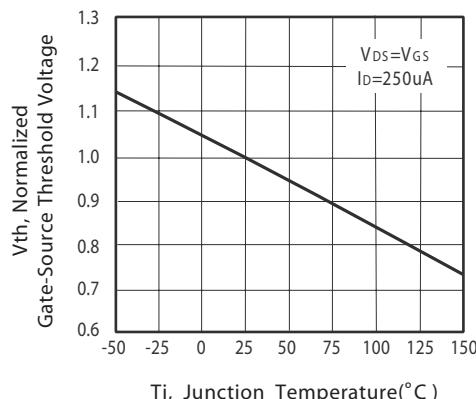


Figure 5. Gate Threshold Variation with Temperature

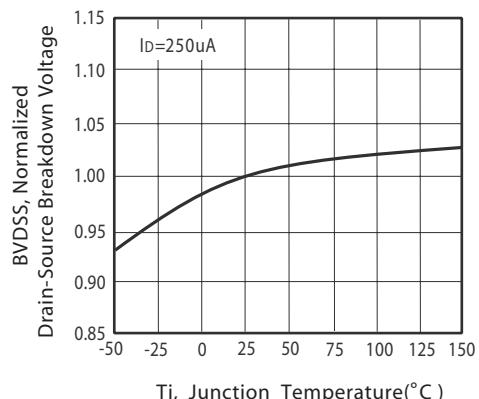


Figure 6. Breakdown Voltage Variation with Temperature

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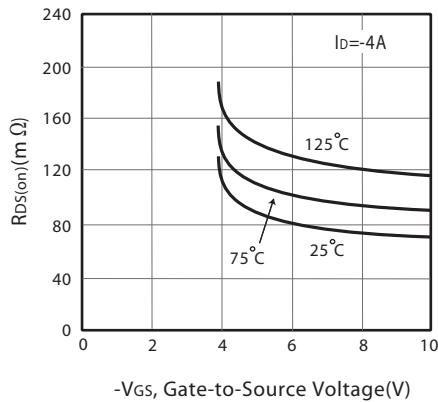


Figure 7. On-Resistance vs.
Gate-Source Voltage

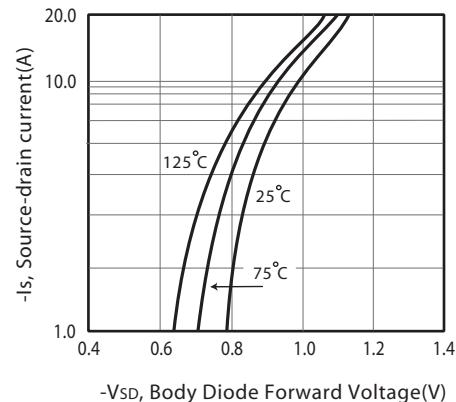


Figure 8. Body Diode Forward Voltage
Variation with Source Current

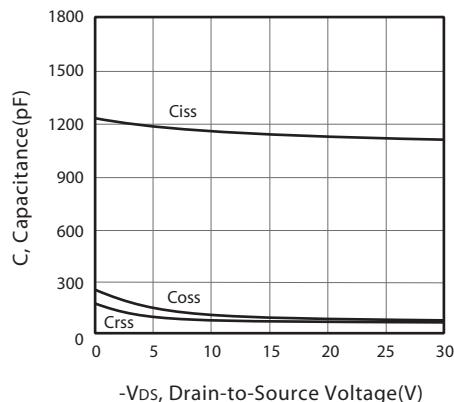


Figure 9. Capacitance

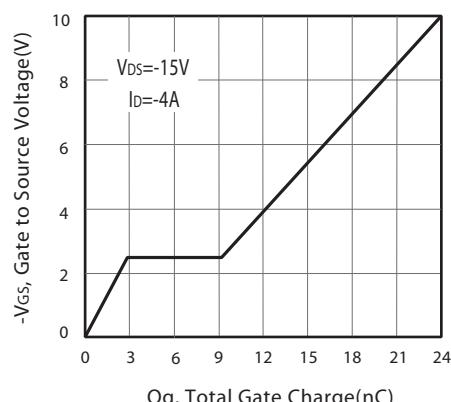


Figure 10. Gate Charge

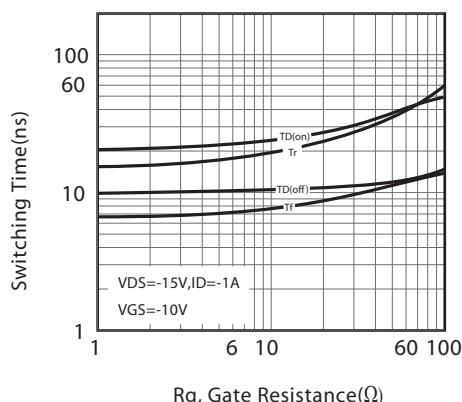


Figure 11. switching characteristics

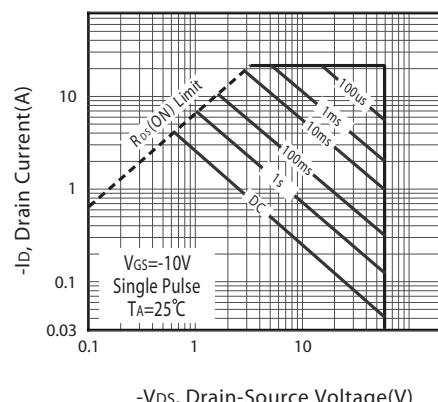
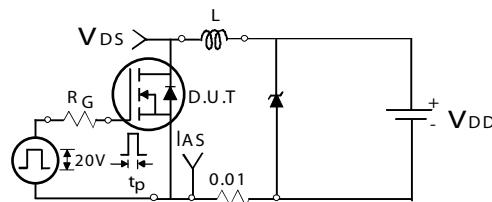


Figure 12. Maximum Safe Operating Area

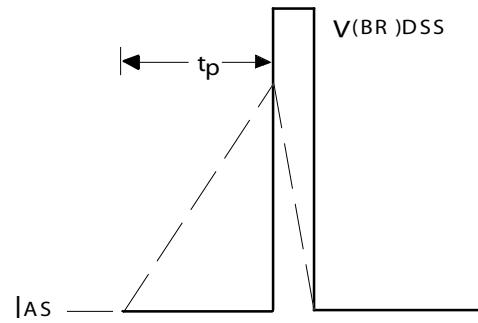
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Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

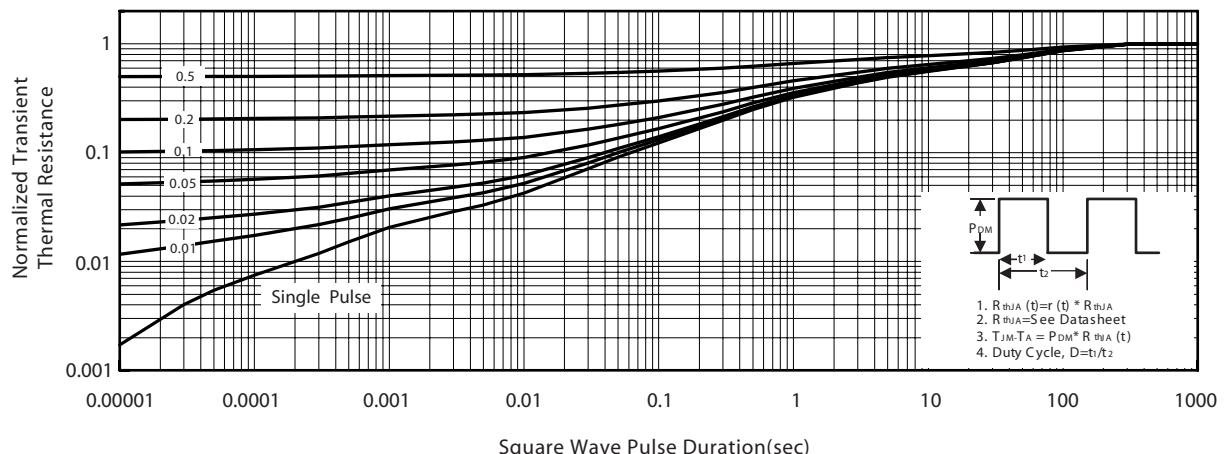
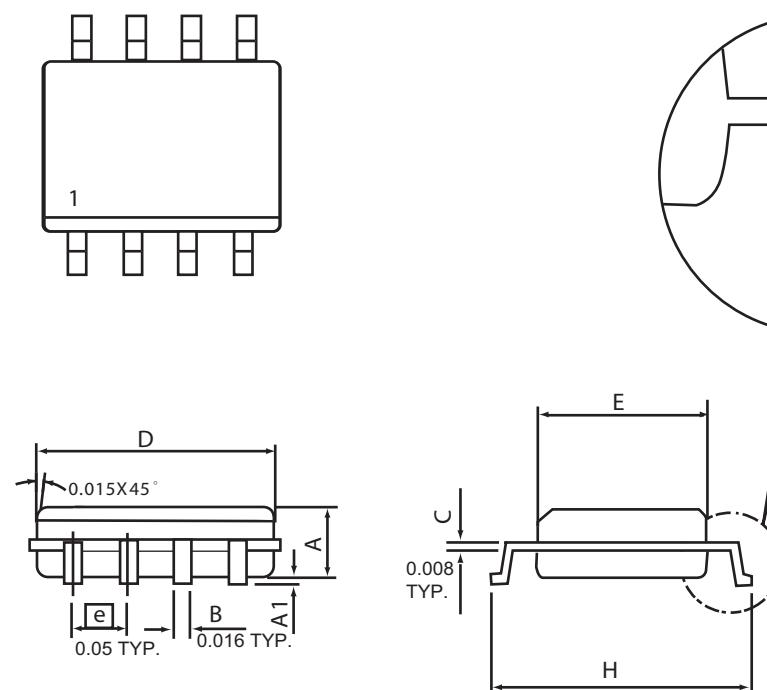


Figure 14. Normalized Thermal Transient Impedance Curve

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PACKAGE OUTLINE DIMENSIONS

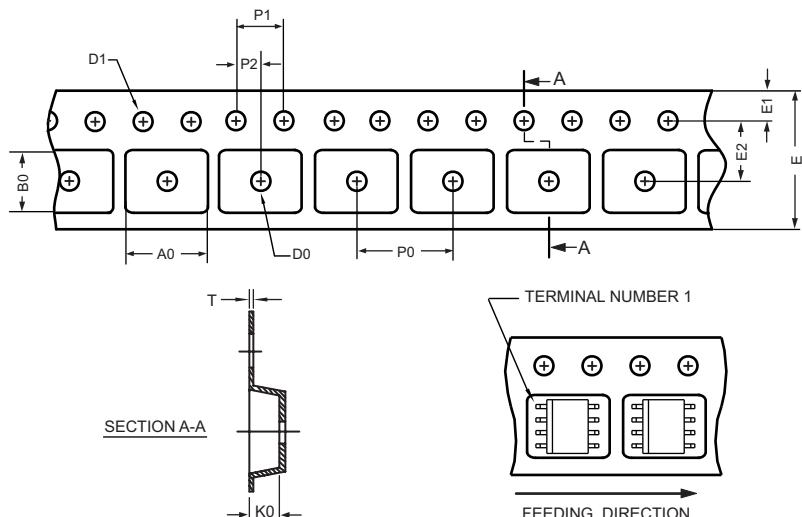
SO-8



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

SO-8 Tape and Reel Data

SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.50 ± 0.15	5.25 ± 0.10	2.10 ± 0.10	$\phi 1.5$ (MIN)	$\phi 1.55$ ± 0.10	12.0 $+0.3$ -0.1	1.75 ± 0.10	5.5 ± 0.10	8.0 ± 0.10	4.0 ± 0.10	2.0 ± 0.10	0.30 ± 0.013

SO-8 Reel

