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BLX95

U.H.F. POWER TRANSISTOR

N-P-N silicon planar epitaxial transistor intended for transmitting applications in class-A, B or C in the u.h.f. frequency range for supply voltages up to 28 V. The transistor is resistance stabilized and is tested under severe load mismatch conditions. Due to a gold metallization excellent reliability properties have been obtained. The transistor is housed in a capstan envelope with a moulded cap. All leads are isolated from the stud.

QUICK REFERENCE DATA

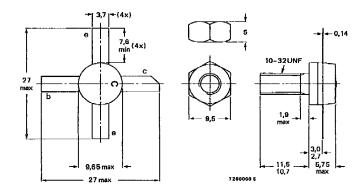
R.F. performance up to $T_h = 25$ °C in an unneutralized common-emitter class-B circuit

mode of operation	V _{CE} V	f MHz	Ps W	P _L W	I _C	G _p dB	η %
c.w.	28	470	< 14,2	40	< 2,4	> 4,5	> 60
	28	175	typ. 3,2	40	typ. 1,9	typ. 11	typ. 75

MECHANICAL DATA

Fig. 1 SOT-56.

Dimensions in mm



Torque on nut: min. 1,5 Nm

(15 kg cm)

max. 1,7 Nm

(17 kg cm)

Diameter of clearance hole in heatsink: max. 4,9 mm.

Mounting hole to have no burrs at either end.

De-burring must leave surface flat; do not chamfer

or countersink either end of hole.

When locking is required an adhesive is preferred instead of a lock washer.

PRODUCT SAFETY This device incorporates beryllium oxide, the dust of which is toxic. The device is entirely safe provided that the BeO disc is not damaged.



NJ Semi-Conductors reserves the right to change test conditions, parameters limits and package dimensions without notice information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Sami-Conductors

 $\mbox{\bf RATINGS}$ Limiting values in accordance with the Absolute Maximum System

Collector-base voltage (open emitter) peak value	V	CBOM n	nax. 65	v
Collector-emitter voltage ($R_{BE} = 10 \Omega$) peak value	V	CERM m	nax. 65	v
Collector-emitter voltage (open base)	v	CEO m	nax. 30	v
Emitter-base voltage (open collector)	\mathbf{v}_{1}	EBO m	nax. 4	v
Collector current (average)	IC	C(AV) m	nax. 3,0	Α
Collector current (peak value) f > 1 MHz	IC	M m	nax. 10,0	Α
10 ² D.C. SOAR Th ≤ 25 °C Rth mb-h=0,3 K/W 10 Ptot max (d.c.) 1 10 VCE (V) 10 ² Storage temperature Junction temperature	50 norm	Relation of the second	3,0,0,3,KW	100 °C °C
THERMAL RESISTANCE				
From junction to mounting base From mounting base to heatsink	R _{ti} R _{ti}	h j-mb = h mb-h =	2,0 0,3	K/W K/W

CHARACTERISTICS $T_j = 25$ °C unless otherwise specified				
Breakdown voltages				
Collector-base voltage open emitter, $I_C = 50 \text{ mA}$	V(BR)CBO	>	65	v
Collector-emitter voltage $R_{BE} = 10 \Omega$, $I_C = 50 \text{ mA}$	V _{(BR)CER}	>	65	v
open base, IC = 50 mA	V(BR)CEO	>	30	v
Emitter-base voltage open collector, IE = 10 mA	V _{(BR)EBO}	>	4	v
Transient energy				
L = 25 mH; f = 50 Hz				
open base $-V_{BE} = 1.5 \text{ V}; R_{BE} = 33 \Omega$	E E	>	4,5 4,5	mS mS
D.C. current gain				
$I_{C} = 1.0 A; V_{CE} = 5 V$	hFE	25 to	100	
Transition frequency				
$I_C = 4 A: V_{CE} = 25 V$	$\mathbf{f}_{\mathbf{T}}$	typ.	900	MHz
Collector capacitance at f = 1 MHz				
$I_E = I_e = 0$: $V_{CB} = 30 \text{ V}$	C_c	typ.	68 80	pF pF
Feedback capacitance at f = 1 MHz				
$I_C = 200 \text{ mA}; V_{CE} = 30 \text{ V}$	$\mathrm{c}_{\mathtt{re}}$	typ.	39	pF
Collector-stud capacitance	$C_{\mathbf{cs}}$	typ.	2	pF