Regulator IC with the Soft-Start
Monolithic IC MM192x Series

Outline

This IC is 1A regulator IC with soft-start. When Cs pin is connected with a capacitor, the output voltage is started up slowly. This IC can reduce a rush current by the soft-start.
Therefore a transformer in a power supply for this IC can be small, and it is possible to reduce a total cost in the power supply system. And this IC can be turned off by Cont pin.
Package TO-252-5 or HSOP-8 were used for this device.

Features

1.	Input Voltage Range	2.4V~12V
2.	Output Voltage Range	12V~5.1V
3.	Output Voltage accuracy 1 (V_{OUT}<1.5V) :	V_{OUT}±30mV
4.	Output Voltage accuracy 2 (V_{OUT}≧1.5V) :	V_{OUT}±2%
5.	Maximum Output Current	1A
6.	Supply Current	1mA typ. (No-Load Input Current) 1μA max. (OFF)
7.	Dropout Voltage	0.3V max. (I_O=500mA), 0.6V max. (I_O=1A)
8.	Line Regulation	10mV typ., 20mV max. (I_O=250mA)
9.	Load Regulation	20mV typ., 100mV max. (I_O=1mA~1A)
10.	Output Capacitor	1μF
11.	Output Rise Time	10msec typ. (Cs Capacitor=0.1μF)
12.	With ON/OFF Control Pin	

Phased Out Products

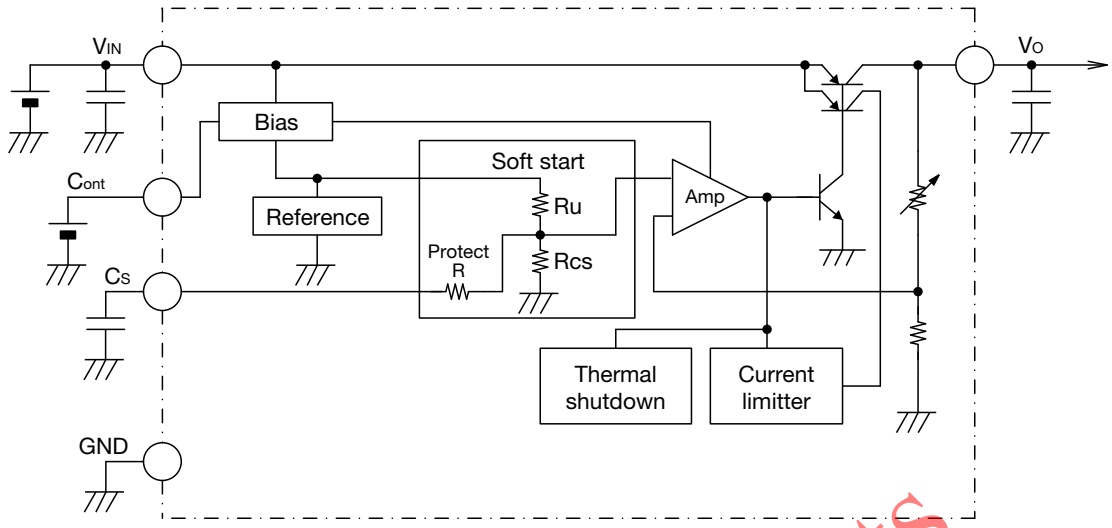
Package

TO-252-5
HSOP-8A

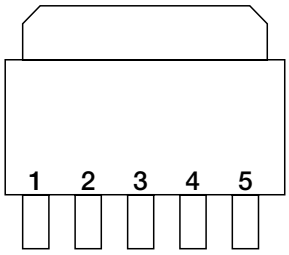
Applications

1. DVD Recorders
2. Blu-ray Disc Recorders
3. TVs

Block Diagram

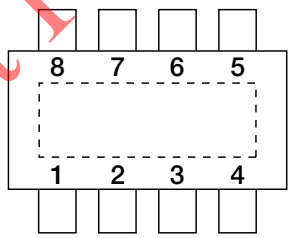


Pin Assignment



TO-252-5
(TOP VIEW)

1	Cont
2	VIN
3	GND
4	VO
5	Cs



HSOP-8A
(TOP VIEW)

1	VO
2	NC
3	GND
4	Cs
5	Cont
6	NC
7	NC
8	VIN

Phased Out Products

Pin Description

TO-252-5

Pin No.	Pin name	Functions
1	Cont	ON/OFF-Control pin Cont: Low = OFF, High = ON Connect Cont pin with VIN pin, when it is not used.
2	VIN	Voltage-supply pin
3	GND	Ground
4	VO	Output pin
5	Cs	Soft-start pin

HSOP-8A

Pin No.	Pin name	Functions
1	VO	Output pin
2	NC	No connection
3	GND	Ground
4	Cs	Soft-start pin
5	Cont	ON/OFF-Control pin Cont: Low = OFF, High = ON Connect Cont pin with VIN pin, when it is not used.
6	NC	No connection
7	NC	No connection
8	VIN	Voltage-supply pin

Absolute Maximum Ratings (Except where noted otherwise Ta=25°C)

Item	Symbol	Ratings		Units
Storage Temperature	TSTG	-40~+150		°C
Supply Voltage	VIN	13.2		V
Power Dissipation	Pd	2.5(Note1)	TO-252-5	W
		1.8(Note2)	HSOP-8A	

Note1 : With the double sided PC Board of glass epoxy
(Copper plane 80%, 150 × 100 × 1.0^{t}mm)
Note2 : With the double sided PC Board of glass epoxy
(Copper plane 80%, 37 × 37 × 1.6^{t}mm)

Phased Out Products

Recommended Operating Conditions (Except where noted otherwise Ta=25°C)

Item	Symbol	Ratings	Units
Operating Ambient Temperature	TJOP	-40~85	°C
Operating Voltage	VOP	VO<2.0V 2.4~12 VO≧2.0V VO (typ.)+0.5~12	V
Output Current	IO	0~1	A

Electrical Characteristics 1 (Except where noted otherwise $V_{IN}=V_O+1V$, $V_{Cont}=V_{IN}$, Ta=25°C)

Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Units
Input Current(OFF)	Iinoff	V_{Cont}=0V			1	μA
No-Load Input Current	Iin	I_O=0mA		1	2	mA
Output Voltage Tolerance	V_O	I_O=250mA V_O≧1.5V	–2		2	%
		I_O=250mA V_O<1.5V	–30		30	mV
Line Regulation	V_{LINE}	$V_{IN}=V_O+1.5\sim V_O+2.5V$, I_O=250mA		10	20	mV
Load Regulation	V_{LOAD}	1mA≦I_O≦1A		20	100	mV
Dropout Voltage 1	Vio1	V_{IN}=Vo-0.2V, I_O=500mA			0.3	V
Dropout Voltage 2	Vio2	V_{IN}=Vo-0.2V, I_O=1A			0.6	V
Ripple Rejection	RR	f=1kHz, Vripple=1V, I_O=250mA (Note3)		70		dB
V_{OUT} Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	–40≦T_{OP}≦85°C (Note3)		±100		ppm/°C
Output Rise Time	tr	V_{Cont}=L→H~V_O×0.9, Cs=0.1μF (Note3)		10		ms
Cs Discharge Resistance	Rcs		63	90	117	kΩ
Cont Pin Input Current	I_{Cont}	V_{Cont}=5V	10	20	30	μA
Cont Pin High Threshold Level	V_{ContH}		2			V
Cont Pin Low Threshold Level	V_{ContL}				1	V
Thermal shutdown	TSD	(Note3, 4)	110	130	150	°C

Note3 : The parameter is guaranteed by design.
Note4 : The parameter has Thermal shutdown hysteresis.

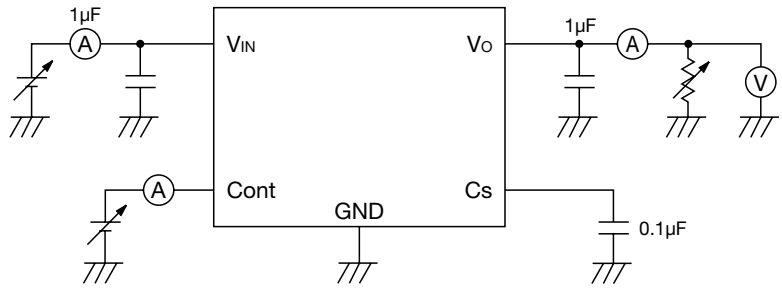
Phased Out Products

Electrical Characteristics 2 (Except where noted otherwise $V_{IN}=V_O+0.5V$, $I_O=1mA$, Ta=25°C)

Model No.	Measurement Conditions	Output Voltage (V)		
		Min.	Typ.	Max.
MM1921C	Ta=25°C $V_{IN}=2.4V$ $I_O=250mA$	1.170	1.2	1.230
MM1921Z		1.230	1.26	1.290
MM1921D		1.270	1.3	1.330
MM1921E		1.370	1.4	1.430
MM1921F		1.470	1.5	1.530
MM1921G		1.568	1.6	1.632
MM1921H		1.666	1.7	1.734
MM1921J		1.764	1.8	1.836
MM1921K		1.862	1.9	1.938
MM1922A	Ta=25°C $V_{IN}=V_O+1V$ $I_O=250mA$	1.960	2.0	2.040
MM1922B		2.058	2.1	2.142
MM1922C		2.156	2.2	2.244
MM1922D		2.254	2.3	2.346
MM1922E		2.352	2.4	2.448
MM1922F		2.450	2.5	2.550
MM1922G		2.548	2.6	2.652
MM1922H		2.646	2.7	2.754
MM1922J		2.744	2.8	2.856
MM1922K		2.842	2.9	2.958
MM1923A		2.940	3.0	3.060
MM1923B		3.038	3.1	3.162
MM1923C		3.136	3.2	3.264
MM1923D		3.234	3.3	3.366
MM1923E		3.332	3.4	3.468
MM1923F		3.430	3.5	3.570
MM1923G		3.528	3.6	3.672
MM1923H		3.626	3.7	3.774
MM1923J		3.724	3.8	3.876
MM1923K		3.822	3.9	3.978
MM1924A		3.920	4.0	4.080
MM1924B		4.018	4.1	4.182
MM1924C		4.116	4.2	4.284
MM1924D		4.214	4.3	4.386
MM1924E		4.312	4.4	4.488
MM1924F		4.410	4.5	4.590
MM1924G		4.508	4.6	4.692
MM1924H		4.606	4.7	4.794
MM1924J		4.704	4.8	4.896
MM1924K		4.802	4.9	4.998
MM1925A		4.900	5.0	5.100
MM1925B		4.998	5.1	5.202
MM1923W		3.283	3.35	3.417
MM1923Y		3.773	3.85	3.927

Phased Out Products

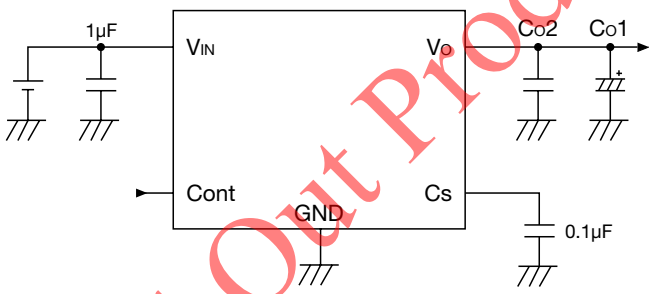
Measuring Circuit



(Reference example of external parts)

· Output capacitor C_O Ceramic capacitor (B temperature characteristics) 1µF
· Input Capacitor C_{IN} Ceramic capacitor 1µF
· Softstart Capacitor Cs Ceramic capacitor 0.1µF

Application Circuit

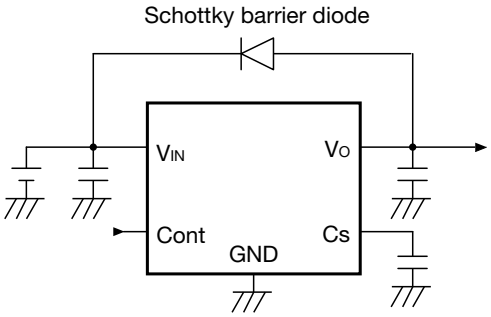


Cin	Co1			Co2		
Capacity	Capacity	Kind	Characteristics	Capacity	Kind	Characteristics
−40°C≦Top≦85°C				1.0µFor more	Ceramic	B type/X5R
	Free	AL electrolytic	Standard	1.0µFor more	Ceramic	B type/X5R
Using only by AL electrolytic does not recommend.						

Phased Out Products

· Note

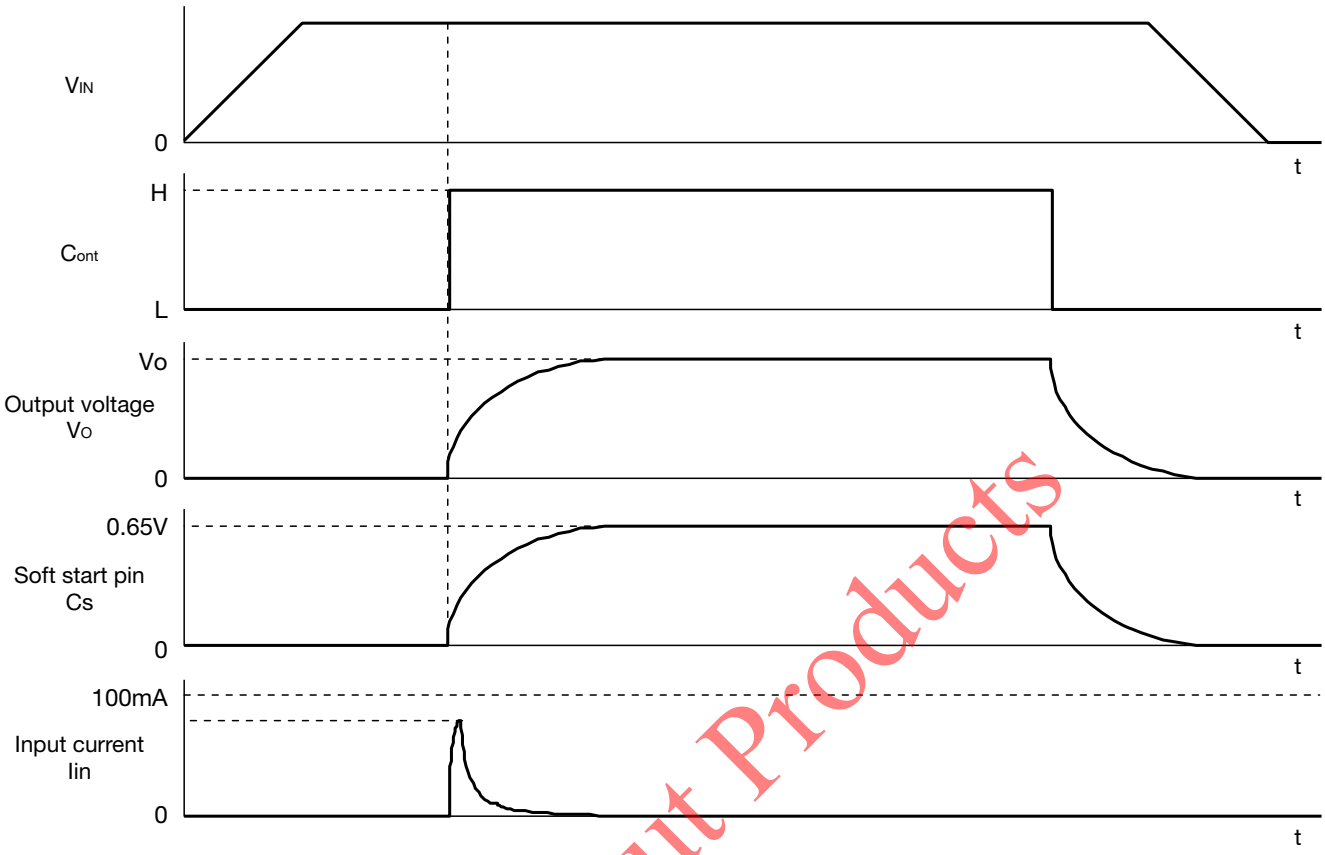
1. The output capacitor is required between output and GND to prevent oscillation.
2. The ESR of capacitor must be defined in ESR stability area.
3. The wire of Vcc and GND is required to print full ground plane for noise and stability.
4. The input capacitor must be connected a distance of less than 1cm from input pin.
5. In case the output voltage is above the input voltage, the overcurrent flow by internal parasitic diode from output to input. Therefore please connect a schottoky barrier diode from output to input.



6. Please connect the soft-start capacitor(Cs) more than 0.01µF, if not, there's possible to occur oscillation.
7. The output capacitor and the softstart capacitor must be connected it within the limits a rush current peak level 1A showed in the typical performance characteristics.
8. When rush current exceeds 1A, it is restricted with the current limit set up with the chip, an output rise time is uncontrollable by soft-start capacitor.
9. When use connecting Vin and Cont, in the case of starting Vin in input rise time longer then the set-up soft-start time, an output rise time is decied by a Vin input rise time.
   Moreover, in this case, in the case of the Vo-model≦2.4V, since there is a period when output voltage is raised to input voltage by Vin≦2.4V, Vin input rise time earlier than the set-up soft-start time is recommended.
10. When use Vin in a smalll dropout voltage, there's possible to occur oscillation.
    In this case, connect the input capacitor(Cin) or output capacitor(Co) more than 10µF is recommended.

Phased Out Products

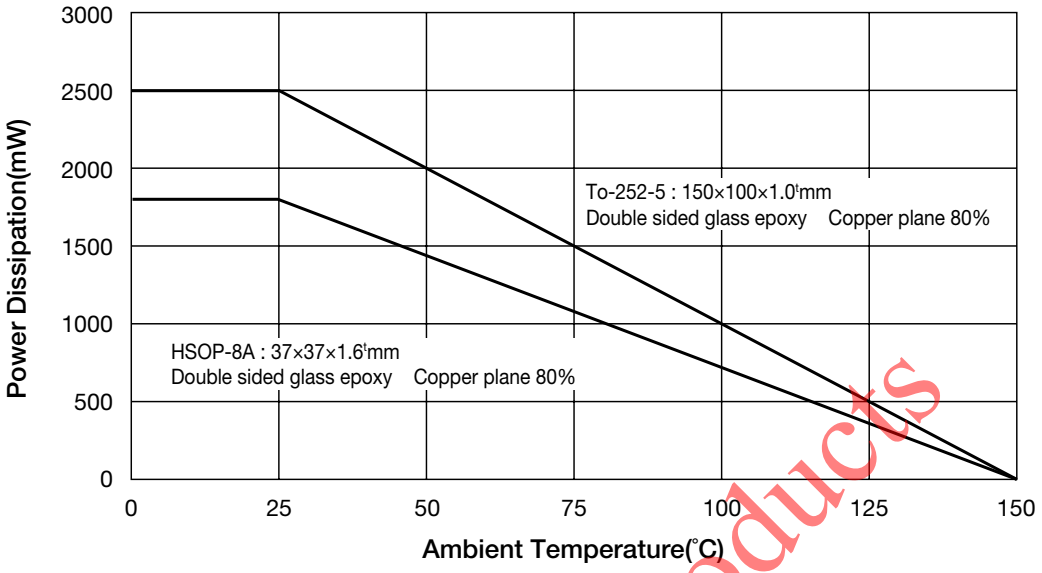
Timing Chart



Phased Out Products

About Power Dissipation

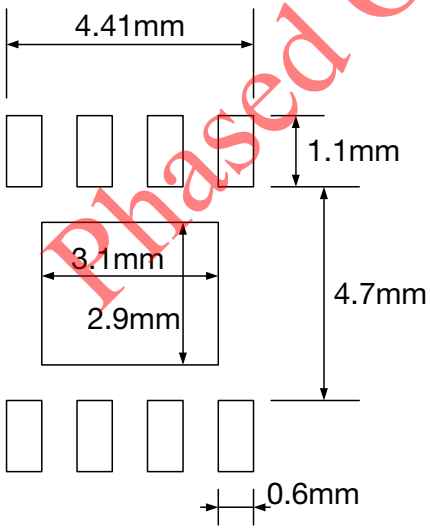
This IC's GND pin and Heat Spreader Bottom effectively radiate heat. By increasing these copper foil pattern area of PCB, Power dissipation improves. Please kindly design PCB pattern taking care of above features about power dissipation.



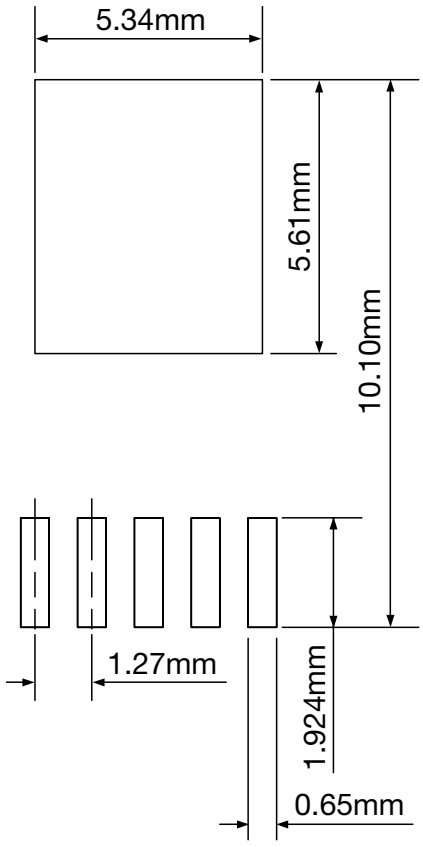
Phased Out Products

Land Pattern Recommendation

HSOP-8A



TO-252-5

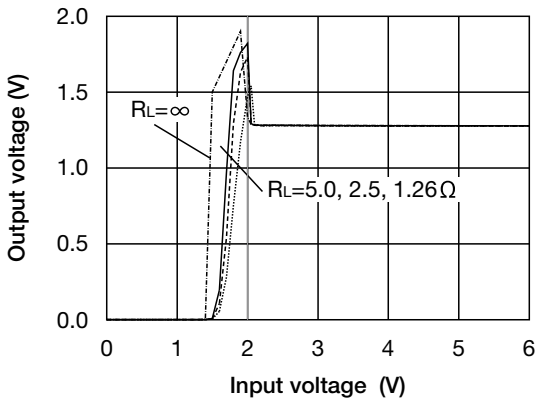


Note : These Dimensions are the reference values.

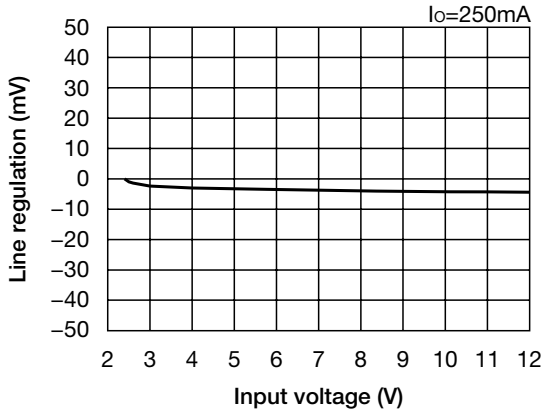
Characteristics (Vo=1.26V)

(Except where noted otherwise V_{IN}=2.4V, V_{Cont}=V_{IN}, Cin=1.0µF, Co=1.0µF, Cs=0.1µF, Ta=25°C)

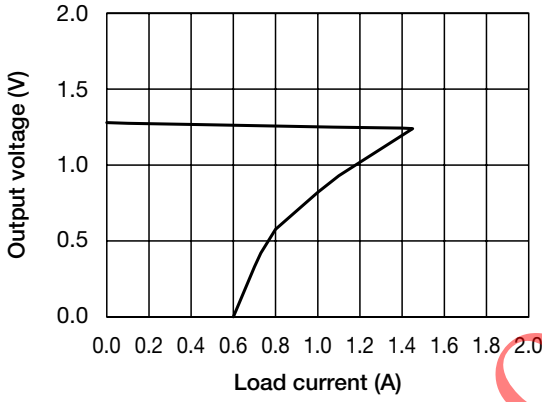
Output - Input voltage



Line regulation

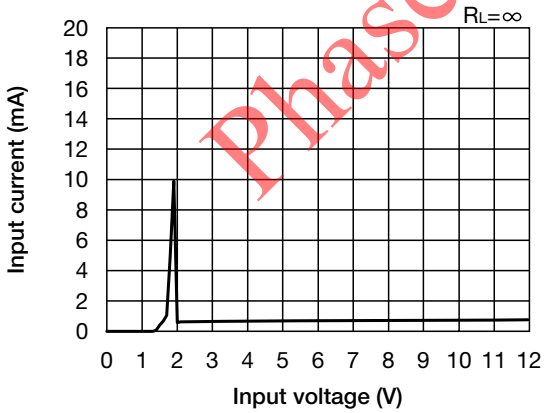


Load current - Output voltage



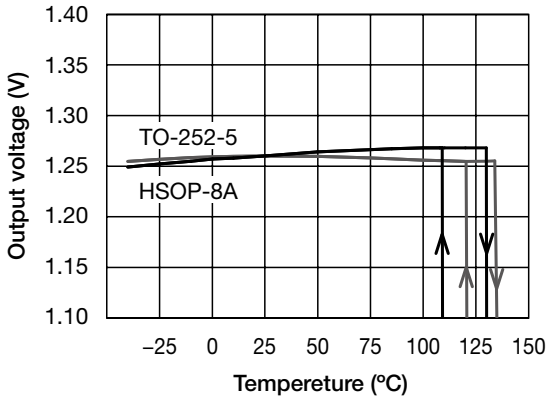
Load regulation

Input voltage - Input current

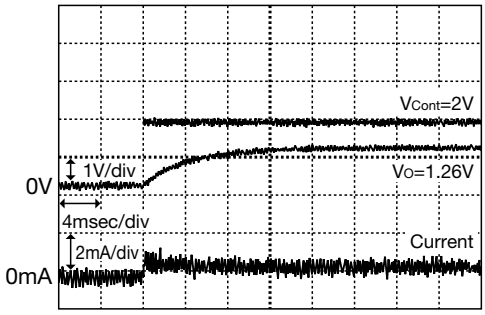


Phased Out Products

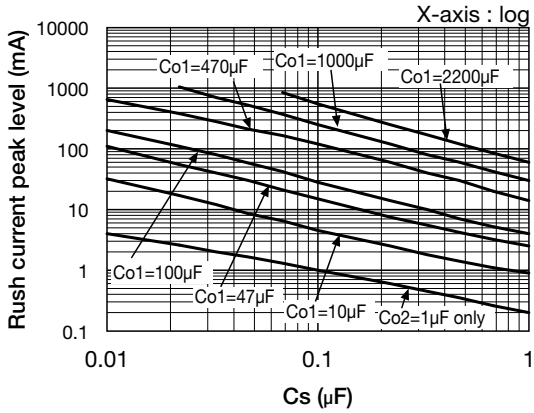
Output voltage – Temperature



Output rise time

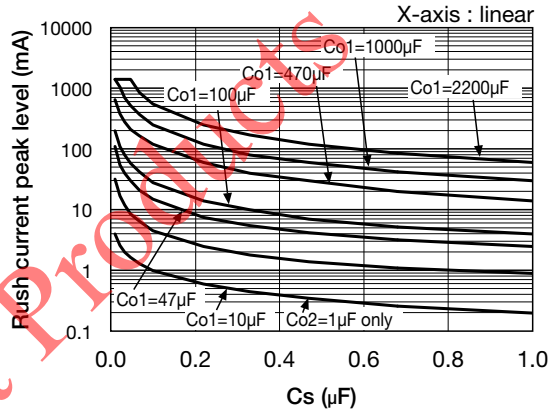


Rush current peak level



∗ Please refer to NOTE 7,8.

ESR Stable area

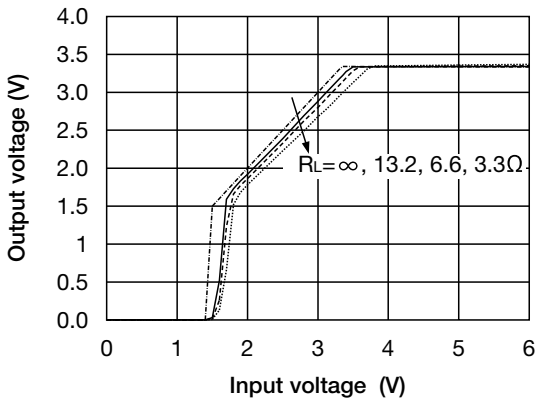


∗ Please refer to NOTE 7,8.

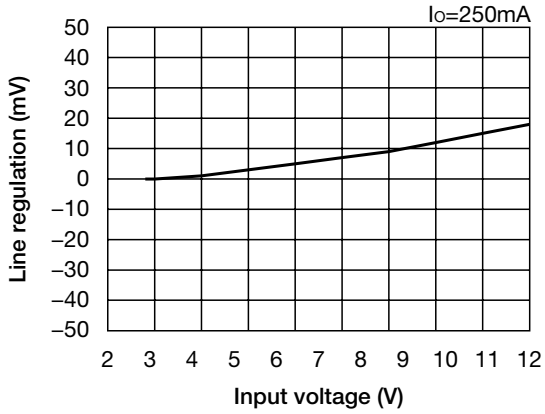
Phased Out Products

Characteristics (Vo=3.3V) (Except where noted otherwise V_{IN}=V_O+1V, V_{Cont}=V_{IN}, Cin=1.0μF, C_O=1.0μF, Cs=0.1μF, Ta=25°C)

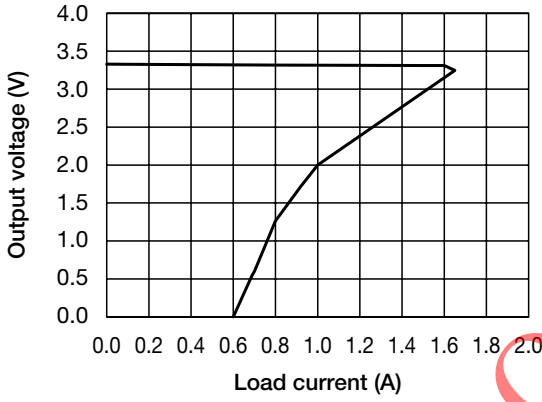
Output - Input voltage



Line regulation

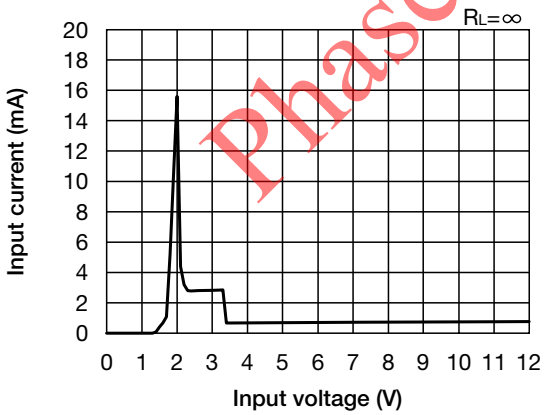


Load current - Output voltage

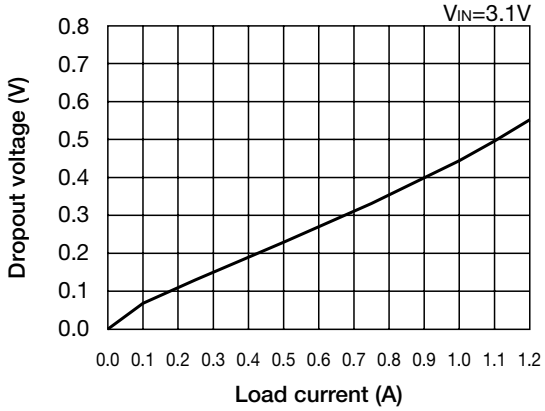


Load regulation

Input voltage - Input current

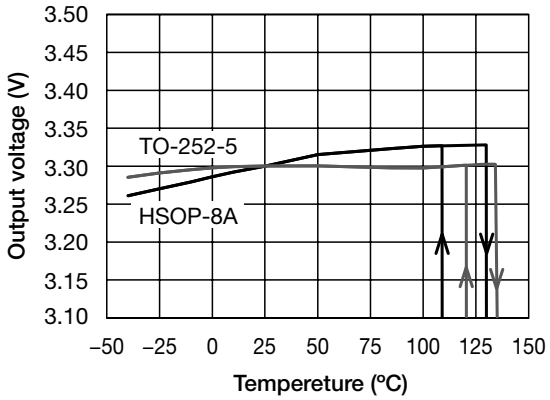


Dropout voltage

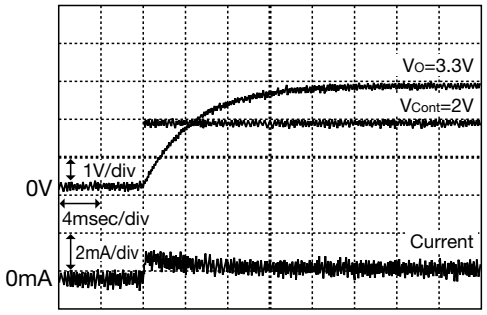


Phased Out Products

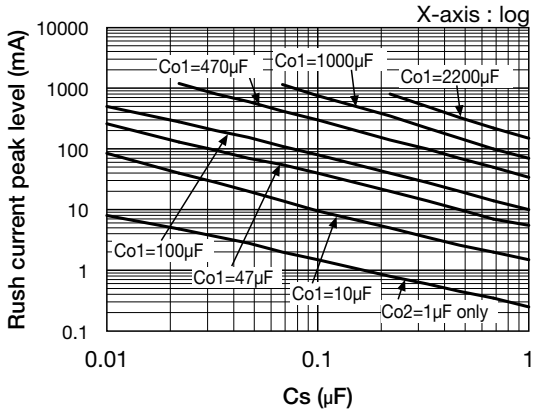
Output voltage – Temperature



Output rise time



Rush current peak level



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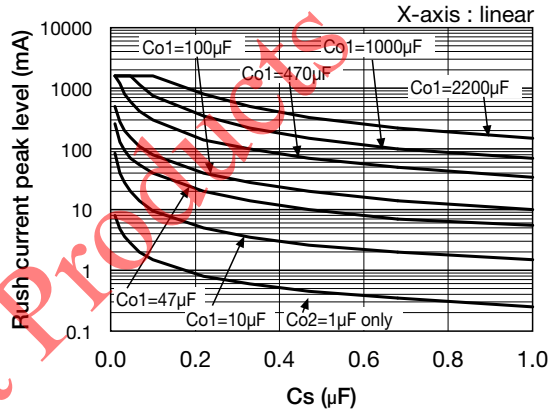
Rush current peak level

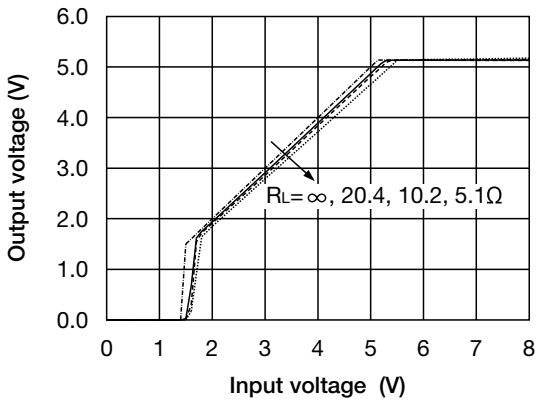


∗ Please refer to NOTE 7,8.

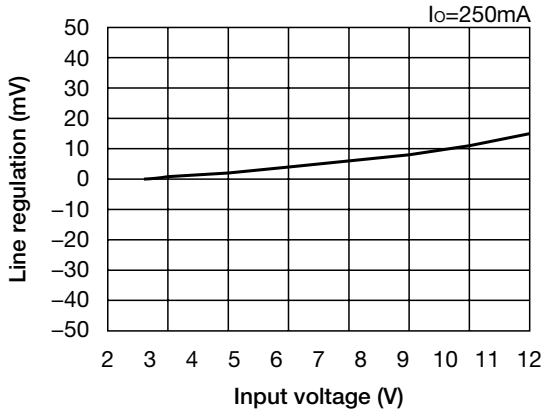
Phased Out Products

Characteristics (Vo=5.1V) (Except where noted otherwise V_{IN}=V_O+1V, V_{Cont}=V_{IN}, Cin=1.0μF, C_O=1.0μF, Cs=0.1μF, Ta=25°C)

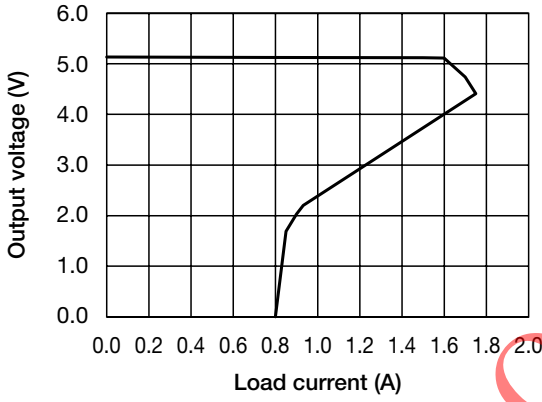
Output - Input voltage



Line regulation

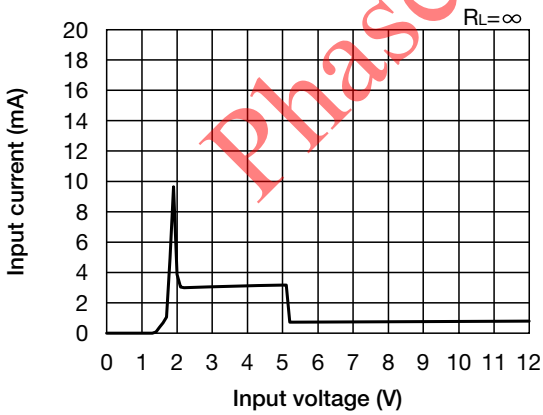


Load current - Output voltage

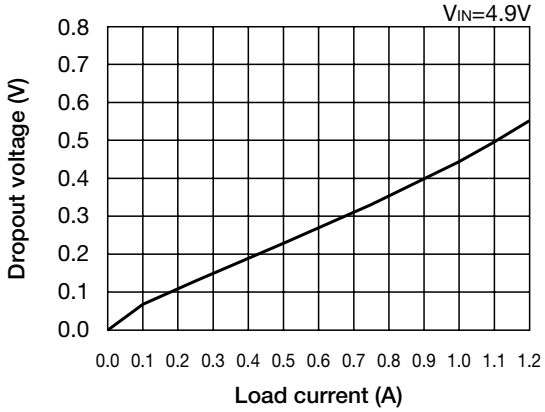


Load regulation

Input voltage - Input current

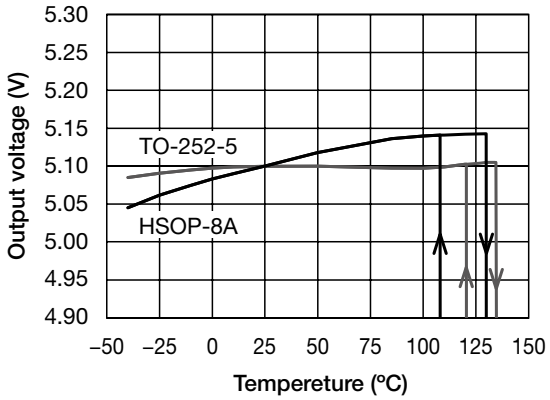


Dropout voltage

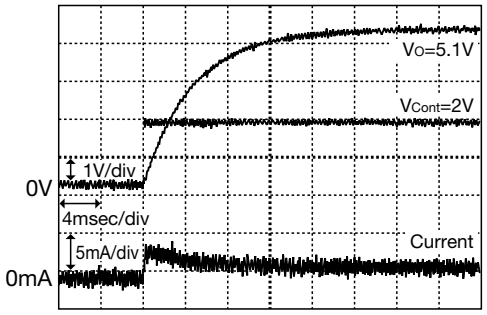


Phased Out Products

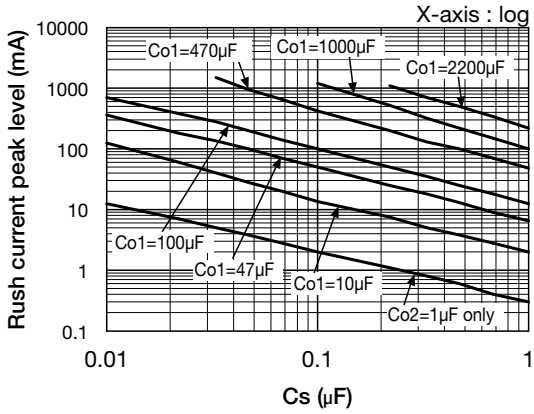
Output voltage – Temperature



Output rise time

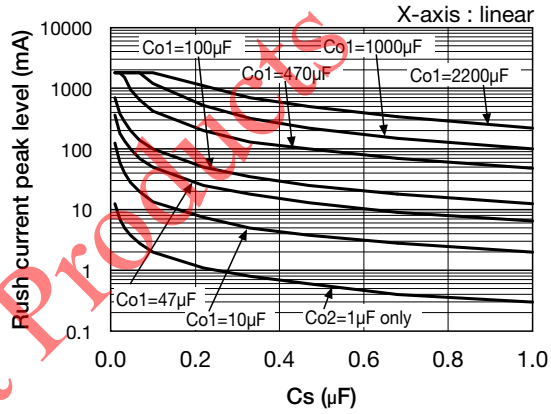


Rush current peak level



∗ Please refer to NOTE 7,8.

Rush current peak level



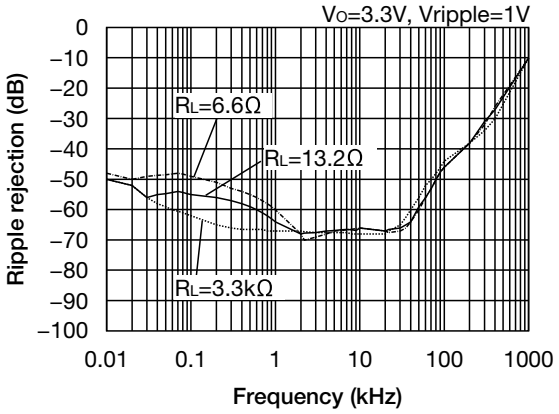
∗ Please refer to NOTE 7,8.

Phased Out Products

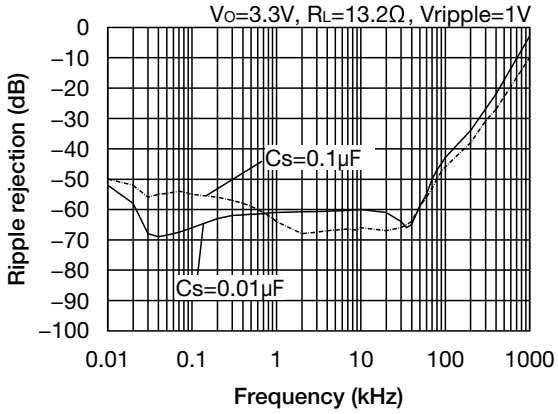
Characteristics

(Except where noted otherwise $V_{IN}=V_O+1V$, $V_{Cont}=V_{IN}$, Cin=1.0μF, C_O=1.0μF, Cs=0.1μF, Ta=25°C)

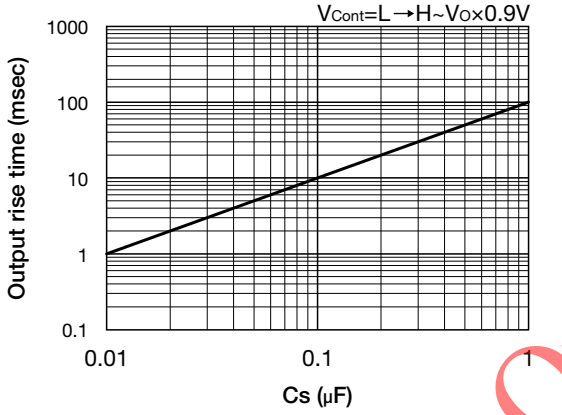
Ripple Rejection



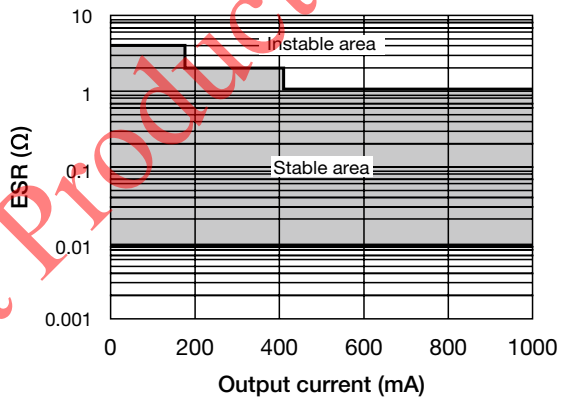
Ripple Rejection



Output rise time



Load regulation



Phased Out Products