



**Advanced Power
Electronics Corp.**

AP4433GM-HF

Halogen-Free Product

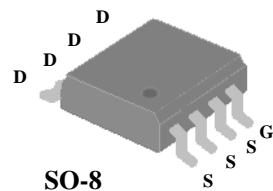
**P-CHANNEL ENHANCEMENT MODE
POWER MOSFET**

▼ Simple Drive Requirement

▼ Lower Gate Charge

▼ Fast Switching Characteristic

▼ RoHS Compliant & Halogen-Free

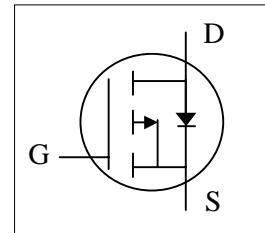


BV_{DSS}	-30V
$R_{DS(ON)}$	30mΩ
I_D	-7.4A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The SO-8 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	+20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current ³ , $V_{GS} @ 10V$	-7.4	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current ³ , $V_{GS} @ 10V$	-6	A
I_{DM}	Pulsed Drain Current ¹	-30	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation	2.5	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient ³	50	°C/W



Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-6\text{A}$	-	23.3	30	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-4\text{A}$	-	38	50	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1	-1.5	-3	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-6\text{A}$	-	12	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	-10	uA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge	$I_{\text{D}}=-6\text{A}$	-	8	12.8	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-15\text{V}$	-	2	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=-4.5\text{V}$	-	4	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=-15\text{V}$	-	7.5	-	ns
t_r	Rise Time	$I_{\text{D}}=-1\text{A}$	-	7	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_{\text{G}}=3.3\Omega$	-	23	-	ns
t_f	Fall Time	$V_{\text{GS}}=-10\text{V}$	-	29	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	670	1070	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=-15\text{V}$	-	140	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	120	-	pF
R_g	Gate Resistance	f=1.0MHz	-	8.5	17	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_{\text{S}}=-2.1\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	$I_{\text{S}}=-6\text{A}$, $V_{\text{GS}}=0\text{V}$,	-	17	-	ns
			-	8	-	nC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in² copper pad of FR4 board ; 125 °C/W when mounted on Min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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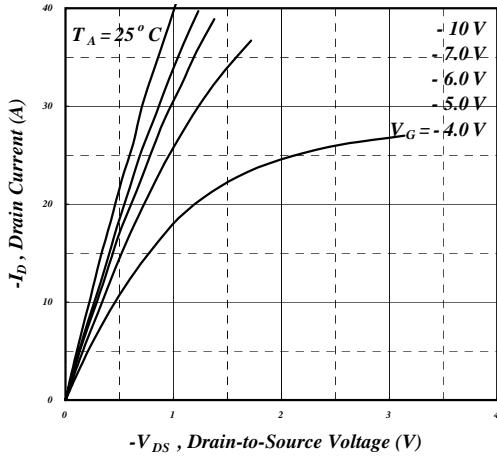


Fig 1. Typical Output Characteristics

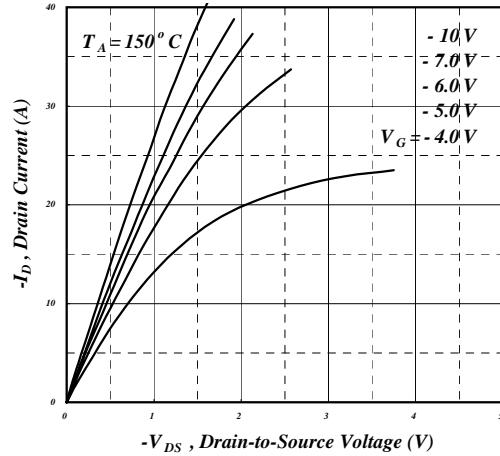


Fig 2. Typical Output Characteristics

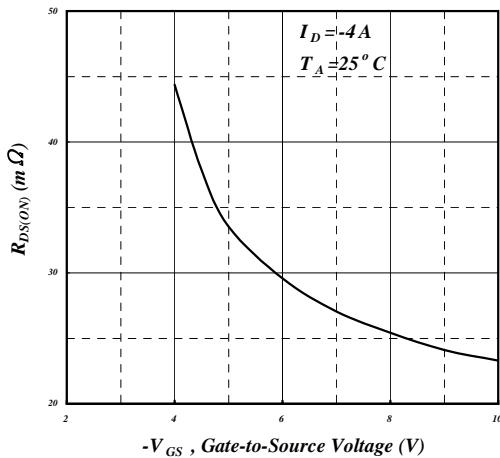


Fig 3. On-Resistance v.s. Gate Voltage

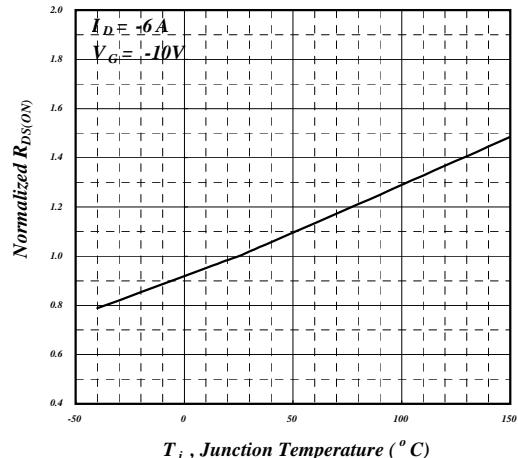


Fig 4. Normalized On-Resistance v.s. Junction Temperature

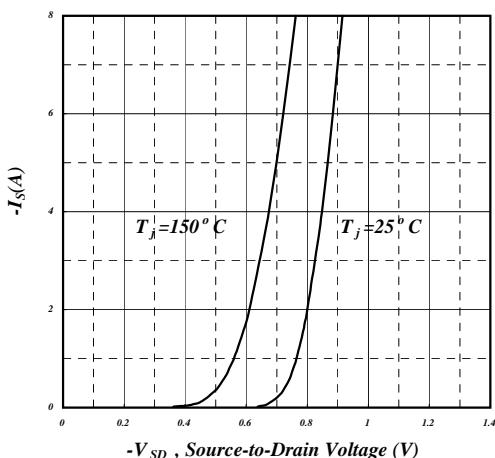


Fig 5. Forward Characteristic of Reverse Diode

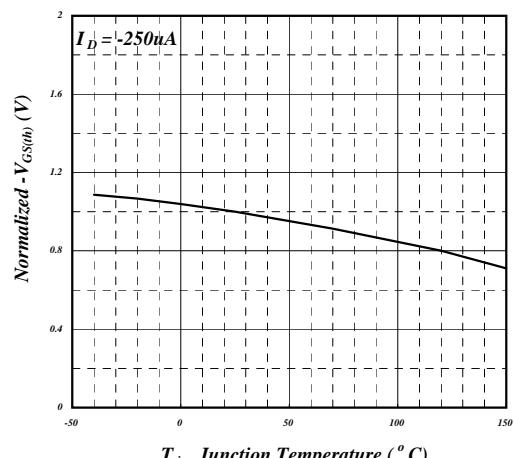


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

