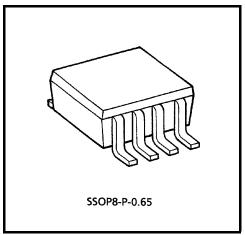
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA75W01FU

### **Dual Operational Amplifier**

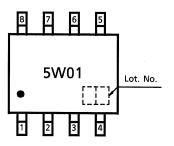
### Features

- In the linear mode the input common mode voltage range includes ground.
- The internally compensated operational amplifier is small package.
- Low power dissipation and power drain suitable for battery operation.
- Differential input voltage range equal to the power supply voltage.
- Large output voltage swing : 0V<sub>DC</sub> to 3.4V<sub>DC</sub> (V<sub>CC</sub> = 5V<sub>DC</sub>)
- Wide power supply voltage range and single power supply is possible.
- Single supply 3V<sub>DC</sub> to 12V<sub>DC</sub> or dual supplies ± 1.5V<sub>DC</sub> to ± 6V<sub>DC</sub>.

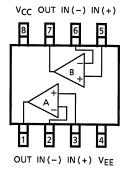


### Weight: 0.021g (typ.)

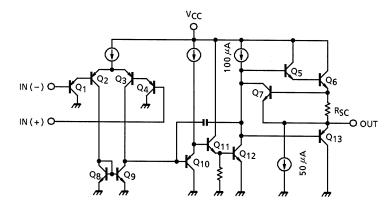
### Marking (Top View)



### Pin Connection (Top View)



### **Equivalent Circuit**



### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	$V_{CC}, V_{EE}$	±6 or 12	V
Differential input voltage	DVIN	±12	V
Input voltage	V <sub>IN</sub>	-0.3 ~V <sub>CC</sub>	V
Power dissipation	PD	250	mW
Operating temperature	T <sub>opr</sub>	-40~85	°C
Storage temperature	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### Test **Test Condition** Characteristic Symbol Min Тур. Max Unit Circuit Input offset voltage Vio 1 R<sub>q</sub>≤10kΩ 2 7 mV Input offset current 2 \_\_\_\_ 5 50 lio \_ 2 250 Input bias current Ιį \_ \_\_\_\_ 45 nA 3 \_ Common mode input voltage **CMVIN** 0 \_\_\_\_ V<sub>CC</sub>-1.5 4 Supply current 0.7 1.2 mΑ Icc \_ \_\_\_\_ Voltage gain Gv R<sub>L</sub>≥2kΩ 86 100 \_\_\_\_ \_ Maximum output voltage swing V<sub>op-p</sub> 5 $R_L=2k\Omega$ 0 3.4 \_\_\_\_\_ Common mode rejection ratio CMRR 3 \_ 65 85 \_ Supply voltage rejection ratio **SVRR** $R_{a}=10k\Omega$ 65 100 \_\_\_\_ Source current Isource 6 IN (-) = 0V, IN (+) = 1V 20 40 mΑ 7 IN (-) = 1V, IN (+) = 0V Sink current 10 20 mΑ Isink \_ 0.3 MHz Unity gain cross frequency $\mathsf{f}_\mathsf{T}$ \_\_\_\_ \_\_\_\_ \_ \_

### Electrical Characteristics (V<sub>CC</sub> = 5V, V<sub>EE</sub> = GND, Ta = 25°C)

nA

V

dB

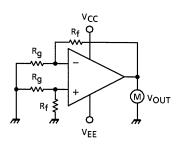
V

dB

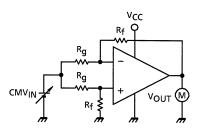
dB

### Test Circuit

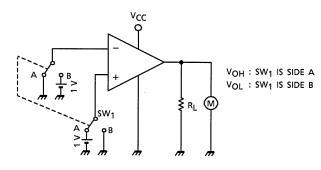
(1) V<sub>IO</sub>



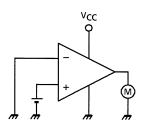
### (3) CMV<sub>IN</sub>, CMRR



(5) V<sub>OP-P</sub>



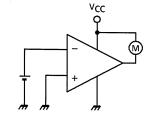
(6) I<sub>source</sub>



(7) I<sub>sink</sub>

(2) I<sub>I</sub>, I<sub>IO</sub>

(4) I<sub>CC</sub>



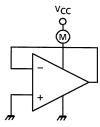
.

|IO = |I|(+) - |I(-)|

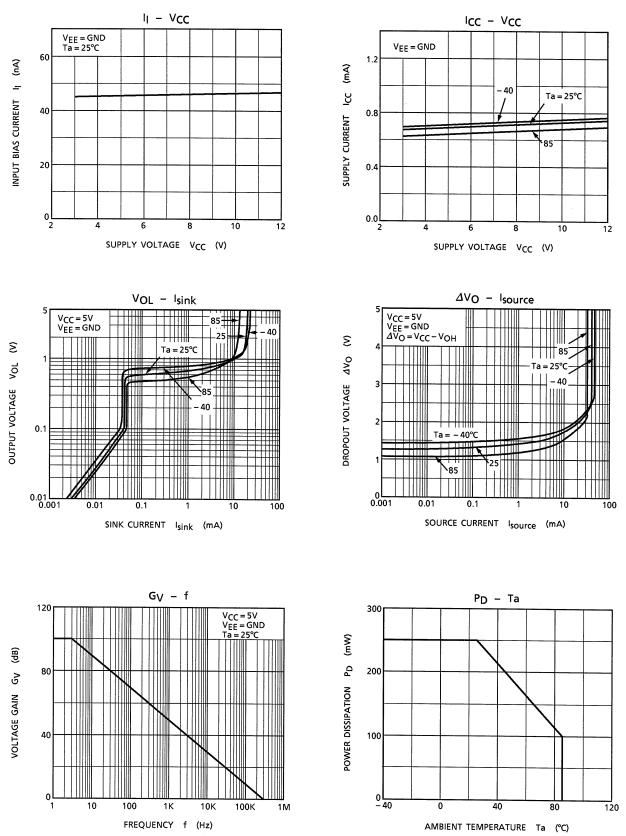
4(+)

(M)

4(



Vcc

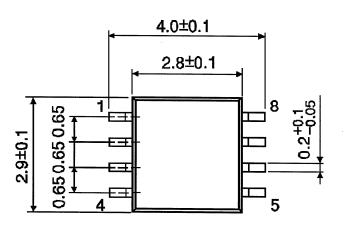


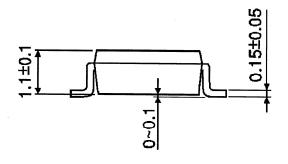
### Package Dimensions

SSOP8-P-0.65

Unit: mm

TA75W01FU





Weight: 0.021g (typ.)

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