



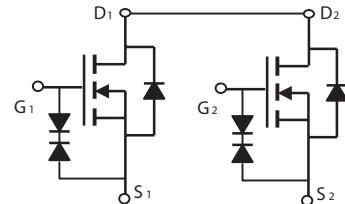
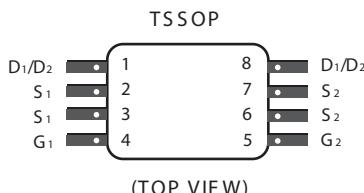
## Dual N-Channel Enhancement Mode Field Effect Transistor

### PRODUCT SUMMARY

$V_{DSS}$	$I_D$	$R_{DS(ON)}$ (mΩ) Max
20V	10A	13.5 @ $V_{GS} = 4.0V$ 18 @ $V_{GS} = 2.5V$

### FEATURES

- Super high dense cell design for low  $R_{DS(ON)}$ .
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous <sup>a</sup> @ T <sub>J</sub> =25°C -Pulsed <sup>b</sup>	$I_D$	10	A
	$I_{DM}$	40	A
Drain-Source Diode Forward Current <sup>a</sup>	$I_S$	1.7	A
Maximum Power Dissipation <sup>a</sup>	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	$R_{\theta JA}$	85	°C/W
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# STG8211

ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ C$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 16V, V_{GS} = 0V$		1		$\mu A$
Gate-Body Leakage	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS} = 0V$			$\pm 10$	$\mu A$
<b>ON CHARACTERISTICS<sup>b</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.5	0.8	1.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 4V, I_D = 5A$		11	13.5	m ohm
		$V_{GS} = 2.5V, I_D = 3A$		13.5	18	m ohm
Forward Transconductance	$g_{FS}$	$V_{DS} = 5V, I_D = 5A$		22		S
<b>DYNAMIC CHARACTERISTICS<sup>c</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 8V, V_{GS} = 0V$ $f = 1.0MHz$		1815		pF
Output Capacitance	$C_{oss}$			406		pF
Reverse Transfer Capacitance	$C_{rss}$			255		pF
<b>SWITCHING CHARACTERISTICS<sup>c</sup></b>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD} = 10V,$ $I_D = 1A,$ $V_{GEN} = 4V,$ $R_L = 10 \text{ ohm}$ $R_{GEN} = 10 \text{ ohm}$		31		ns
Rise Time	$t_r$			62		ns
Turn-Off Delay Time	$t_{D(OFF)}$			96		ns
Fall Time	$t_f$			40		ns
Total Gate Charge	$Q_g$	$V_{DS} = 10V, I_D = 5A,$ $V_{GS} = 4V$		19		nC
Gate-Source Charge	$Q_{gs}$			3.5		nC
Gate-Drain Charge	$Q_{gd}$			6.7		nC

# STG8211

## ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>DRAIN-SOURCE DIODE CHARACTERISTICS<sup>b</sup></b>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_S = 1.7A$		0.75	1.2	V

### Notes

a. Surface Mounted on FR4 Board,  $t \leq 10\text{ sec}$ .

b. Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

c. Guaranteed by design, not subject to production testing.

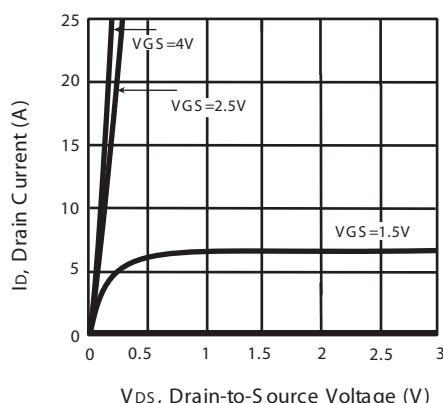


Figure 1. Output Characteristics

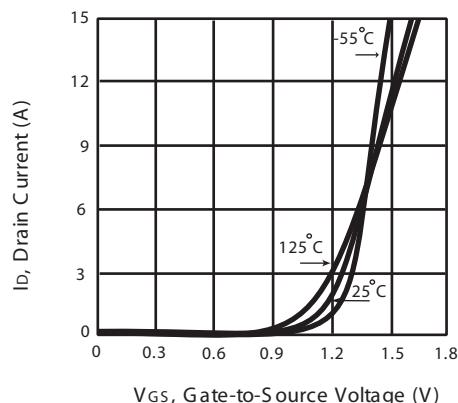


Figure 2. Transfer Characteristics

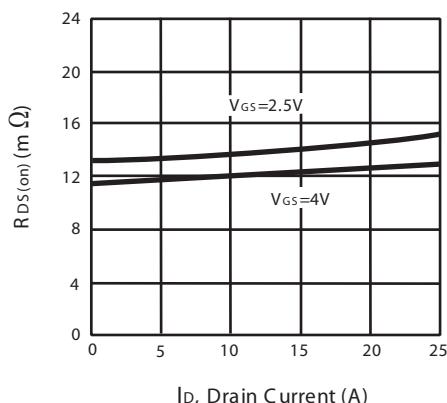


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

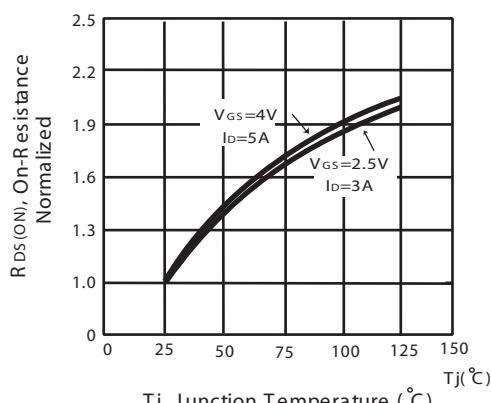


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STG8211

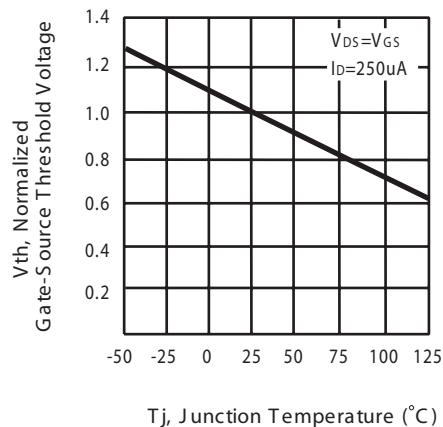


Figure 5. Gate Threshold Variation with Temperature

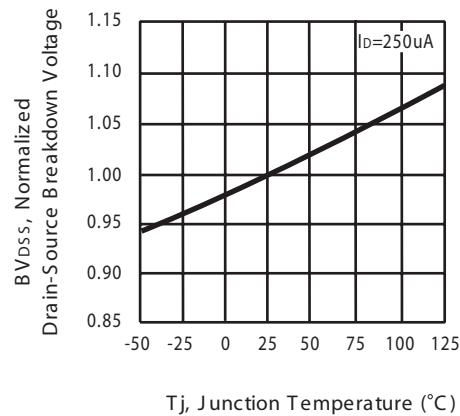


Figure 6. Breakdown Voltage Variation with Temperature

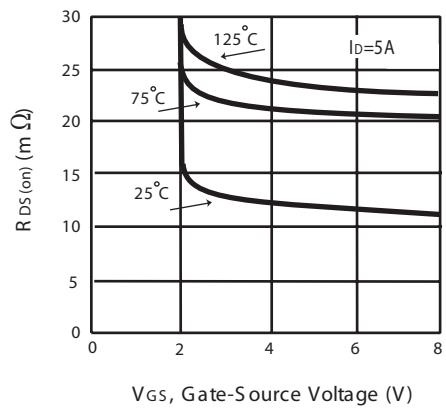


Figure 7. On-Resistance vs. Gate-Source Voltage

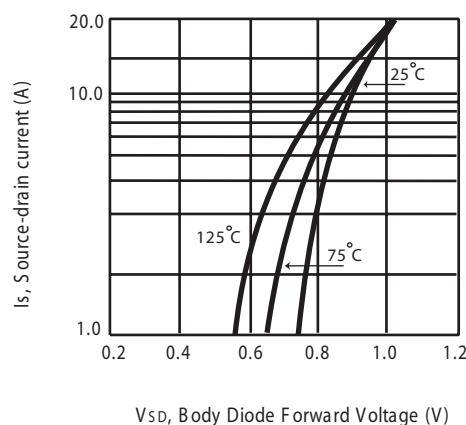


Figure 8. Body Diode Forward Voltage Variation with Source Current

# STG8211

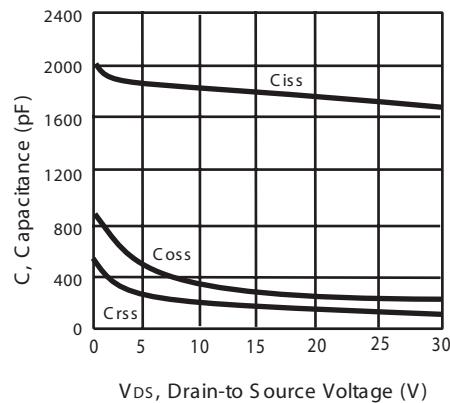


Figure 9. Capacitance

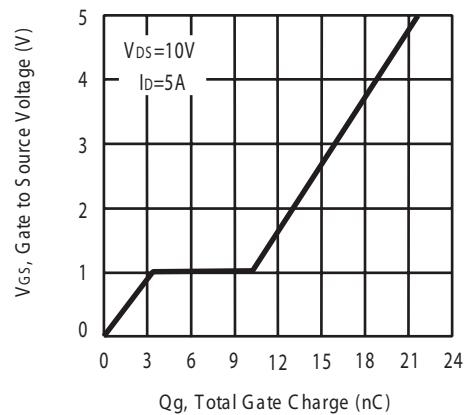


Figure 10. Gate Charge

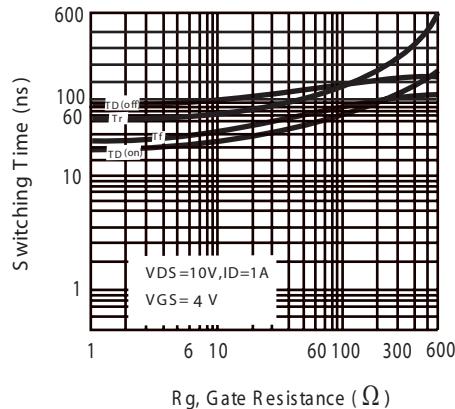


Figure 11. switching characteristics

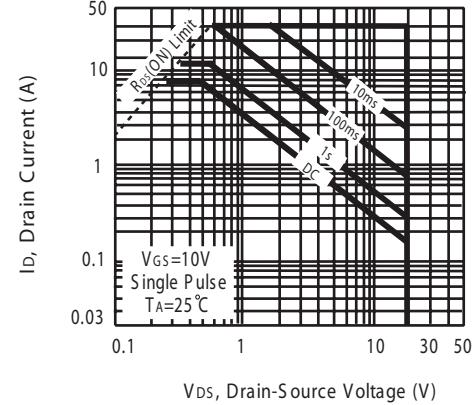


Figure 12. Maximum Safe Operating Area

# STG8211

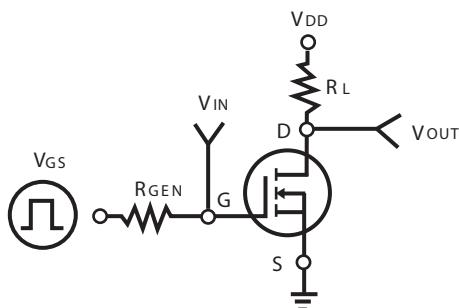


Figure 13. Switching Test Circuit

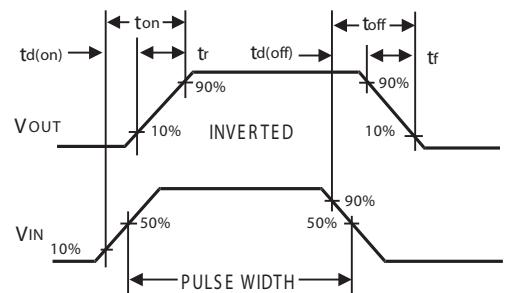


Figure 14. Switching Waveforms

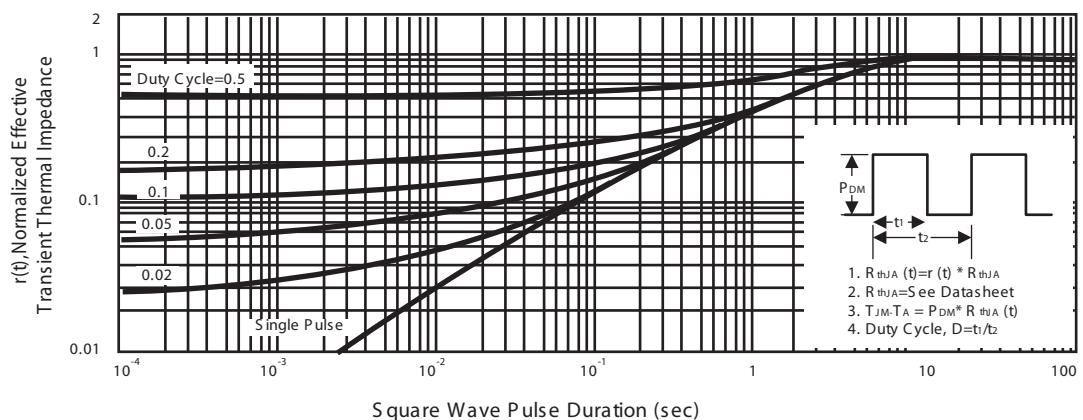
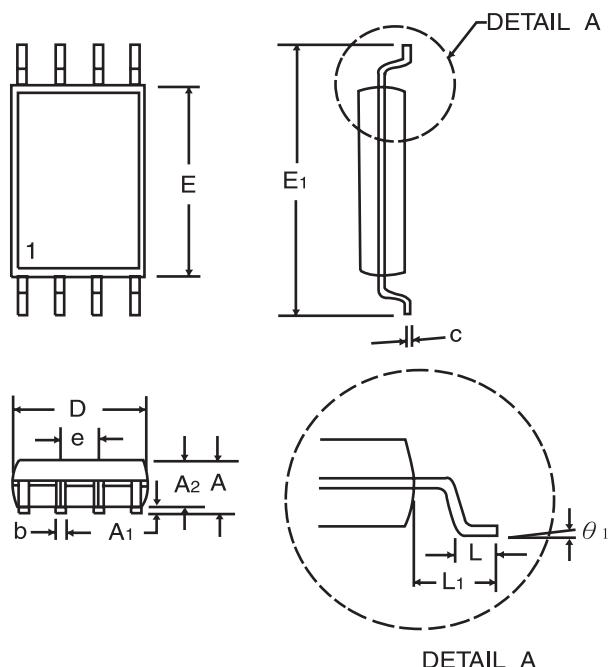


Figure 15. Normalized Thermal Transient Impedance Curve

# STG8211

## PACKAGE OUTLINE DIMENSIONS

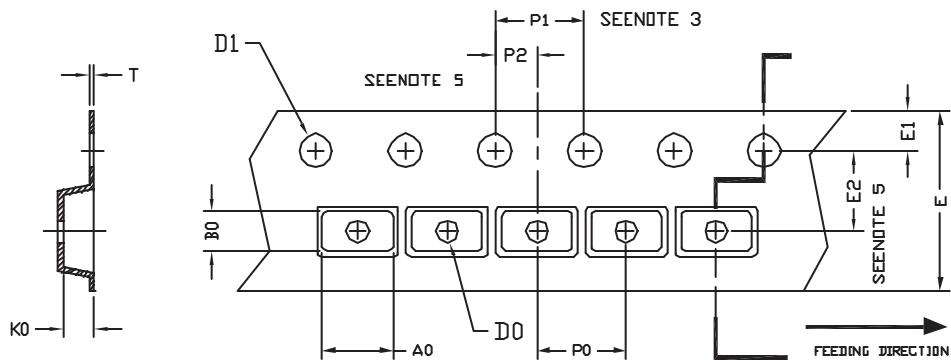
### TSSOP-8



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.05	1.20	0.041	0.047
A1	0.05	0.15	0.002	0.006
A2	-	1.05	-	0.041
b	0.20	0.28	0.008	0.011
c	0.127		0.005	
D-8	2.90	3.10	0.114	0.122
E	4.30	4.50	0.169	0.177
E1	6.20	6.60	0.244	0.260
e	0.65BSC		0.025BSC	
L	0.50	0.70	0.020	0.028
L1	1.00		0.039	
$\theta_1$	0°	8°	0°	8°

## TSSOP-8 Tape and Reel Data

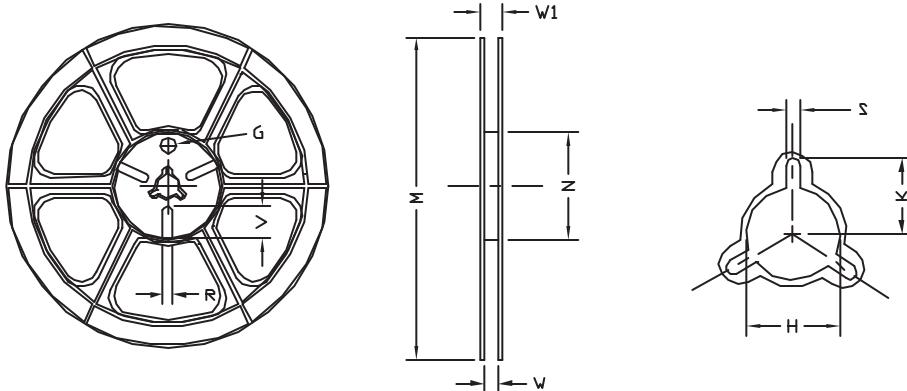
### TSSOP-8 Carrier Tape



UNIT : mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
TSSOP 8	6.08	4.40	1.60	$\phi 1.50$ + 0.1 - 0.0	$\phi 1.50$ + 0.1 - 0.0	12.00	1.75	5.50 $\pm 0.05$	8.00	4.00	2.00 $\pm 0.05$	0.30 $\pm 0.05$

### TSSOP-8 Reel



UNIT : mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330	100	12.5	16.0	$\phi 13.0$ + 0.5 - 0.2	10.6	2.0 $\pm 0.5$	---	---	---