

Programmable VCXO

• 9.6 x 11.4 SMD

Series **CVPP**

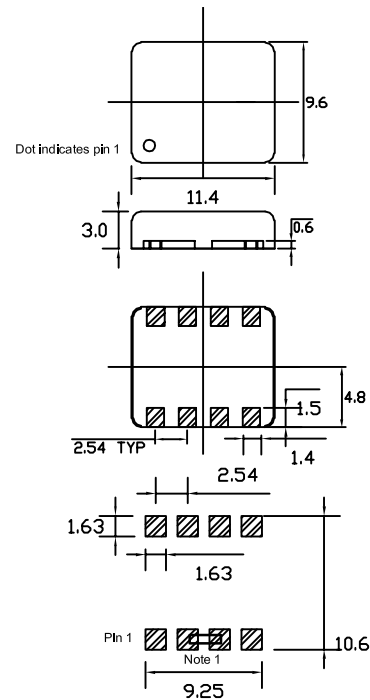


Part Numbering Example: CVPPZ - 100.0

| | | |
|---------------|-----------------------|------------------|
| CVPP | Z | 100.0 |
| SERIES | ADDED FEATURES | FREQUENCY |
| Blank | = Bulk | 1.0 – 200.0 |
| T | = Tube | |
| Z | = Tape/Reel | |

| Specifications: | Min | Typ | Max | Unit |
|---|-------|-----|-------|-------|
| Frequency Range: | 1.0 | | 200.0 | MHz |
| Stability Options | -50 | | 50 | ppm |
| Programmable Input Voltage | 3.135 | 3.3 | 3.465 | V |
| Operating Temperature Range Options: | -30 | | 75 | °C |
| Storage Temperature: | -55 | | 125 | °C |
| Aging (PPM/1st Year): Ta=25C, Vdd=3.3V | | | ± 5 | |
| Pull Range | -100 | | 100 | |
| Supply Current | | 40 | | mA |
| Frequency Tuning Range | | 74 | | ppm/V |
| Start-Up Time | | 3 | 10 | ms |
| Programmable Output Level: | CMOS | | | |

CVPP



Recommended solder pad layout
Note 1: Connect pin 2 to pin 3

PIN FUNCTION

- PIN 1 OE
- PIN 2 CONNECT TO PIN 3
- PIN 3 CONNECT TO PIN 2
- PIN 4 GND
- PIN 5 OUTPUT
- PIN 6 VCONTROL
- PIN 7 FACTORY USE (MAKE NO CONNECTION)
- PIN 8 VDD

Tristate internal pull up. Output active when high.

Notes: Recommended .01 µF bypass capacitor from Vcc to GND. Capacitor should be as close to oscillator as possible.

