

INTERNATIONAL RECTIFIER

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T-25-20

940A RMS Hockey Puk Thyristors**600PE SERIES****Description**

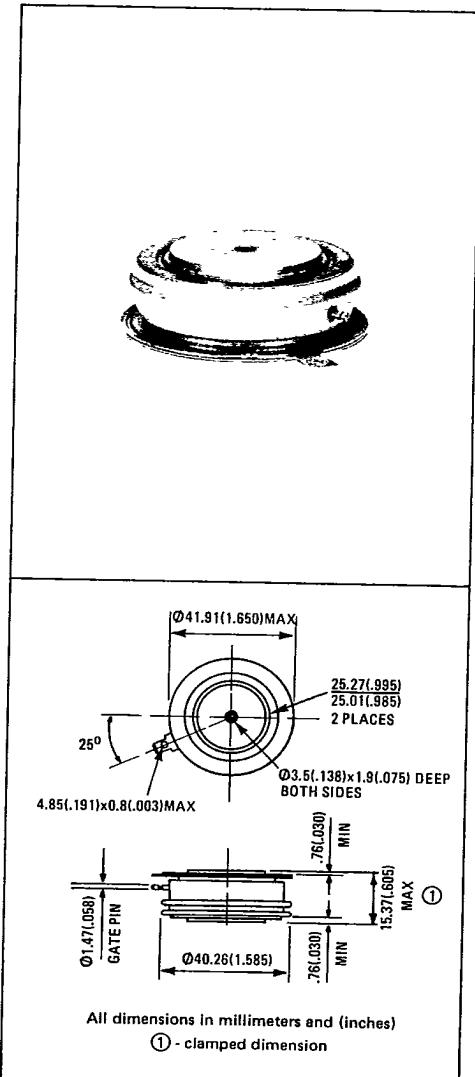
The 600PE series of converter type hockey puk thyristors use centre amplified gate junction technology. These devices with their high current capability and small package size are ideal for use in phase control applications in converters, battery chargers, regulated power supplies, lighting circuits and temperature and motor speed control circuits, where compactness is an advantage.

Features

- Centre Amplified Gate
- High di/dt and dv/dt capabilities
- High surge capabilities
- Available up to 1800V V_{RRM} , V_{DRM}
- Fully characterised information

Major ratings and characteristics

	600PE...	Units
$I_T(AV)$	600	A
$I_T(RMS)$	940	A
I_{TSM} 50Hz	6730	A
I_{TSM} 60Hz	7040	A
I^2t 50Hz	226,000	A^2s
I^2t 60Hz	207,000	A^2s
$I^2\sqrt{t}$	3 200 000	$A^2\sqrt{s}$
V_{RRM}	800 to 1800	V
T_J	-40 to 125	$^{\circ}C$



ELECTRICAL SPECIFICATIONS

Forward conduction

	600PE...	Units	Conditions		
I _{T(AV)} Average on state current	600	A	180° conduction, half sine wave, double side cooled, T _c = 70°C		
I _{T(RMS)} Nominal continuous RMS on-state current	940	A			
I _{TRM} Maximum peak repetitive on-state current	5540	A	30° sinusoidal conduction, T _c = 70°C		
Mounting force ± 10%	8920(2000)	N(lbf)			
I _{TSM} Maximum peak, one cycle non repetitive on-state current	8000	6000	A	t = 10ms	No voltage reapplied
	8370	6300	A	t = 8.3ms	Sinusoidal half wave
	6730	5050	A	t = 10ms	Initial T _j = 125°C
	7040	5300	A	t = 8.3ms	100% V _{RRM} reapplied
I ² t Maximum I ² t for fusing	320	kA ² s	t = 10ms	No voltage reapplied	
	292	165	kA ² s	t = 8.3ms	
	226	127	kA ² s	t = 10ms	Initial T _j = 125°C
	207	116	kA ² s	t = 8.3ms	100% V _{RRM} reapplied
I _V t Maximum I _V t for fusing	3200	1800	kA ² s	t = 0.1 - 10ms, no voltage reapplied	
V _{TM} Maximum peak on state voltage	190	V	T _j = 25°C, 180° conduction, I _{TM} = T x I _{T(AV)} (1885 A peak)		
dv/dt Maximum non repetitive rate of rise of turned on current	800	A/μs	JEDEC STD. RS-397, 6.2.2.6.; T _c = 125°C, V _{DM} = V _{DRM} , I _{TM} = 1600A gate source 20V open circuit 20ΩL _r = 0.5μs, I _p = 20μs		
I _H Maximum holding current	250	mA	T _j = 25°C, anode supply = 6V, resistive load, gate open circuit		
I _L Maximum latching current	500	mA	T _j = 25°C, anode supply = 6V, resistive load		

Triggering

P _{GM} Maximum peak gate power	10	W	t _p ≤ 5ms	
P _{G(AV)} Maximum average gate power	2	W	T _j = 25 to 125°C	
I _{GM} Maximum peak gate current	3	A		
V _{GM} Maximum peak gate voltage	20	V		
-V _{GM} Maximum peak negative gate voltage	5	V		
V _{GT} Maximum gate voltage required to trigger	3.0	V	T _j = -40°C	Anode supply = 6V resistive load
	2.5	V	T _j = 25°C	
	1.7	V	T _j = 125°C	
I _{GT} Maximum gate current required to trigger	300	mA	T _j = -40°C	Anode supply = 6V resistive load
	150	mA	T _j = 25°C	
	100	mA	T _j = 125°C	
V _{GD} Maximum gate voltage that will not trigger	0.2	V	T _j = 125°C, rated V _{DRM} applied	

Switching

t _d Maximum delay time	1.0	μs	T _j = 25°C, V _D = 0.8 V _{DRM} , I _{TM} = 500A, gate source 20V open circuit, R _{source} = 20Ω, resistive load, t _f (pulse rise time) 0.5/1μs, t _p = 20μs
t _q Typical turn off time	220	μs	T _j = 125°C, I _{TM} = 500A for 200/μs, V _R = 50V reapplied dv/dt = 20V/1μs linear to 0.8 V _{DRM} , di/dt = -25 A/μs
Q _{tr} Typical stored charge	470	μC	T _j = 125°C, I _{TM} = 400A, -di/dt = 20 A/μs

Blocking

dv/dt Minimum critical rate of rise of off state voltage	500	V/μs	T _j = 125°C, linear to 0.8 V _{DRM} , gate open circuit
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Voltage ratings

Part number	V _{RRM} , maximum repetitive peak reverse voltage V _g < 0 ①	V _{RSM} , maximum non repetitive peak reverse voltage V	V _{DRM} , maximum repetitive peak off state voltage, gate open circuit ①	I _{RM} , I _{DRM} , maximum peak reverse and off-state leakage current at V _{RRM} , V _{DRM} T _j = 125°C, gate open circuit	mA
600PE80	800	900	800		30
600PE100	1000	1100	1000		30
600PE120	1200	1300	1200		30
600PE140	1400	1500	1400		30
600PE160	1600	1700	1600		30
600PE180	1800	1900	1800		30

① For V_{RRM}, V_{DRM} ≤ 1200V T_j = -40°C to 125°C
> 1200V T_j = 0°C to 125°C

For V_{RRM}, V_{DRM} > 1200V and T_j = -40°C to 0°C, derate V_{RRM}, V_{DRM} by 5%

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THERMAL AND MECHANICAL SPECIFICATIONS

	600PE...	Units	Conditions
T _J	Junction operating temperature range	-40 to 125	°C
T _{SIG}	Storage temperature range	-40 to 150	°C
R _{thJC}	Single side cooled	0.08	K/W
	Double side cooled	0.04	K/W
R _{thCS}	Maximum thermal resistance, one pole piece to one heat exchanger	0.04 (0.05)	K/W
		0.03 (0.04)	K/W
Mounting force ±10%	1000 (4460)	lbf (N)	Mounting surface smooth flat and greased (JEDEC STD RS-397, 7.9.4)
	2000 (8920)	lbf (N)	
W	Approximate weight	3	oz
		85	g

Fig. 1 — Current Ratings — sinusoidal waveforms, 50—400Hz

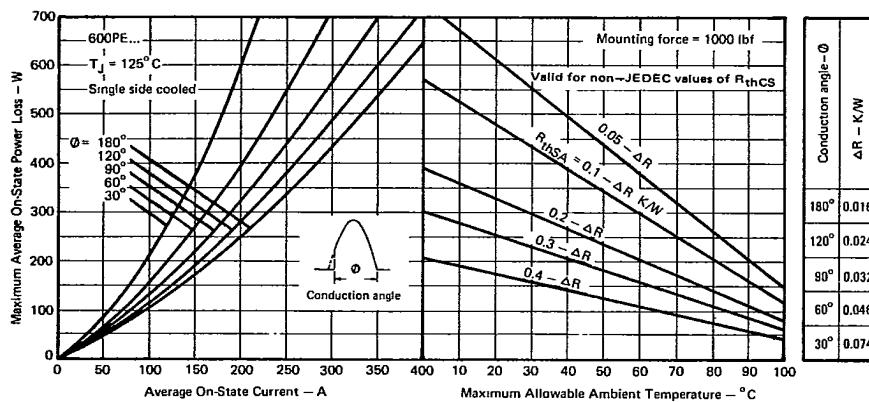
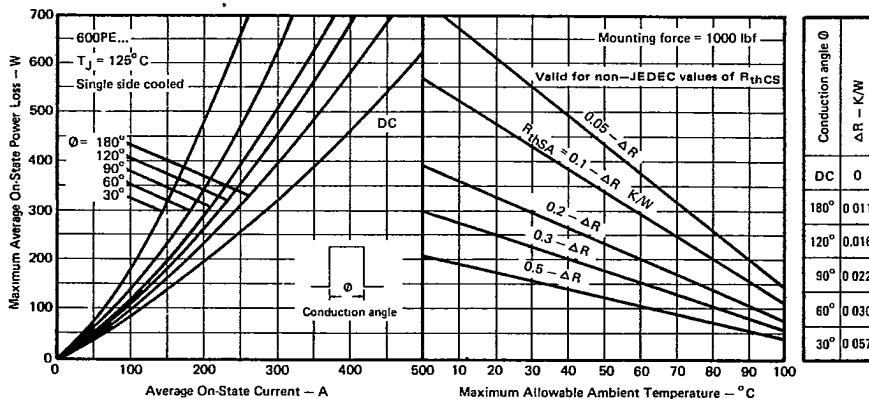


Fig. 2 — Current Ratings — rectangular waveforms, 50—400Hz



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Fig. 3 — Current Ratings — sinusoidal waveforms, 50—400Hz

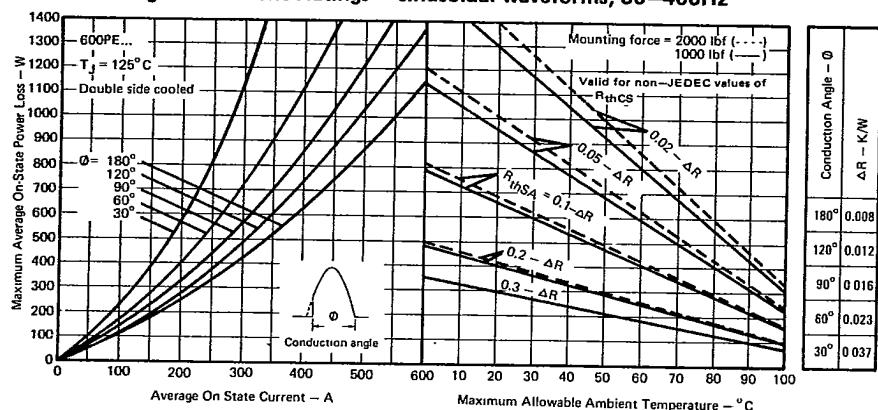


Fig. 4 — Current Ratings — rectangular waveforms, 50—400Hz

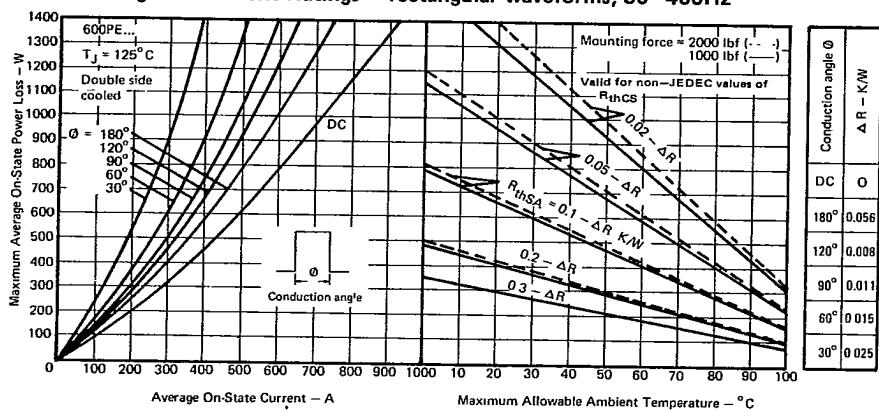


Fig. 5 — Case Temperature Ratings

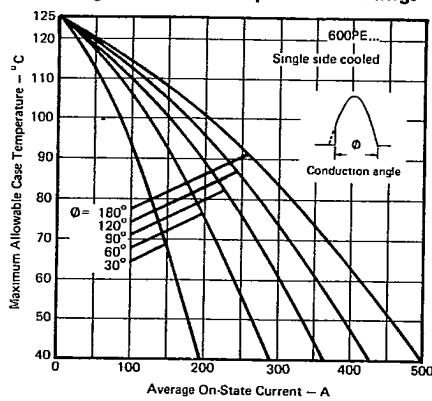
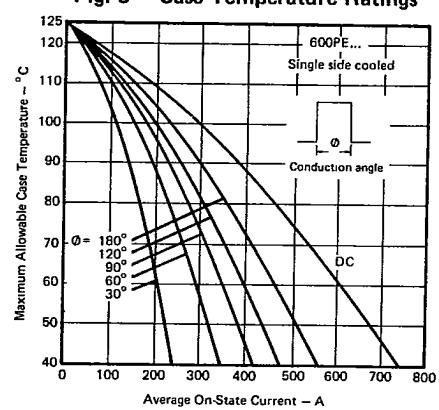


Fig. 6 — Case Temperature Ratings



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Fig. 7 – Case Temperature Ratings

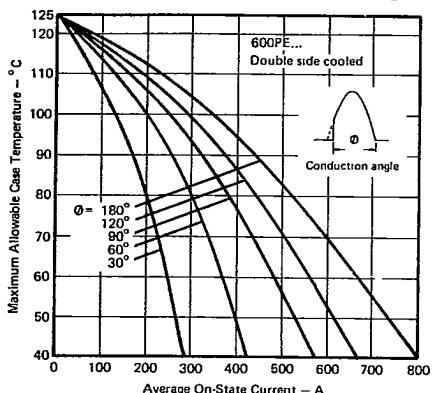


Fig. 9 – Power Loss Characteristics

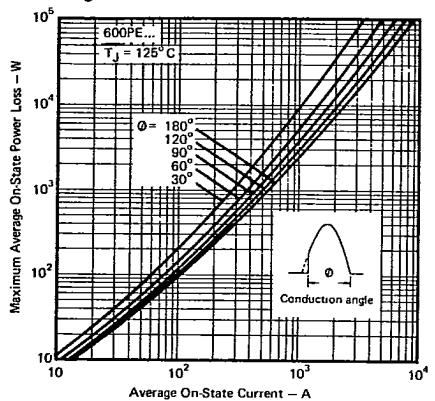


Fig. 11 – On-State Characteristics

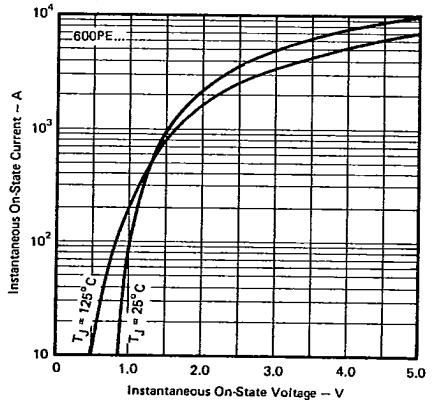


Fig. 8 – Case Temperature Ratings

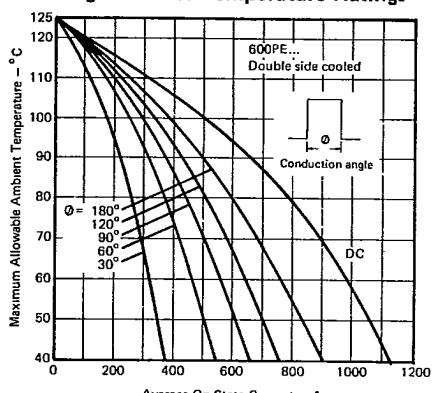


Fig. 10 – Power Loss Characteristics

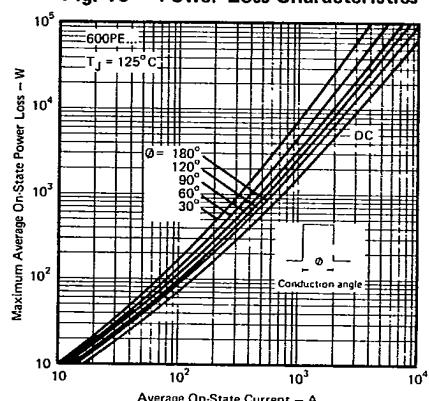
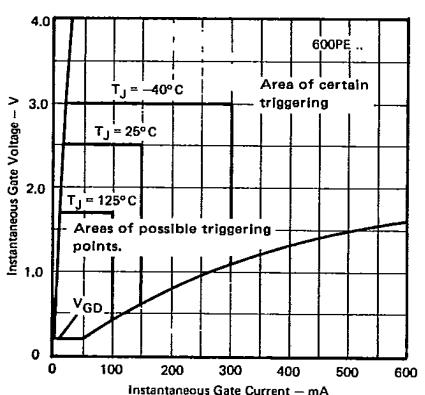


Fig. 12 – Gate Characteristics



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Fig. 13 – Transient Thermal Impedance

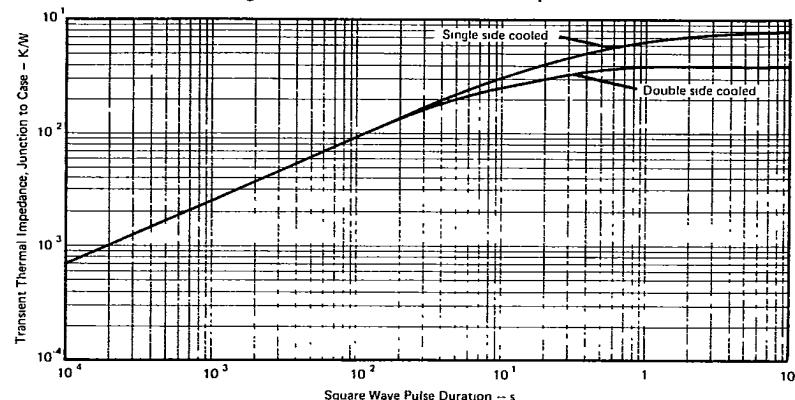


Fig. 14 – Non-Repetitive Surge Ratings

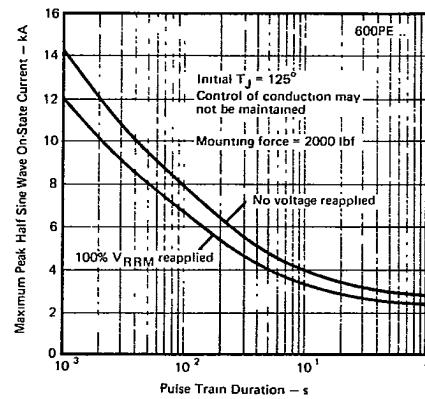
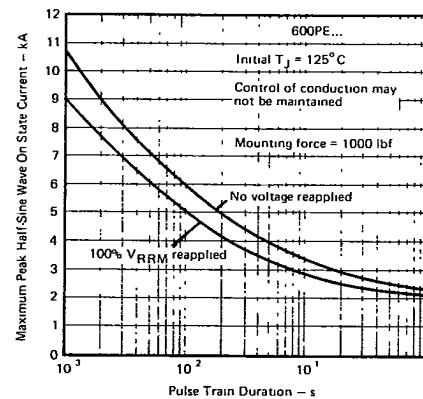


Fig. 15 – Non-Repetitive Surge Ratings



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In the interest of product improvement INTERNATIONAL RECTIFIER reserves the right to change specifications at any time without notice

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