N-Channel16 –V (D–S) MOSFET

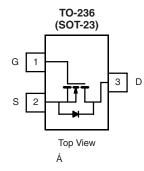
PRODUCT SUMMARY					
V	P		I _D (A)		
V _{(BR)DSS} Min (V)	r _{DS(on)} Max (Ω)	V _{GS(th)} (V)	DTS03K16	DTS03K16A	
16	1.0 @ V _{GS} = 10 V	1.0 to 3.0	0.42	0.64	
	1.4 @ V _{GS} = 4.5 V		0.35	0.53	

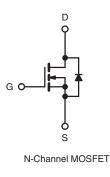
FEATURES

• TrenchFET® Power MOSFET

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays





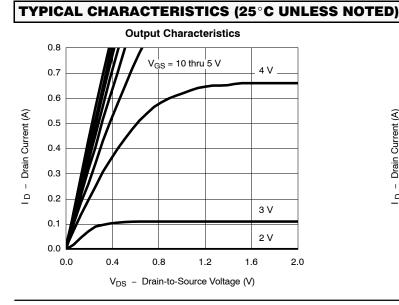
			Limit		Unit
Parameter	Symbol	DTS03K16	DTS03K16A		
Drain-Source Voltage		V _{DS}	16		v
Gate-Source Voltage		V _{GS}	±8		
	T _A = 25°C	- I _D	0.42	0.64	А
Continuous Drain Current ($T_J = 150^{\circ}C$)	T _A = 70°C		0.33	0.51	
Pulsed Drain Current ^a		I _{DM}	0.8	1.5	
	T _A = 25°C	P _D	0.35	0.8	w
Power Dissipation	T _A = 70°C		0.22	0.51	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	357	156	°C/M	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	–55 to 150		°C	

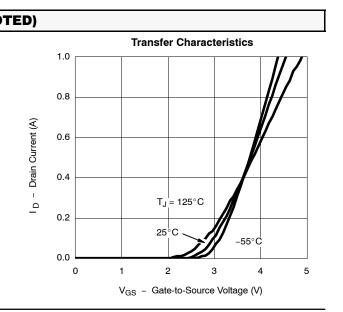
Notes a. Pulse width limited by maximum junction temperature.

Din-Tek

				Limits			
Parameter	Symbol Test Conditions		Min	Тур	Max	Unit	
Static					•	•	•
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 10 μ A		16			
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 0$.25 mA	1.0	2.0	3.0	V
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} =	±20 V			±100	nA
		V _{DS} = 16 V, V _{GS}	V_{DS} = 16 V, V_{GS} = 0 V			1	μA
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = 16 V, V_{GS} = 0 V, T_{J} = 55 °C				10	
		V _{DS} = 10 V, V _{GS} = 8'V''	DTS03K16	0.5			- A
On-State Drain Current ^a	I _{D(on)}		DTS03K16A	0.8			
Drain-Source On-Resistance ^a		$v_{GS} = 4.5 \text{ V}, \text{ I}_{\text{D}} = 0.1 \text{ A}$ $v_{GS} = 10 \text{ V}, \text{ I}_{\text{D}} = 0.3 \text{ A}$			0.8	1.4	Ω
	DS(on)				0.47	1.0	
Forward Transconductance ^a	9fs	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.3 \text{ A}$			550		mS
Diode Forward Voltage	V _{SD}	$I_{\rm S} = 0.3$ A, $V_{\rm GS} = 0$ V			0.85	1.2	V
Dynamic ^b							
Total Gate Charge	Qg	V_{DS} = 16 V, V_{GS} = 10 V I _D \cong 0.3 A			1000	1500	рС
Gate-Source Charge	Q _{gs}				205		
Gate-Drain Charge	Q _{gd}				200		1
Gate Resistance	Rg				48		Ω
	t _{d(on)}	$\begin{array}{l} V_{\text{DD}} = 15 \ V, R_{\text{L}} = 50 \ \Omega \\ I_{\text{D}} \cong 0.3 \ A, V_{\text{GEN}} = 10 \ V \\ R_{\text{G}} = 6 \ \Omega \end{array}$			4.5	8	ns
Turn-On Time	t _r				8	15	
Turn Off Time	t _{d(off)}				9	15	
Turn-Off Time	t _f				6.3	12	

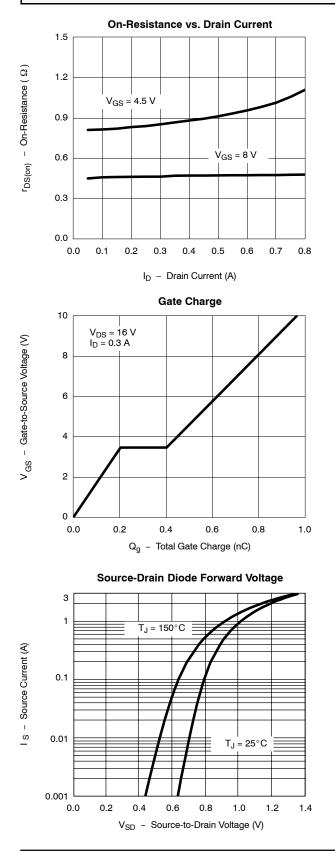
Notes a. Pulse test: PW \leq 300 μ s duty cycle \leq 2%. b. Guaranteed by design, not subject to production testing.

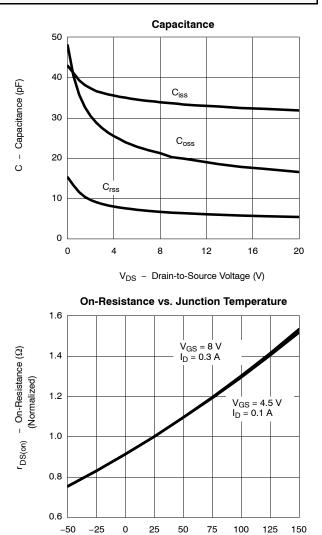




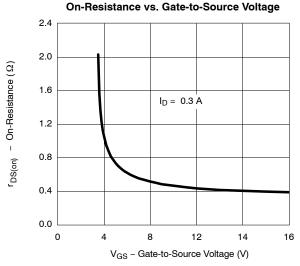


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



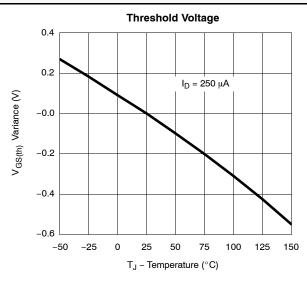


T_J – Junction Temperature (°C)

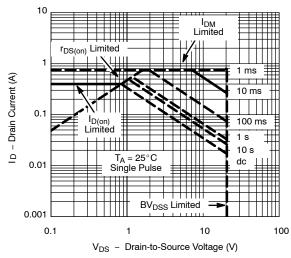


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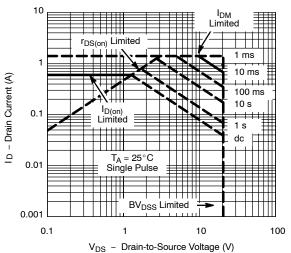
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

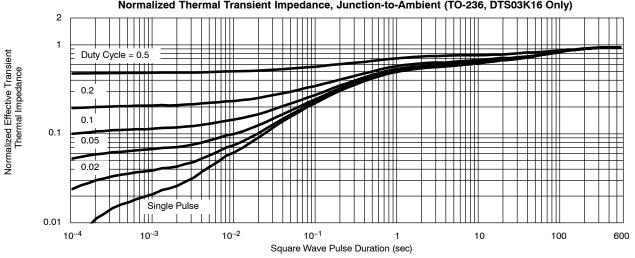


Safe Operating Area (TO-236, DTS03K16 Only)



Safe Operating Area (TO-226AA, DTS03K16A Only)

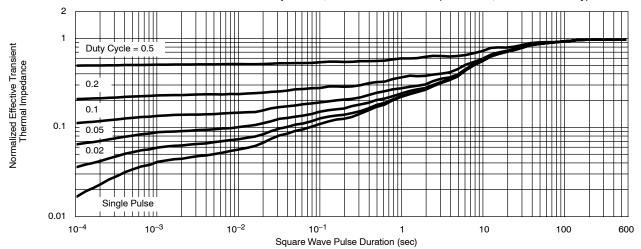




Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-236, DTS03K16 Only)



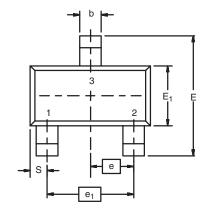
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

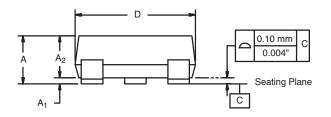


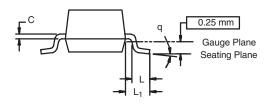
Normalized Thermal Transient Impedance, Junction-to-Ambient (TO-226AA, DTS03K16A Only)



SOT-23 (TO-236): 3-LEAD



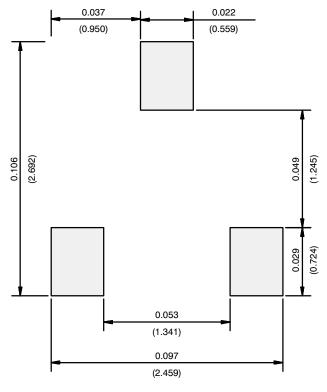




Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
C	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

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