

APPLICATION

- I/O ESD protection for mobile handsets, notebook, PDAs, etc.
- EMI filtering for data ports in cell phones, PDAs, notebook computers
- EMI filtering for LCD, camera and chip-to-chip data lines

FEATURES

- EMI/RFI filtering
- ESD Protection to IEC 61000-4-2 Level 4
- Low insertion loss
- Good attenuation of high frequency signals
- Low clamping voltage
- Low operating and leakage current
- Four elements in one package

DESCRIPTION

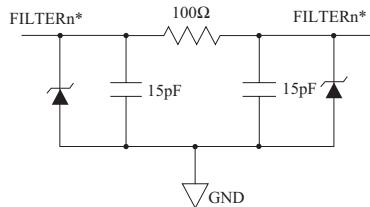
PF1015UDF8 is an EMI filter array with electrostatic discharge (ESD) protection, which integrates four pi filters (C-R-C). These parts include ESD protection diodes on every pin, providing a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge.

The PF1015UDF8 provides the recommended line termination while implementing a low pass filter to limit EMI levels and providing ESD protection which exceeds IEC 61000-4-2 level 4 standard. The UDFN package is a very effective PCB space occupation and a very thin package (0.4mm Pitch, 0.5mm height)

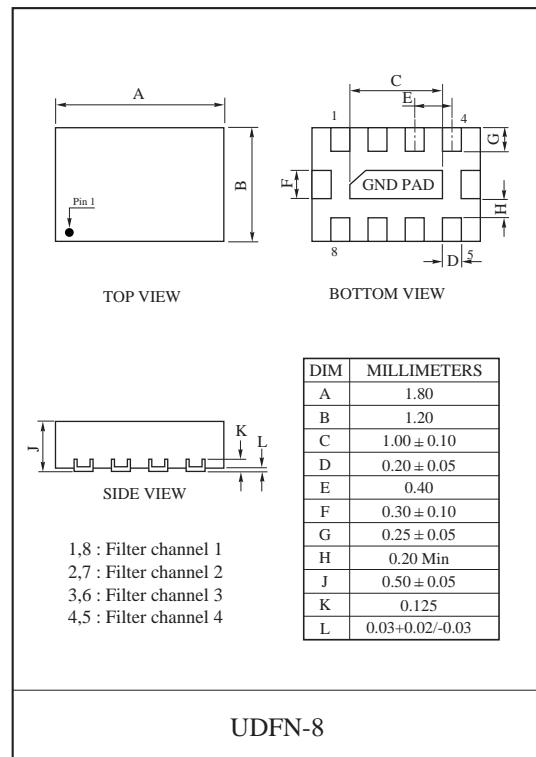
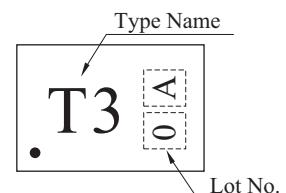
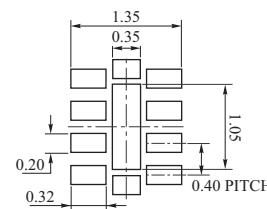
MAXIMUM RATING (Ta=25 °C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Power Per Resistor	P _R	100	mW
Power Dissipation	*P _D	400	
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 150	°C

* Total Package Power Dissipation

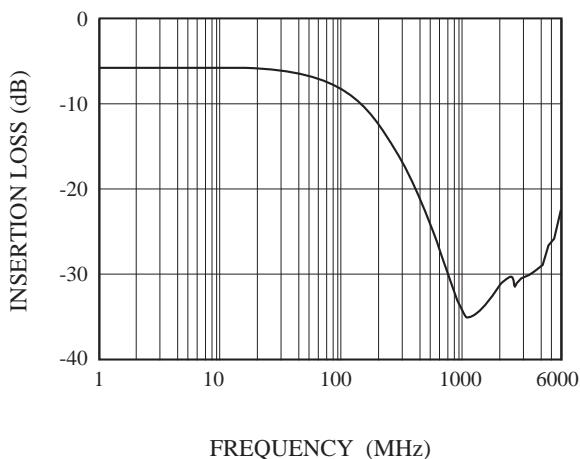
EQUIVALENT CIRCUIT**ELECTRICAL CHARACTERISTICS (Ta=25 °C)**

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reverse Stand-Off Voltage	V _{RWM}	-	-	-	5	V
Reverse Breakdown Voltage	V _{BR}	I _f =1mA	6	-	-	V
Reverse Leakage Current	I _R	V _{RWM} =3.3V	-	-	1.0	µA
Cutoff Frequency	f _{c-3dB}	V _{Line} =0V, Z _{SOURCE} =50Ω, Z _{LOAD} =50Ω	-	110	-	MHz
Channel Resistance	R _{LINE}	Between Input and Output	80	100	120	
Line Capacitance	C _{LINE}	V _{Line} =0V DC, 1MHz, Between I/O Pins and GND	36	45	54	pF
		V _{Line} =2.5V, 1MHz, Between I/O Pins and GND	24	30	36	

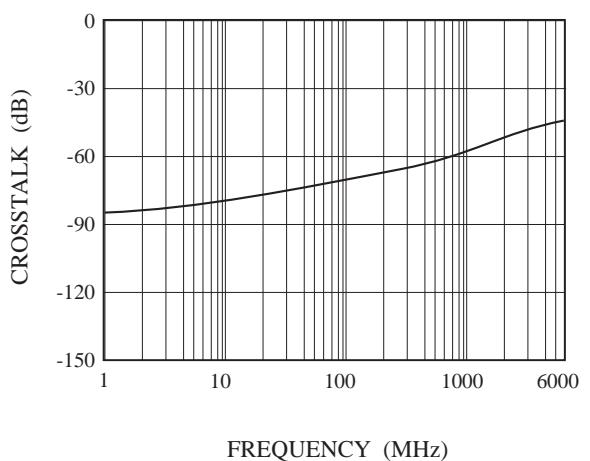
**MARKING****RECOMMENDED FOOTPRINT (dimensions in mm)**

PF1015UDF8

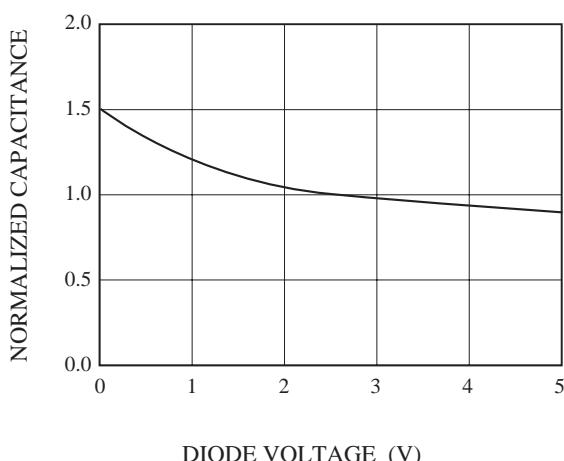
S₂₁ - FREQUENCY



ANALOG CROSSTALK



DIODE CAPACITANCE vs. INPUT VOLTAGE



R_{Line} - TEMPERATURE

