STRUCTURE Silicon monolithic integrated circuits

PRODUCT SERIES Bipolar stepping motor driver

TYPE **BD6383EFV**

FUNCTION
- PWM constant current controllable two H bridge driver
- Built-in translator circuit for CLK-IN control
- Full, Half, Quarter step
- Mixed Decay control
- Parallel IN control

○Absolute maximum ratings(Ta=25℃)

Item	Symbol	Limit	Unit
Supply voltage	$V_{CC0,1,2}$	-0.2～+36.0	V
Power dissipation	Pd	1.6※1	W
		4.7※2	W
Input voltage for control pin	V_{IN}	-0.2～+5.5	V
RNF maximum voltage	V_{RNF}	0.5	V
Maximum output current	I_{OUT}	1.0※3	A/phase
Operating temperature range	T_{opr}	-25～+75	℃
Storage temperature range	T_{stg}	-55～+150	℃
Junction temperature	T_{jmax}	150	℃

※1 70mm×70mm×1.6mm glass epoxy board. Derating in done at 12.8mW/℃ for operating above Ta=25℃.
※2 4-layer recommended board. Derating in done at 37.6mW/℃ for operating above Ta=25℃.
※3 Do not, however exceed Pd, ASO and Tjmax=150℃.

○Recommended operating conditions (Ta=-25～+75℃)

Item	Symbol	Min	Typ	Max	Unit
Supply voltage	$V_{CC0,1,2}$	10	24	28	V
Output current	I_{OUT}	-	0.5	0.7※4	A/phase

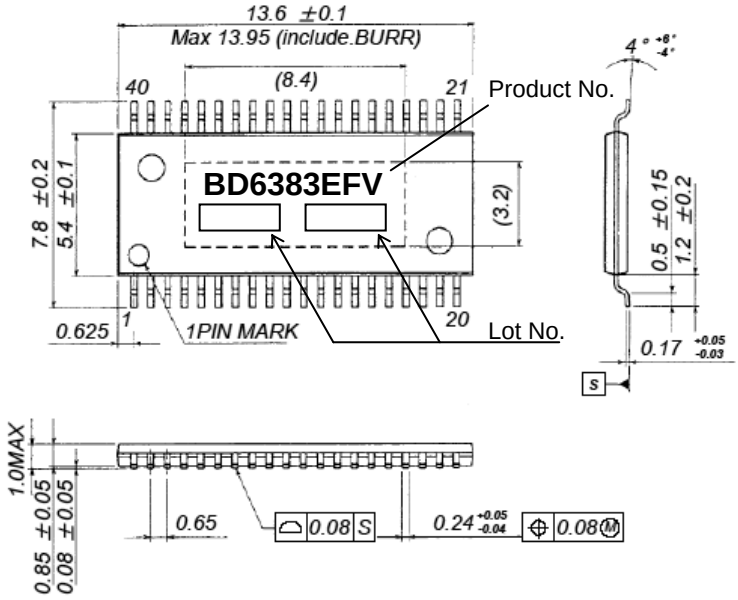
※4 Do not, however exceed Pd, ASO.

This product isn't designed for protection against radioactive rays.

○Electrical characteristics (Unless otherwise specified Ta=25℃, VCC0,1,2=24V)

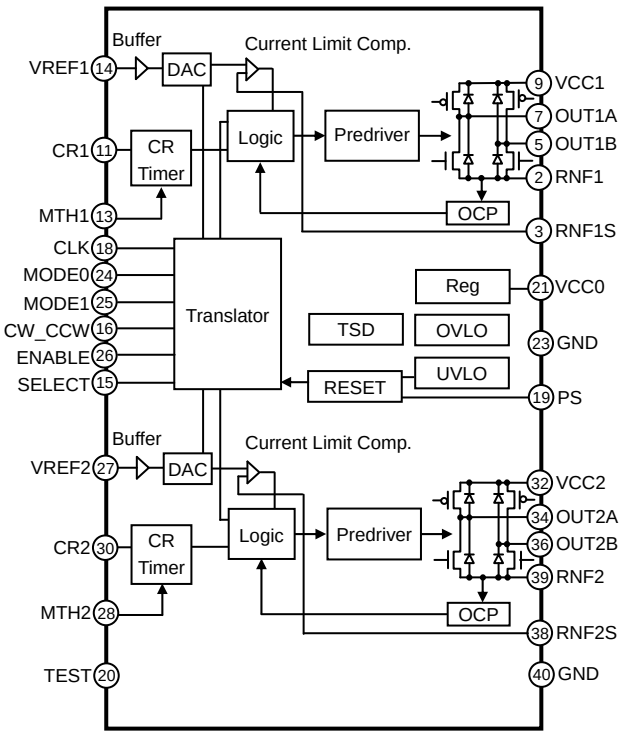
Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Whole						
Circuit current at standby	I_{CCST}	-	1.0	3.0	mA	PS=L
Circuit current	I_{CC}	-	4.5	10	mA	PS=H, VREFX=2V
Control input (SELECT, CW_CCW, CLK, PS, MODE0, MODE1, ENABLE)						
H level input voltage	V_{INH}	2.0	-	-	V	
L level input voltage	V_{INL}	-	-	0.8	V	
H level input current	I_{INH}	35	50	85	μA	V_{IN}=5V
L level input current	I_{INL}	-10	0	-	μA	V_{IN}=0V
Output (OUT1A, OUT1B, OUT2A, OUT2B)						
Output ON resistance	R_{ON}	-	1.50	1.95	Ω	I_{OUT}=0.5A, Sum of upper and lower
Output leak current	I_{LEAK}	-	-	10	μA	
Current control						
RNFXS input current	I_{RNFS}	-2.0	-0.2	-	μA	RNFXS =0V
RNFX input current	I_{RNF}	-40	-20	-	μA	RNFX=0V
VREFX input current	I_{VREF}	-2.0	-0.1	-	μA	VREFX=0V
VREFX input voltage range	V_{REF}	0	-	2.0	V	
MTHX input current	I_{MTH}	-2.0	-0.1	-	μA	MTHX=0V
MTHX input voltage range	V_{MTH}	0	-	3.5	V	
Comparator threshold	V_{CTH}	0.36	0.40	0.44	V	VREFX=2V
Minimum on time	t_{ONMIN}	0.3	0.7	1.2	μs	R=39kΩ,C=1000pF

○Package outline



HTSSOP-B40 (Unit:mm)

○Block diagram



○Pin No. / Pin name

Pin No.	Pin name	Pin No.	Pin name
1	NC	21	VCC0
2	RNF1	22	NC
3	RNF1S	23	GND
4	NC	24	MODE0
5	OUT1B	25	MODE1
6	NC	26	ENABLE
7	OUT1A	27	VREF2
8	NC	28	MTH2
9	VCC1	29	NC
10	NC	30	CR2
11	CR1	31	NC
12	NC	32	VCC2
13	MTH1	33	NC
14	VREF1	34	OUT2A
15	SELECT	35	NC
16	CW_CCW	36	OUT2B
17	NC	37	NC
18	CLK	38	RNF2S
19	PS	39	RNF2
20	TEST	40	GND

NC : Non Connection

○Operation Notes

(1) Absolute maximum ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.

(2) Power supply lines

As return of current regenerated by back EMF of motor happens, take steps such as putting capacitor between power supply and GND as an electric pathway for the regenerated current. Be sure that there is no problem with each property such as emptied capacity at lower temperature regarding electrolytic capacitor to decide capacity value. If the connected power supply does not have sufficient current absorption capacity, regenerative current will cause the voltage on the power supply line to rise, which combined with the product and its peripheral circuitry may exceed the absolute maximum ratings. It is recommended to implement a physical safety measure such as the insertion of a voltage clamp diode between the power supply and GND pins

(3) GND potential

The potential of GND pin must be minimum potential in all operating conditions.

(4) Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions. This IC exposes its frame of the backside of package. Note that this part is assumed to use after providing heat dissipation treatment to improve heat dissipation efficiency . Try to occupy as wide as possible with heat dissipation pattern not only on the board surface but also the backside.

(5) Actions in strong electromagnetic field

Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.

(6) ASO

When using the IC, set the output transistor so that it does not exceed absolute maximum ratings or ASO.

(7) Thermal shutdown circuit

The IC has a built-in thermal shutdown circuit (TSD circuit). If the chip temperature becomes Tjmax=150℃, and higher, coil output to the motor will be open. The TSD circuit is designed only to shut the IC off to prevent runaway thermal operation. It is not designed to protect or indemnify peripheral equipment. Do not use the TSD function to protect peripheral equipment.

(8) Ground Wiring Pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

(9) TEST pin

Be sure to connect TEST pin to GND.

Notes

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