

# SiC Schottky Barrier Diode

# SCS120KE2

#### Applications

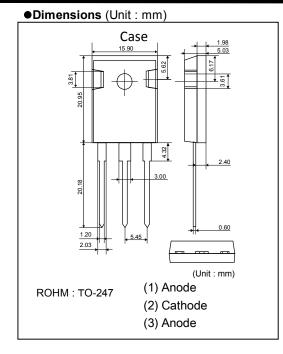
General rectification

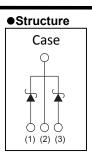
#### Features

- 1)Shorter recovery time
- 2)Reduced temperature dependence
- 3)High-speed switching possible

#### Construction

Silicon carbide epitaxial planer type





●Absolute maximum ratings (Tj=25°C)

Parameter	Symbol	Limits	Unit V	
Reverse voltage (repetitive peak)	$V_{RM}$	1200		
Reverse voltage (DC)	$V_R$	1200	V	
Continuous forward forward current *6	I <sub>F</sub>	10/ 20 * <sup>1</sup>	Α	
Surge no repetitive forward current *6		45 / 90 * <sup>2</sup>	Α	
	I <sub>FSM</sub>	190 / 380* <sup>3</sup>	Α	
Repetitive peak forward current *6	I <sub>FRM</sub>	30 / 58* <sup>4</sup>	Α	
Total power disspation *6	P <sub>D</sub>	115 / 210* <sup>5</sup>	W	
Junction temperature	Tj	175	°C	
Range of storage temperature	Tstg	-55 to +175	°C	
Junction to case *6	Rth(j-c)	1.3 / 0.70	°C / W	

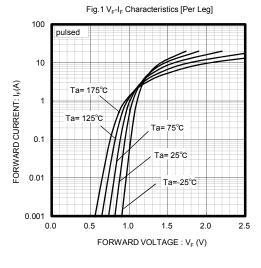
<sup>(\*1)</sup>Tc=135°C / 132°C (\*2)PW=8.3ms sinusoidal,Tj=25°C

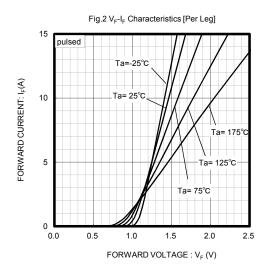
## ●Electrical characteristics (Tj=25°C) [Per Leg]

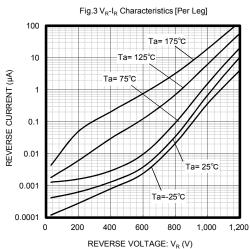
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
DC blocking voltage	$V_{DC}$	1200	-	-	V	$I_R$ =0.2mA
Forward voltage	$V_{F}$	-	1.50	1.70	V	I <sub>F</sub> =10A,Tj=25°C
	VF	-	2.00	-	V	I <sub>F</sub> =10A,Tj=175°C
Reverse current	1	-	10	200	μΑ	V <sub>R</sub> =1200V,Tj=25°C
	IR	-	120	-	μΑ	V <sub>R</sub> =1200V,Tj=175°C
Total capacitance	С	-	650	-	pF	V <sub>R</sub> =1V,f=1MHz
		-	50	-	pF	V <sub>R</sub> =800V,f=1MHz
Total capacitive charge	Qc	-	34	_	nC	V <sub>R</sub> =800V,di/dt=500A/μs
Switching time	tc	-	16	-	ns	V <sub>R</sub> =800V,di/dt=500A/μs

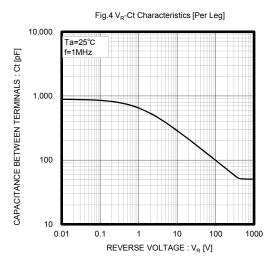
<sup>(\*3)</sup>PW=10μs square,Tj=25°C (\*4)Tc=120°C,Tj=150°C,Duty cycle=10% (\*5)Tc=25°C (\*6)Per Leg / Per Device

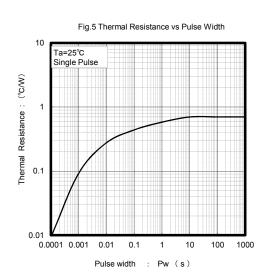
### ●Electrical characteristic curves (Ta=25°C)

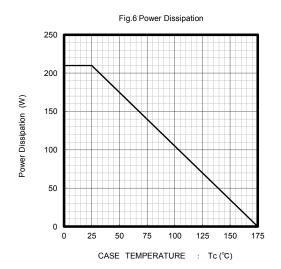


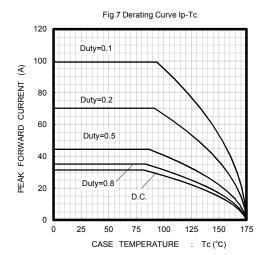


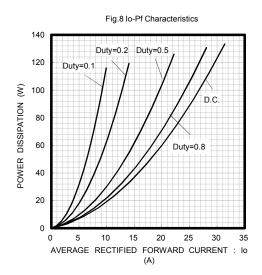












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