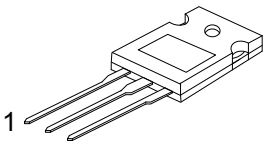
20A, 650V N-CHANNEL POWER MOSFET



TO-247

■ DESCRIPTION

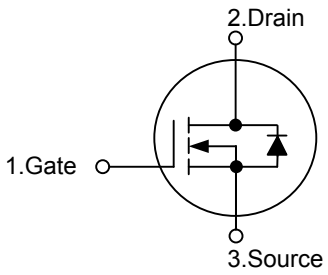
The UTC **20N65** is an N-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **20N65** is universally applied in motor control, UPS, DC choppers and switch-mode and resonant-mode power supplies.

■ FEATURES

* $R_{DS(ON)}$ = 0.45Ω @V_{GS} = 10 V
* High switching speed

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
20N65L-T47-T	20N65G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

20N65L-T47-T (1)Packing Type (2)Package Type (3)Lead Free	(1) T: Tube (2) T47: TO-247 (3) G: Halogen Free, L: Lead Free

■ ABSOLUTE MAXIMUM RATINGS (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current (T_C=25°C)	Continuous	I_D	20	A
	Pulsed	I_{DM}	80	A
Avalanche Energy	Single Pulsed(Note 2)	E_{AS}	1200	mJ
Power Dissipation (T_C=25°C)		P_D	300	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. V_{DD}=50V, Starting T_J=25°C, Peak I_{AS}=20A, L=6mH

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	0.42	°C/ W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	650			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=650V, V_{GS}=0V			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=10A, Pulse test, t≤300μs, duty cycle d≤2%		0.32	0.45	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1MHz		4500		pF
Output Capacitance		C_{OSS}			420		pF
Reverse Transfer Capacitance		C_{RSS}			140		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{GS}=10V, V_{DS}=520V, I_D=10A (Note 1, 2)		150	170	nC
Gate to Source Charge		Q_{GS}			29	40	nC
Gate to Drain Charge		Q_{GD}			60	85	nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{GS}=10V, V_{DS}=325V, I_D=10A, R_G=2Ω (Note 1, 2)		20	40	ns
Rise Time		t_R			43	60	ns
Turn-OFF Delay Time		$t_{D(OFF)}$			70	90	ns
Fall-Time		t_F			40	60	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S	V_{GS}=0V			20	A
Maximum Body-Diode Pulsed Current		I_{SM}	Repetitive			80	A
Drain-Source Diode Forward Voltage		V_{SD}	I_F=I_S, V_{GS}=0V, Pulse test, t≤300μs, duty cycle d≤2%			1.5	V

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle≤2%
2. Essentially independent of operating temperature