

# **NP16N04YUG** MOS FIELD EFFECT TRANSISTOR

R07DS0362EJ0100 Rev.1.00 Jun 13, 2011

## Description

The NP16N04YUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

## Features

- Low on-state resistance
  - -- R<sub>DS(on)</sub> = 25 m $\Omega$  MAX. (V<sub>GS</sub> = 10 V, I<sub>D</sub> = 8 A)
- Low  $C_{iss}$ :  $C_{iss} = 740 \text{ pF TYP}$ .  $(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V})$
- Designed for automotive application and AEC-Q101 qualified
- Small size package 8-pin HSON

## **Ordering Information**

Part No.	Lead Plating	Packing		Package
NP16N04YUG-E1-AY *1	Pure Sn (Tin)	Tape 2500 p/reel	Taping (E1 type)	8-pin HSON
NP16N04YUG-E2-AY *1			Taping (E2 type)	

Note: \*1. Pb-free (This product does not contain Pb in the external electrode.)

## Absolute Maximum Ratings ( $T_A = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to Source Voltage ( $V_{GS} = 0 V$ )	V <sub>DSS</sub>	40	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±20	V
Drain Current (DC) ( $T_c = 25^{\circ}C$ )	I <sub>D(DC)</sub>	±16	А
Drain Current (pulse) *1	I <sub>D(pulse)</sub>	±48	A
Total Power Dissipation ( $T_C = 25^{\circ}C$ )	P <sub>T1</sub>	36	W
Total Power Dissipation ( $T_A = 25^{\circ}C$ ) * <sup>2</sup>	P <sub>T2</sub>	1.0	W
Channel Temperature	T <sub>ch</sub>	175	°C
Storage Temperature	T <sub>stg</sub>	-55 to +175	°C
Repetitive Avalanche Current *3	I <sub>AR</sub>	13	A
Repetitive Avalanche Energy *3	E <sub>AR</sub>	12	mJ

## **Thermal Resistance**

Channel to Case Thermal Resistance	R <sub>th(ch-C)</sub>	4.17	°C/W
Channel to Ambient Thermal Resistance *2	R <sub>th(ch-A)</sub>	150	°C/W

Notes: \*1. T<sub>C</sub> = 25°C, PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

<sup>\*</sup>2. Mounted on glass epoxy substrate of 40 mm x 40 mm x 1.6 mmt with 4% copper area (35  $\mu$ m)

\*3.  $T_{ch(peak)} \leq 150^{\circ}C$ ,  $R_G = 25 \Omega$ 

Caution This product is an electrostatic-sensitive device due to low ESD capability and should be handled with caution for electrostatic discharge. HBM (C = 100 pF, R =  $1.5 \text{ k}\Omega$ )  $\pm 500 \text{ V}$ .



## Electrical Characteristics ( $T_A = 25^{\circ}C$ )

Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±100	nA	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V
Gate to Source Threshold Voltage	V <sub>GS(th)</sub>	2.0	3.0	4.0	V	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$
Forward Transfer Admittance *1	y <sub>fs</sub>	4	8		S	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 8 A
Drain to Source On-state Resistance <sup>*1</sup>	R <sub>DS(on)</sub>		20	25	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A
Input Capacitance	C <sub>iss</sub>		740	1110	pF	V <sub>DS</sub> = 25 V,
Output Capacitance	C <sub>oss</sub>		83	110	pF	V <sub>GS</sub> = 0 V,
Reverse Transfer Capacitance	C <sub>rss</sub>		57	100	pF	f = 1 MHz
Turn-on Delay Time	t <sub>d(on)</sub>		10	20	ns	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 8 A,
Rise Time	tr		4	10	ns	V <sub>GS</sub> = 10 V,
Turn-off Delay Time	t <sub>d(off)</sub>		19	38	ns	R <sub>G</sub> = 0 Ω
Fall Time	t <sub>f</sub>		5	13	ns	
Total Gate Charge	Q <sub>G</sub>		16	24	nC	V <sub>DD</sub> = 32 V,
Gate to Source Charge	Q <sub>GS</sub>		5		nC	V <sub>GS</sub> = 10 V,
Gate to Drain Charge	Q <sub>GD</sub>		6		nC	I <sub>D</sub> = 16 A
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>		0.9	1.5	V	I <sub>F</sub> = 16 A, V <sub>GS</sub> = 0 V
Reverse Recovery Time	t <sub>rr</sub>		25		ns	I <sub>F</sub> = 16 A, V <sub>GS</sub> = 0 V,
Reverse Recovery Charge	Q <sub>rr</sub>		24		nC	di/dt = 100 A/µs

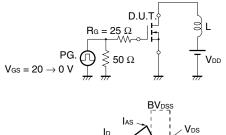
PG.

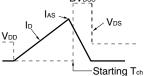
Vgs

0-

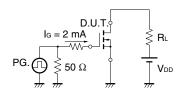
Note: \*1. Pulsed test

### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

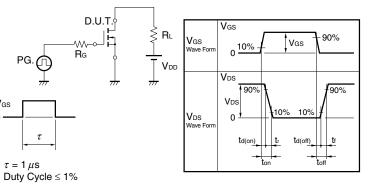




### **TEST CIRCUIT 3 GATE CHARGE**



### **TEST CIRCUIT 2 SWITCHING TIME**

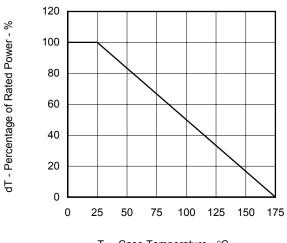


RENESAS

I<sub>D</sub> - Drain Current - A

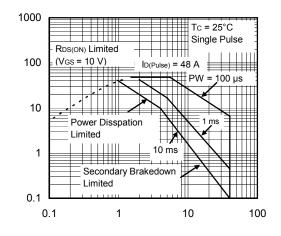
## Typical Characteristics ( $T_A = 25^{\circ}C$ )

DERATING FACTOR OF FORWARD BIAS SAFE **OPERATING AREA** 

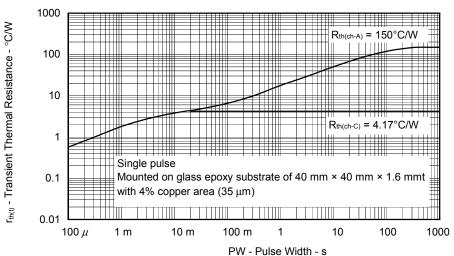


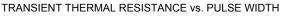
T<sub>c</sub> - Case Temperature - °C





V<sub>DS</sub> - Drain to Source Voltage - V





 $P_{\rm T}$  - Total Power Dissipation - W

T<sub>c</sub> - Case Temperature - °C

75

100

125

150

175

TOTAL POWER DISSIPATION vs.

CASE TEMPERATURE

40

35

30

25 20

15

10

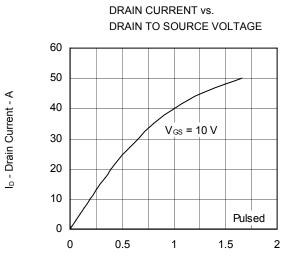
5 0

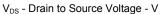
0

25

50

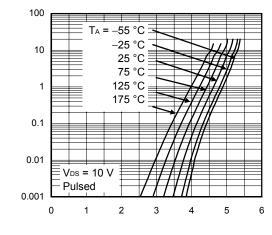






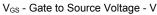
GATE TO SOURCE THRESHOLD VOLTAGE

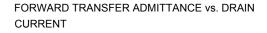
FORWARD TRANSFER CHARACTERISTICS

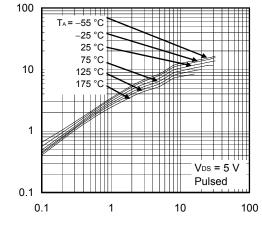


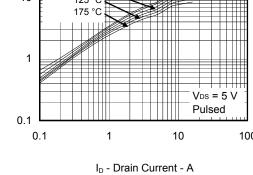
I<sub>D</sub> - Drain Current - A

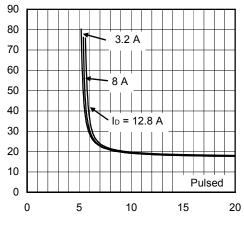
 $\mid y_{fs} \mid$  - Forward Transfer Admittance - S



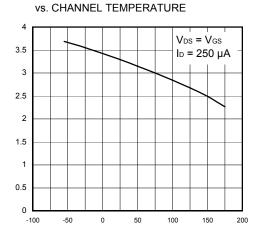




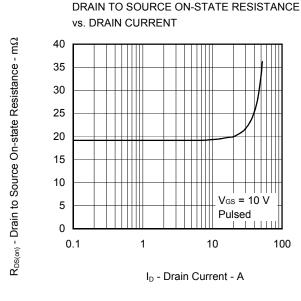




V<sub>GS</sub> - Gate to Source Voltage - V



T<sub>ch</sub> - Channel Temperature - °C



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



 $R_{DS(on)}$  - Drain to Source On-state Resistance - m $\Omega$ 

1000

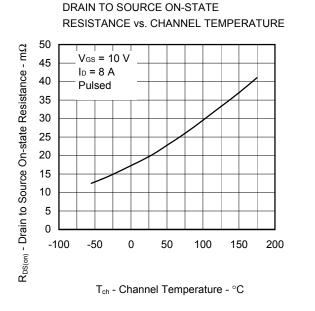
100

10

1

0.1

t<sub>d(on)</sub>, t, t<sub>d(off)</sub>, t<sub>f</sub> - Switching Time - ns



SWITCHING CHARACTERISTICS

VDD = 20 V

Vgs = 10 V

R<sub>G</sub> = 0 Ω

td(off)

td(on)

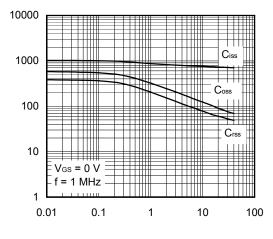
100

tf

tr

10

CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

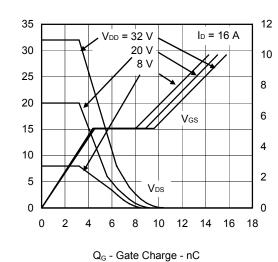


Ciss, Coss, Crss - Capacitance - pF

V<sub>DS</sub> - Drain to Source Voltage - V

 $V_{\mbox{\scriptsize DS}}$  - Drain to Source Voltage - V

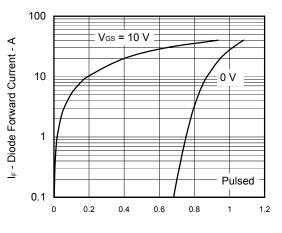
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



I<sub>D</sub> - Drain Current - A

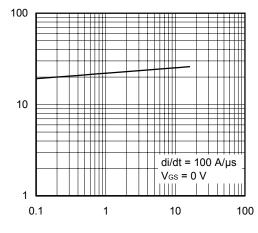
SOURCE TO DRAIN DIODE FORWARD VOLTAGE

1



 $V_{\text{F(S-D)}}$  - Source to Drain Voltage - V

REVERSE RECOVERY TIME vs. DRAIN CURRENT

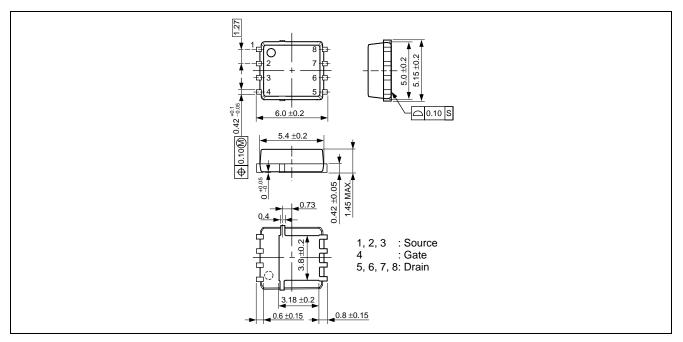


I<sub>F</sub> - Drain Current - A

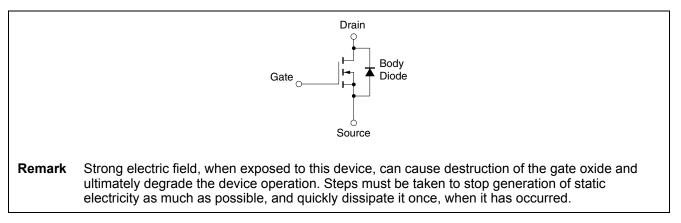
tr - Reverse Recovery Time - ns

## Package Drawings (Unit: mm)

## 8-pin HSON (Mass: 0.13 g TYP.)



## **Equivalent Circuit**





## NP16N04YUG Data Sheet

ľ			Description			
	Rev.	Date	Page	Summary		
ſ	1.00	Jun 13, 2011	-	First Edition Issued		

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