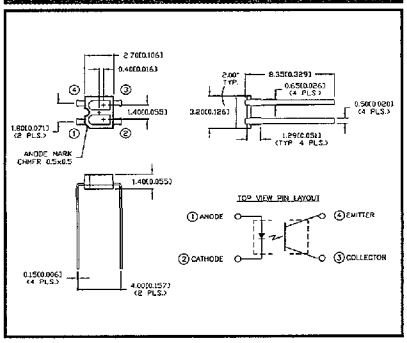
PHOTO REFLECTIVE ASSEMBLIES

OED-SR-105F

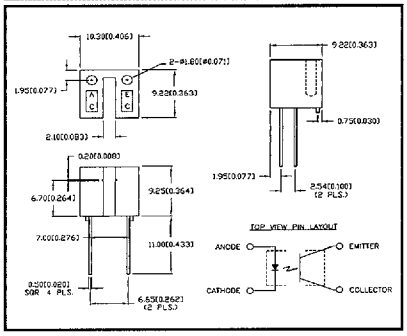


Package Style	Limits of Safe Operation			
	V_R (V)	P_D (mW)	If (mA)	t-opr (C°)
SMT Bent Leads	5.0	75	50	-20 ~ + 85

Input			Output		
Vf @ 20mA	Po (mW)	I_R (μA)	I_L (μA)	I_{CEO} (μA)	Type
1.3	3.5	1.3	90	0.2	Photo Transistor

PHOTO INTERRUPTIVE ASSEMBLIES

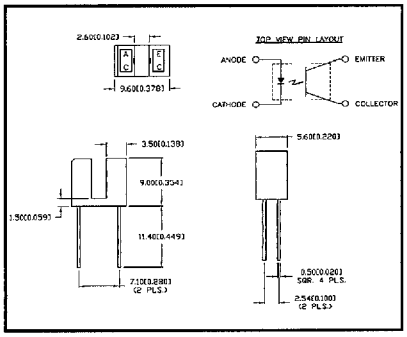
OED-SIV2167



Package Style	Limits of Safe Operation			
	V_R (V)	P_D (mW)	If (mA)	t-opr (C°)
Slotted Holder	5.0	100	50	-20 ~ + 85

Input			Output		
Vf @ 20mA	Po (mW)	I_R (μA)	I_L (μA)	I_{CEO} (μA)	Type
1.6	3.0	10.0	2.0	0.1	Photo Transistor

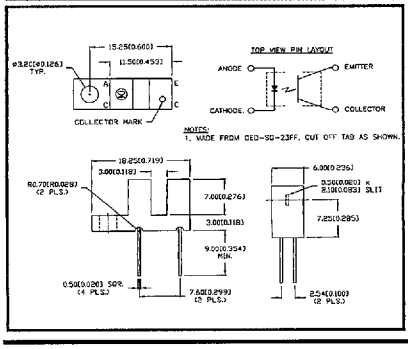
OED-SI2671



Package Style	Limits of Safe Operation			
	V_R (V)	P_D (mW)	If (mA)	t-opr (C°)
Slotted Holder	5.0	100	50	-20 ~ + 85

Input			Output		
Vf @ 20mA	Po (mW)	I_R (μA)	I_L (μA)	I_{CEO} (μA)	Type
1.6	3.0	10.0	15.0	0.1	Photo Transistor

OED-SI3076-1T



Package Style	Limits of Safe Operation			
	V_R (V)	P_D (mW)	If (mA)	t-opr (C°)
Slotted Holder + Tab	5.0	100	60	-20 ~ + 85

Input			Output		
Vf @ 20mA	Po (mW)	I_R (μA)	I_L (μA)	I_{CEO} (μA)	Type
1.5	3.0	10.0	1.0	0.1	Photo Transistor

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