Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-1515 (Z)

Rev.0 May. 2002

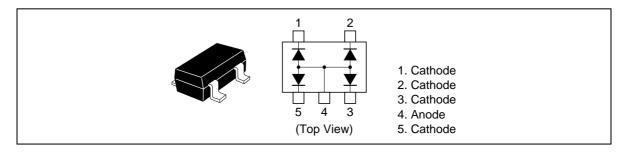
Features

- HZM6.2ZMFA has four devices in a monolithic, and can absorb surge.
- Low capacitance (C = 8.5 pF max) and can protect ESD of signal line.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.2ZMFA	62N	MPAK-5

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Power dissipation	Pd *	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: Four device total, See Fig.2.

Electrical Characteristics *1

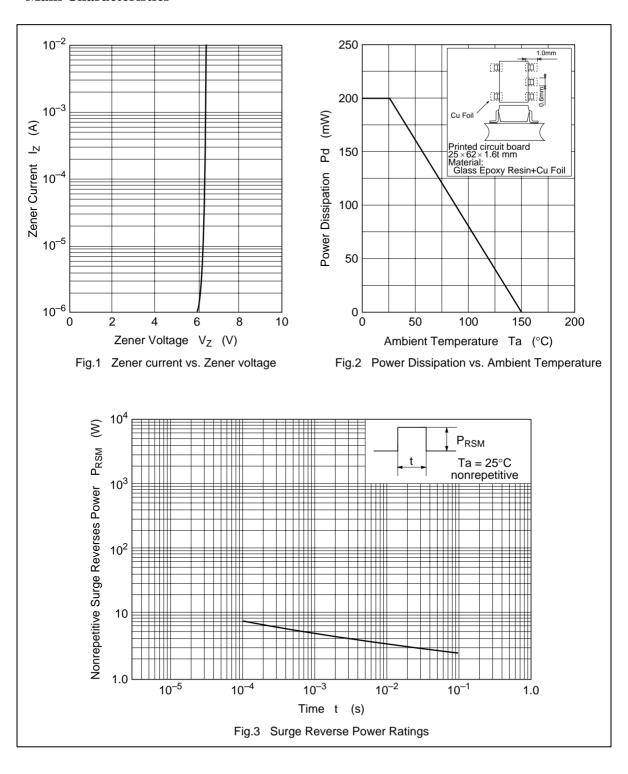
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V _z	5.90	_	6.50	V	$I_z = 5 \text{ mA}$, 40 ms pulse
Reverse current	I _R	_	_	3	μА	V _R = 5.5 V
Capacitance	С	_	_	8.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	_	_	60	Ω	$I_z = 5 \text{ mA}$
ESD-Capability *2	_	13		_	kV	$C = 150 \text{ pF}, R = 330 \Omega$, Both forward and reverse direction 10 pulse

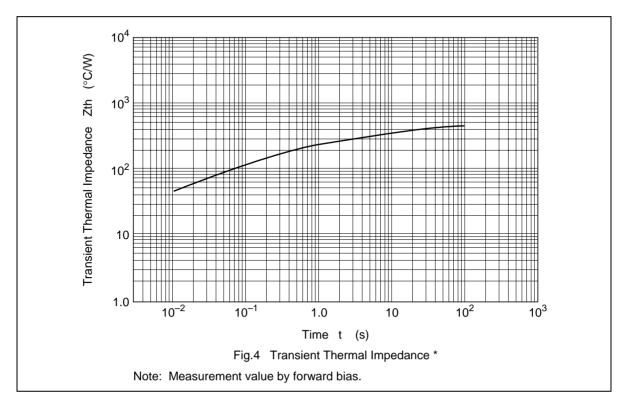
Notes: 1. Per one device.

2. Failure criterion ; $I_{\scriptscriptstyle R}$ > 3 μA at $V_{\scriptscriptstyle R}$ = 5.5 V.

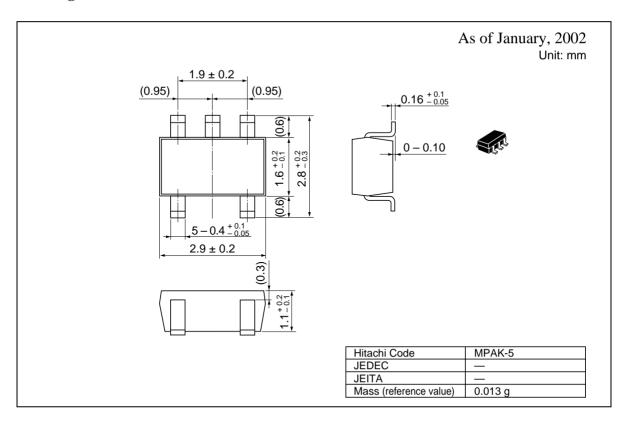
Main Characteristics



Main Characteristics (cont)



Package Dimensions



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