

REVISIONS														
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED											
REV														
SHEET														
REV														
SHEET														
REV STATUS OF SHEETS	REV													
	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13
PMIC N/A	PREPARED BY <i>Marcia B Kelleher</i>		DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444											
STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE	CHECKED BY <i>Thomas J. Picciuti</i>		MICROCIRCUITS, DIGITAL, HIGH SPEED CMOS, OCTAL BUS TRANSCEIVER WITH THREE-STATE OUTPUTS, MONOLITHIC SILICON											
	APPROVED BY <i>[Signature]</i>		SIZE A	CAGE CODE 67268	5962-89989									
	DRAWING APPROVAL DATE 31 JULY 1990		REVISION LEVEL SHEET 1											
AMSC N/A														

DESC FORM 193
SEP 87

U.S. GOVERNMENT PRINTING OFFICE: 1987 - 748-129/60911

5962-E1683

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawing (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.5 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 3

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
High level output voltage	V _{OH}	V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 20 μA	1, 2, 3	V _{CC} = 2.0 V	1.9	V
				V _{CC} = 4.5 V	4.4	
				V _{CC} = 6.0 V	5.9	
		V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 6.0 mA		V _{CC} = 4.5 V	3.7	
		V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 7.8 mA		V _{CC} = 6.0 V	5.2	
Low level output voltage	V _{OL}	V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 20 μA	1, 2, 3	V _{CC} = 2.0 V	0.1	V
				V _{CC} = 4.5 V	0.1	
				V _{CC} = 6.0 V	0.1	
		V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 6.0 mA		V _{CC} = 4.5 V	0.4	
		V _{IN} = V _{IH} minimum or V _{IL} maximum I _O ≤ 7.8 mA		V _{CC} = 6.0 V	0.4	
High level input voltage ^{2/}	V _{IH}		1, 2, 3	V _{CC} = 2.0 V	1.5	V
				V _{CC} = 4.5 V	3.15	
				V _{CC} = 6.0 V	4.2	

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89989
	REVISION LEVEL	SHEET 4

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit	
				Min	Max		
Low level input voltage <u>2/</u>	V _{IL}		V _{CC} = 2.0 V	1, 2, 3		0.3	V
			V _{CC} = 4.5 V			0.9	
			V _{CC} = 6.0 V			1.2	
Quiescent supply current (standby)	I _{CC}	V _{IN} = V _{CC} or GND I _{OUT} = 0 μA	V _{CC} = 6.0 V	1, 2, 3		160	μA
Input leakage current, GAB or GBA	I _{IN}	V _{CC} = 6.0 V V _{IN} = V _{CC} or GND		1, 2, 3		±1.0	μA
3-state output current, A or B	I _{OZ}	V _{CC} = 6.0 V V _{IN} = V _{CC} or GND		1, 2, 3		±10.0	μA
Functional tests		See 4.3.1d		7,8			
Propagation delay time, A or B to B or A	t _{PHL} , t _{PLH}	T _C = +25°C C _L = 50 pF See figure 4	V _{CC} = 2.0 V	9		105	ns
			V _{CC} = 4.5 V			21	
			V _{CC} = 6.0 V			18	
		T _C = -55°C, +125°C C _L = 50 pF See figure 4	V _{CC} = 2.0 V	10, 11		160	
			V _{CC} = 4.5 V			32	
			V _{CC} = 6.0 V			27	
Propagation delay time, output enable time <u>3/</u>	t _{PZH} , t _{PZL}	T _C = +25°C C _L = 50 pF See figure 4	V _{CC} = 2.0 V	9		210	ns
			V _{CC} = 4.5 V			42	
			V _{CC} = 6.0 V			36	

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89989
	REVISION LEVEL	SHEET 5

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time, output enable time <u>3/</u>	t _{pZH} , t _{pZL}	T _C = -55°C, +125°C C _L = 50 pF See figure 4	10, 11		315	ns
					63	
					54	
Propagation delay time, output disable time <u>3/</u>	t _{pHZ} , t _{pLZ}	T _C = +25°C C _L = 50 pF See figure 4	9		150	ns
					30	
					26	
		T _C = -55°C, +125°C C _L = 50 pF See figure 4	10, 11		225	
					45	
					38	
Output transition time, A or B <u>3/ 4/</u>	t _{THL} , t _{TLH}	T _C = +25°C C _L = 50 pF See figure 4	9		60	ns
					12	
					10	
		T _C = -55°C, +125°C C _L = 50 pF See figure 4	10, 11		90	
					18	
					15	

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 6

DESC FORM 193A
SEP 87

U. S. GOVERNMENT PRINTING OFFICE: 1989-749-033

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Control capacitance	C _C	See 4.3.1c, V _{CC} = GND	4		10	pF
Input/Output capacitance	C _{I/O}	See 4.3.1c, V _{CC} = 6.0 V	4		20	pF

- 1/ For a power supply of 5.0 V ±10 percent, the worst case output voltage (V_{OH} and V_{OL}) occur for HC at 4.5 V. Thus, the 4.5 V values should be used when designing with this supply. Worst case V_{IH} and V_{IL} occur at V_{CC} = 5.5 V and 4.5 V respectively. (The V_{IH} value at 5.5 V is 3.85 V). The worst case leakage current (I_{IN}, I_{CC}, and I_{OZ}) occur for CMOS at the higher voltage so the 6.0 V values should be used. Power dissipation capacitance (C_{PD}), typically 80 pF, determines the no load dynamic power consumption, P_D = C_{PD} V_{CC}² f + I_{CC} V_{CC}, and the no load dynamic current consumption, I_S = C_{PD} V_{CC} f + I_{CC}.
- 2/ V_{IH} and V_{IL} tests are not required if applied as forcing functions for the V_{OH} and V_{OL} tests.
- 3/ AC testing at V_{CC} = 2.0 V and V_{CC} = 6.0 V shall be guaranteed, if not tested, to the specified limit in table I.
- 4/ Transition times (t_{THL}, t_{TLH}), if not tested, shall be guaranteed to the specified limits in table I.
- 5/ Set the input enable control pin(s) to V_{CC} or GND, as applicable, to disable the outputs.

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89989
	REVISION LEVEL	SHEET 7

Device type	01	
Case outlines	R	2
Terminal number	Terminal symbol	
1	GAB	GAB
2	A1	A1
3	A2	A2
4	A3	A3
5	A4	A4
6	A5	A5
7	A6	A6
8	A7	A7
9	A8	A8
10	GND	GND
11	B8	B8
12	B7	B7
13	B6	B6
14	B5	B5
15	B4	B4
16	B3	B3
17	B2	B2
18	B1	B1
19	$\bar{G}BA$	$\bar{G}BA$
20	VCC	VCC

NC = No connection

FIGURE 1. Terminal connections.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 8

DESC FORM 193A
SEP 87

© U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

Enable inputs		Operation
G _{BA}	G _{AB}	
L	L	B data to A bus
H	H	A data to B bus
H	L	Isolation
L	H	B data to A bus, A data to B bus

H = High level
L = Low level

FIGURE 2. Truth table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 9

DESC FORM 193A
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1988-548-804

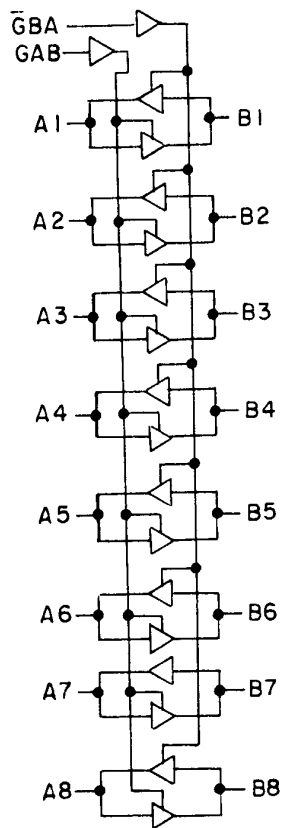
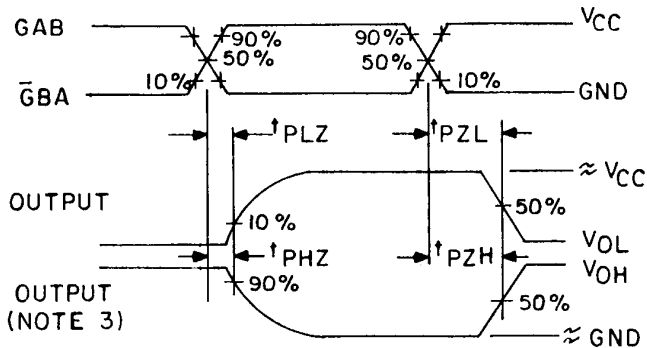
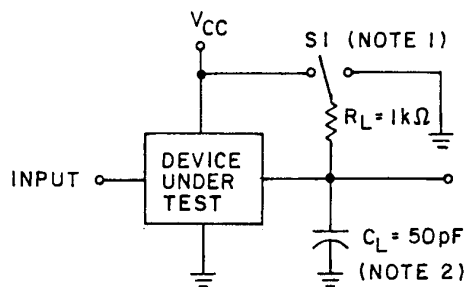
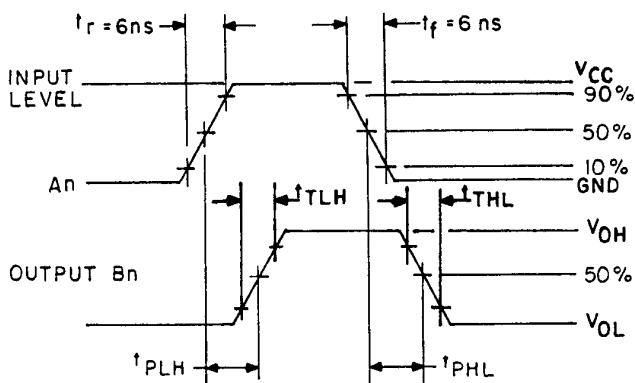
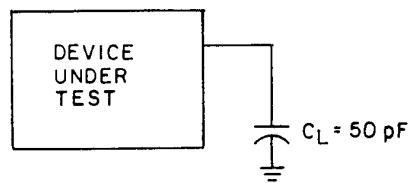


FIGURE 3. Logic diagram.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89989	
		REVISION LEVEL	SHEET 10

DESC FORM 193A
SEP 87

U. S. GOVERNMENT PRINTING OFFICE: 1968-549-804



NOTES:

1. S1 = V_{CC} for t_{pZL} and t_{pLZ} measurements.
S1 = GND for t_{pZH} and t_{pHZ} measurements.
2. C_L includes load and test jig capacitance.
3. This waveform is applicable to both three-state and open drain switching time measurements.

FIGURE 4. Test circuit and switching waveforms.

**STANDARDIZED
MILITARY DRAWING**

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE
A

5962-89989

REVISION LEVEL

SHEET

11

DESC FORM 193A
SEP 87

* U. S. GOVERNMENT PRINTING OFFICE: 1988-549-004

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 5 and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroup 4 (C_C and $C_{T/O}$ measurements) shall be measured only for the initial test and after process or design changes which may affect capacitance. Capacitance shall be measured between the designated terminal and GND at a frequency of 1 MHz. Test all applicable pins on 5 devices with zero failures.
- d. Subgroups 7 and 8 tests shall verify the truth table as specified on figure 2.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test condition, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 12

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9
Group A test requirements (method 5005)	1, 2, 3, 4, 7, 8, 9, 10**, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone (513) 296-8525.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89989
		REVISION LEVEL	SHEET 13

DESC FORM 193A
SEP 87

* U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-BUL-103. Additional sources will be added to MIL-BUL-103 as they become available. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS. The approved sources listed below are for information purposes only and are current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number ^{1/}
5962-8998901RX	01295	SNJ54HC623J
5962-89989012X	01295	SNJ54HC623FK

^{1/} Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

01295

Vendor name and address

Texas Instruments, Incorporated
 13500 N. Central Expressway
 P.O. Box 655303
 Dallas, TX 75265
 Point of contact:
 I-20 at FM 1788
 Midland, TX 79711-0448

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89989
	REVISION LEVEL	SHEET 14

DESC FORM 193A
 SEP 87

U. S. GOVERNMENT PRINTING OFFICE: 1988-749-033

019625