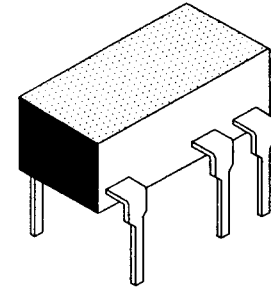


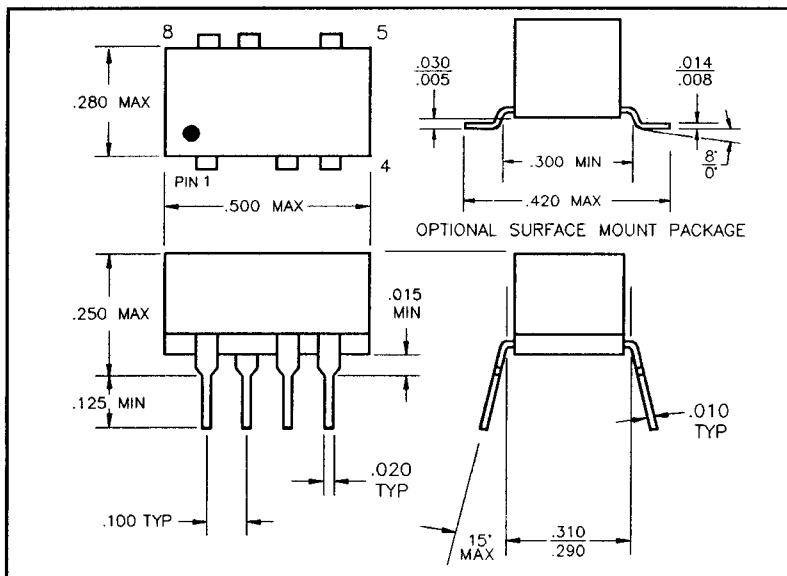
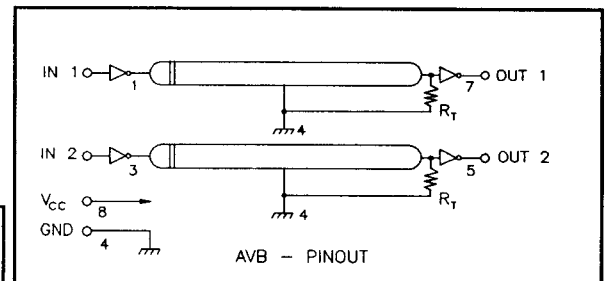
8 PIN, MINI-DIP, DUAL OUTPUT, TTL ACTIVES

DC Electrical Characteristics		Test Conditions	Min	Max	Unit
V _{OH}	High-Level Output Voltage	V _{CC} =min. V _{IL} =max. I _{OH} =max	2.7		V
V _{OL}	Low Level Output Voltage	V _{CC} =min. V _{IH} =min. I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} =min. I _I =-18mA		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} =max. V _{IN} =2.7V		50	uA
I _{IL}	Low-Level Input Current	V _{CC} =max. V _{IN} =5.25V		1.0	mA
I _{OS}	Short Circuit Output Current	V _{CC} =max. V _{OUT} =0 (One output at a time)	-40	-100	mA
I _{CCH}	High-Level Supply Current	V _{CC} =max. V _{IN} =OPEN		90	mA
I _{CCL}	Low-Level Supply Current	V _{CC} =max. V _{IN} =0		90	mA
T _{RO}	Output Rise Time	T _d ≤ 500 nS		4	nS
N _H	Fanout High-Level Output	V _{CC} =max. V _{OH} =2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} =max. V _{OH} =0.5V		10 TTL LOAD	



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-level Input voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW	Pulse Width % of Total Delay	40		%
T _A	Operating Free-Air Temperature	0	+70	°C

Input Pulse Test Conditions @ 25°C		Unit
E _{IN}	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	110 %
T _{RI}	Pulse Rise Time(0.75 - 2.4 Volts)	2.0 nS
PRR	Pulse Repetition Rate	1.0 MHz
V _{CC}	Supply Voltage	5.0 Volts



DELAYS ± 2 nS OR 5%	POLARA P/N AVB - PINOUT
5	AVB-0005
10	AVB-0010
15	AVB-0015
20	AVB-0020
25	AVB-0025
30	AVB-0030
35	AVB-0035
40	AVB-0040
45	AVB-0045
50	AVB-0050
55	AVB-0055
60	AVB-0060
65	AVB-0065
70	AVB-0070
75	AVB-0075
80	AVB-0080
90	AVB-0090
100	AVB-0100

ADD "S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.

* ANY VALUES NOT SHOWN, AVAILABLE UPON REQUEST.