

**150mA EXTREMELY LOW NOISE LDO REGULATOR****AP2121****General Description**

The AP2121 series are positive voltage regulator ICs designed by CMOS process. Each of these ICs consists of a voltage reference, an error amplifier, a resistor network for setting output voltage, a current limit circuit for current protection and a chip enable circuit (5-pin products only).

The AP2121 series have features of high ripple rejection, low dropout voltage, low noise, high output voltage accuracy, and low current consumption which make them ideal for use in various battery-powered devices.

The AP2121 series have 1.5V, 2.5V and 3.2V versions.

The AP2121 are available in standard SOT-23-3 and SOT-23-5 packages.

Features

- Low Dropout Voltage at $I_{OUT}=100mA$: 150mV Typical (Except 1.5V Version)
- Low Standby Current: 0.1 μ A Typical (Except 1.5V Version)
- Low Quiescent Current: 25 μ A Typical
- High Ripple Rejection: 70dB Typical ($f=10kHz$)
- Excellent Line Regulation: 4mV Typical
- Excellent Load Regulation: 12mV Typical
- High Output Voltage Accuracy: $\pm 2\%$
- Excellent Line Transient Response and Load Transient Response
- Low ESR Ceramic Capacitor Available
- ESD (Human Body Model): 2kV

Applications

- Mobile Phones, Cordless Phones
- Wireless Communication Equipment
- Portable Games
- Cameras, Video Recorders
- Sub-board Power Supplies for Telecom Equipment
- Battery Powered Equipment

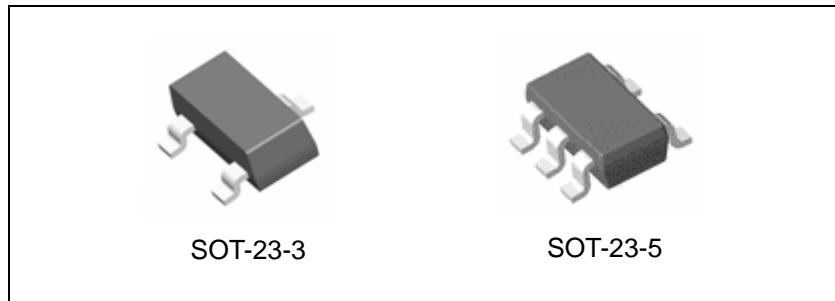


Figure 1. Package Types of AP2121



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Pin Configuration

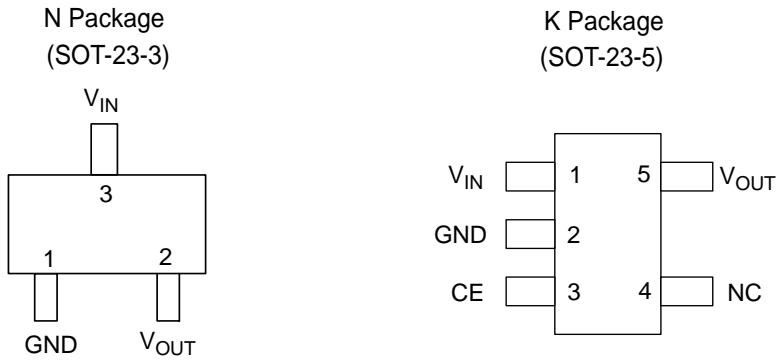


Figure 2. Pin Configuration of AP2121 (Top View)

Pin Description

Pin Number		Pin Name	Function
SOT-23-3	SOT-23-5		
3	1	V _{IN}	Input voltage
1	2	GND	Ground
	3	CE	Active high enable input pin. Logic high=enable, logic low=shutdown
	4	NC	No connection
2	5	V _{OUT}	Regulated output voltage



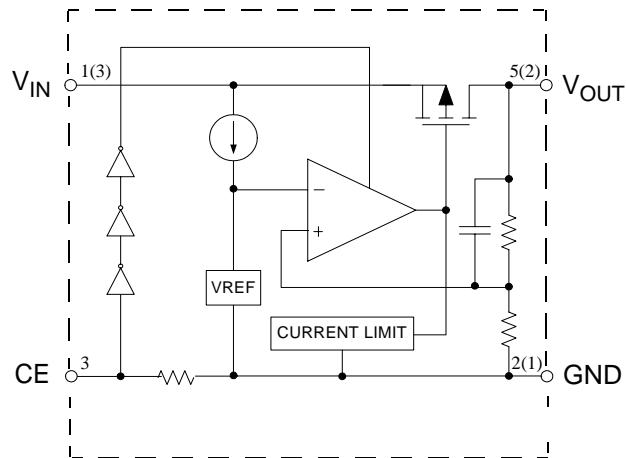
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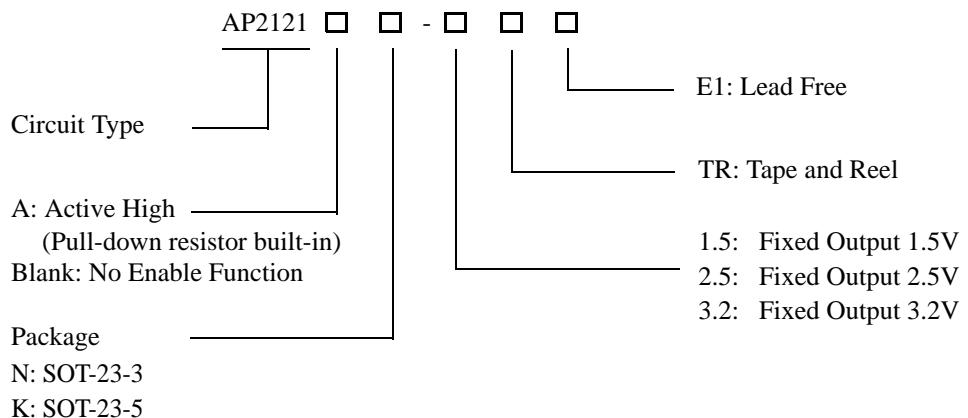
Functional Block Diagram



SOT-23-5 (SOT-23-3)

Figure 3. Functional Block Diagram of AP2121

Ordering Information





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Ordering Information (Continued)

Package	Temperature Range	Condition	Part Number		Marking ID		Packing Type	
			Tin Lead	Lead Free	Tin Lead	Lead Free		
SOT-23-3	-40 to 125°C			AP2121N-1.5TRE1		EF1	Tape & Reel	
SOT-23-5		Active High (Pull-down resistor built-in)		AP2121AK-2.5TRE1		E1V	Tape & Reel	
		Active High (Pull-down resistor built-in)		AP2121AK-3.2TRE1		E3Z	Tape & Reel	

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant.

Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Value	Unit
Input Voltage	V _{IN}	6.5	V
Output Current	I _{OUT}	300	mA
Storage Temperature Range	T _{STG}	-65 to 150	°C
Lead Temperature (Soldering, 10sec)	T _{LEAD}	260	°C
ESD (Human Body Model)		2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Input Voltage	V _{IN}		6	V
Operating Junction Temperature Range	T _J	-40	125	°C



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Electrical Characteristics

AP2121-1.5V Electrical Characteristics

(V_{IN}=2.5V, T_J=25°C, C_{IN}=1μF, C_{OUT}=1μF, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	V _{OUT}	V _{IN} =2.5V, 1mA≤I _{OUT} ≤30mA	1.47	1.5	1.53	V
Input Voltage	V _{IN}				6	V
Output Current	I _{OUT}	V _{IN} -V _{OUT} =1V	150			mA
Load Regulation	V _{RLOAD}	V _{IN} =2.5V, 1mA≤I _{OUT} ≤80mA		12	40	mV
Line Regulation	V _{RLINE}	2.3V≤V _{IN} ≤6V, I _{OUT} =30mA		4	16	mV
Dropout Voltage	V _{DROP}	I _{OUT} =100mA		400	600	mV
Quiescent Current	I _Q	V _{IN} =2.5V, I _{OUT} =0mA		25	50	μA
Power Supply Rejection Ratio	PSRR	Ripple 0.5Vp-p, f=10kHz, V _{IN} =2.5V		70		dB
Output Voltage Temperature Coefficient	ΔV _{OUT} /ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±150		μV/°C
	(ΔV _{OUT} /V _{OUT})/ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±100		ppm/°C
Short Current Limit	I _{LIMIT}	V _{OUT} =0V		50		mA
RMS Output Noise	V _{NOISE}	T _A =25°C, 10Hz ≤f≤100kHz		30		μVrms



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Electrical Characteristics (Continued)

AP2121-2.5V Electrical Characteristics

(V_{IN}=3.5V, T_J=25°C, C_{IN}=1μF, C_{OUT}=1μF, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	V _{OUT}	V _{IN} =3.5V, 1mA≤I _{OUT} ≤30mA	2.45	2.5	2.55	V
Input Voltage	V _{IN}				6	V
Output Current	I _{OUT}	V _{IN} -V _{OUT} =1V	150			mA
Load Regulation	V _{RLOAD}	V _{IN} =3.5V, 1mA≤I _{OUT} ≤80mA		12	40	mV
Line Regulation	V _{RLINE}	3V≤V _{IN} ≤6V, I _{OUT} =30mA		4	16	mV
Dropout Voltage	V _{DROP}	I _{OUT} =100mA		150	300	mV
Quiescent Current	I _Q	V _{IN} =3.5V, I _{OUT} =0mA		25	50	μA
Standby Current	I _{STD}	V _{IN} =3.5V, V _{CE} in OFF mode		0.1	1	μA
Power Supply Rejection Ratio	PSRR	Ripple 0.5Vp-p, f=10kHz, V _{IN} =3.5V		70		dB
Output Voltage Temperature Coefficient	ΔV _{OUT} /ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±250		μV/°C
	(ΔV _{OUT} /V _{OUT})/ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±100		ppm/°C
Short Current Limit	I _{LIMIT}	V _{OUT} =0V		50		mA
RMS Output Noise	V _{NOISE}	T _A =25°C, 10Hz ≤f≤100kHz		30		μVrms
CE "High" Voltage		CE input voltage "High"	1.5		V _{IN}	V
CE "Low" Voltage		CE input voltage "Low"	0		0.25	V
CE pull-down resistance	R _{PD}		2.5	5	10	MΩ



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AP2121-3.2V Electrical Characteristics

(V_{IN}=4.2V, T_J=25°C, C_{IN}=1μF, C_{OUT}=1μF, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	V _{OUT}	V _{IN} =4.2V, 1mA≤I _{OUT} ≤30mA	3.136	3.2	3.264	V
Input Voltage	V _{IN}				6	V
Output Current	I _{OUT}	V _{IN} -V _{OUT} =1V	150			mA
Load Regulation	V _{RLOAD}	V _{IN} =4.2V, 1mA≤I _{OUT} ≤80mA		12	40	mV
Line Regulation	V _{RLINE}	3.7V≤V _{IN} ≤6V, I _{OUT} =30mA		4	16	mV
Dropout Voltage	V _{DROP}	I _{OUT} =100mA		150	300	mV
Quiescent Current	I _Q	V _{IN} =4.2V, I _{OUT} =0mA		25	50	μA
Standby Current	I _{STD}	V _{IN} =4.2V, V _{CE} in OFF mode		0.1	1	μA
Power Supply Rejection Ratio	PSRR	Ripple 0.5Vp-p, f=10kHz, V _{IN} =4.2V		70		dB
Output Voltage Temperature Coefficient	ΔV _{OUT} /ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±320		μV/°C
	(ΔV _{OUT} /V _{OUT})/ΔT	I _{OUT} =30mA, -40°C≤T _J ≤125°C		±100		ppm/°C
Short Current Limit	I _{LIMIT}	V _{OUT} =0V		50		mA
RMS Output Noise	V _{NOISE}	T _A =25°C, 10Hz ≤f≤100kHz		30		μVrms
CE "High" Voltage		CE input voltage "High"	1.5		V _{IN}	V
CE "Low" Voltage		CE input voltage "Low"	0		0.25	V
CE pull-down resistance	R _{PD}		2.5	5	10	MΩ



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Typical Performance Characteristics

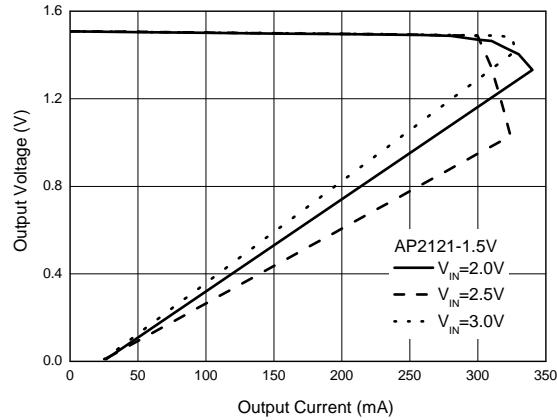


Figure 4. Output Voltage vs. Output Current

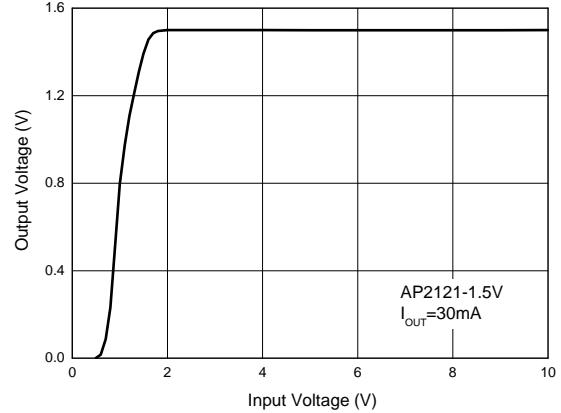


Figure 5. Output Voltage vs. Input Voltage

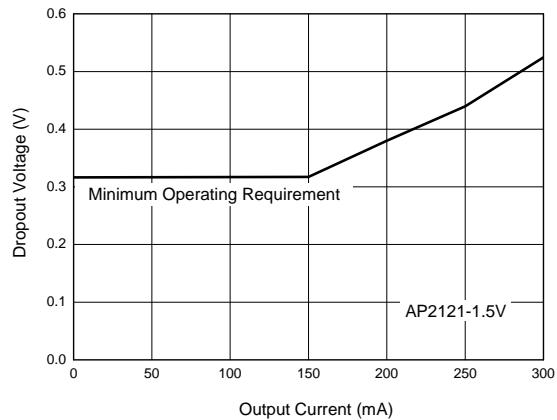


Figure 6. Dropout Voltage vs. Output Current

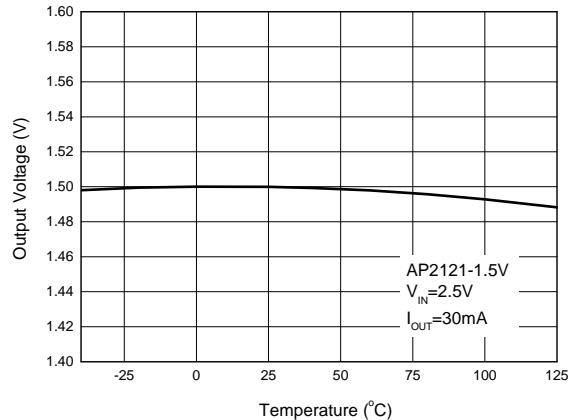


Figure 7. Output Voltage vs. Temperature



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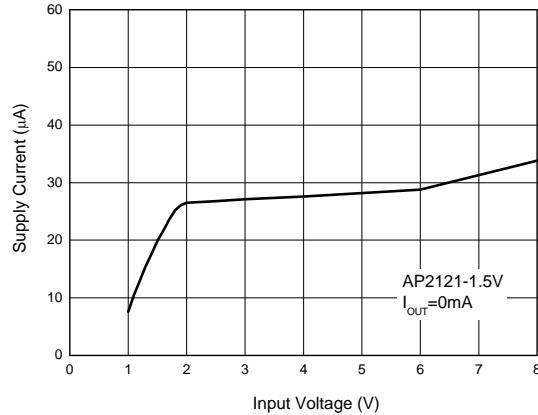


Figure 8. Supply Current vs. Input Voltage

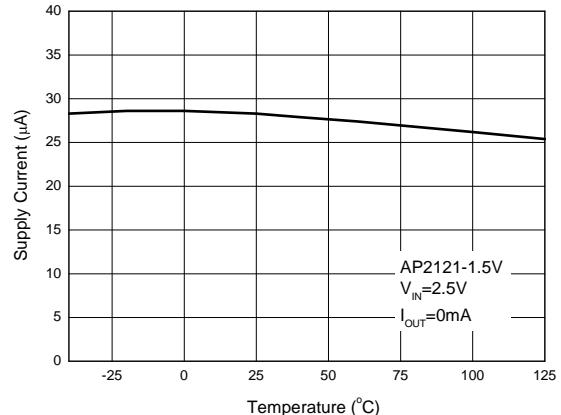


Figure 9. Supply Current vs. Temperature

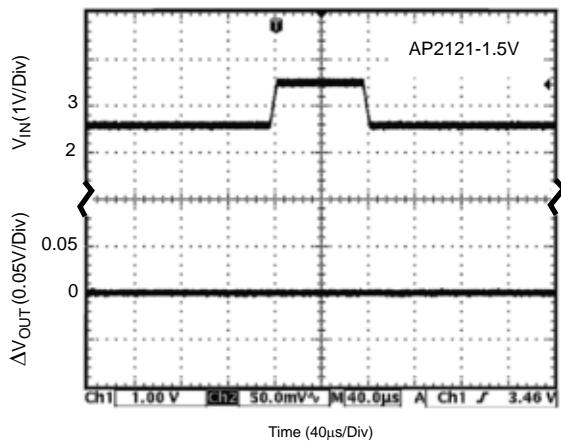


Figure 10. Line Transient
(Conditions: I_{OUT}=30mA, C_{IN}=1 μ F, C_{OUT}=1 μ F)

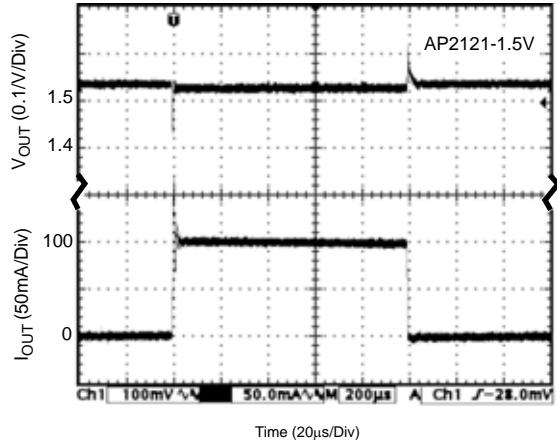


Figure 11. Load Transient
(Conditions: V_{IN}=2.5V, C_{IN}=1 μ F, C_{OUT}=1 μ F)



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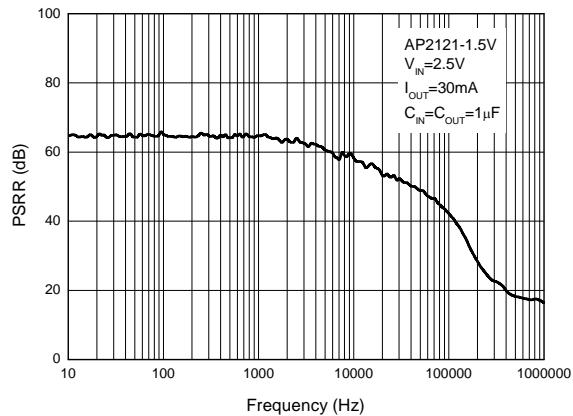


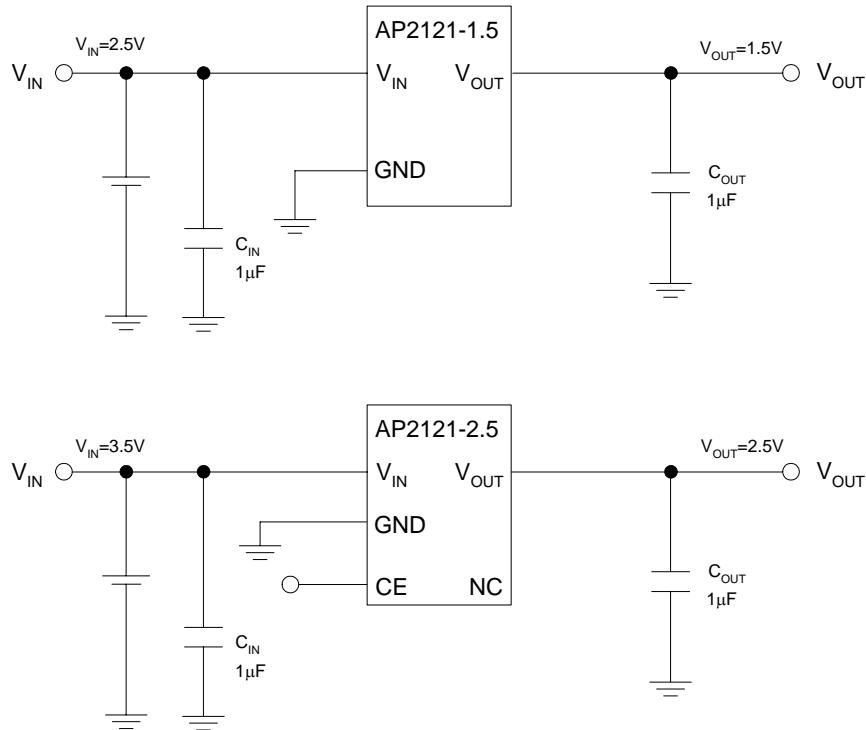
Figure 12. PSRR vs. Frequency



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Typical Application



Note: Filter capacitors are required at the AP2121's input and output. 1 μF capacitor is required at the input. The minimum output capacitance required for stability should be more than 1 μF with ESR from 0.1 Ω to 10 Ω . Ceramic capacitors are recommended.

Figure 13. Typical Application of AP2121



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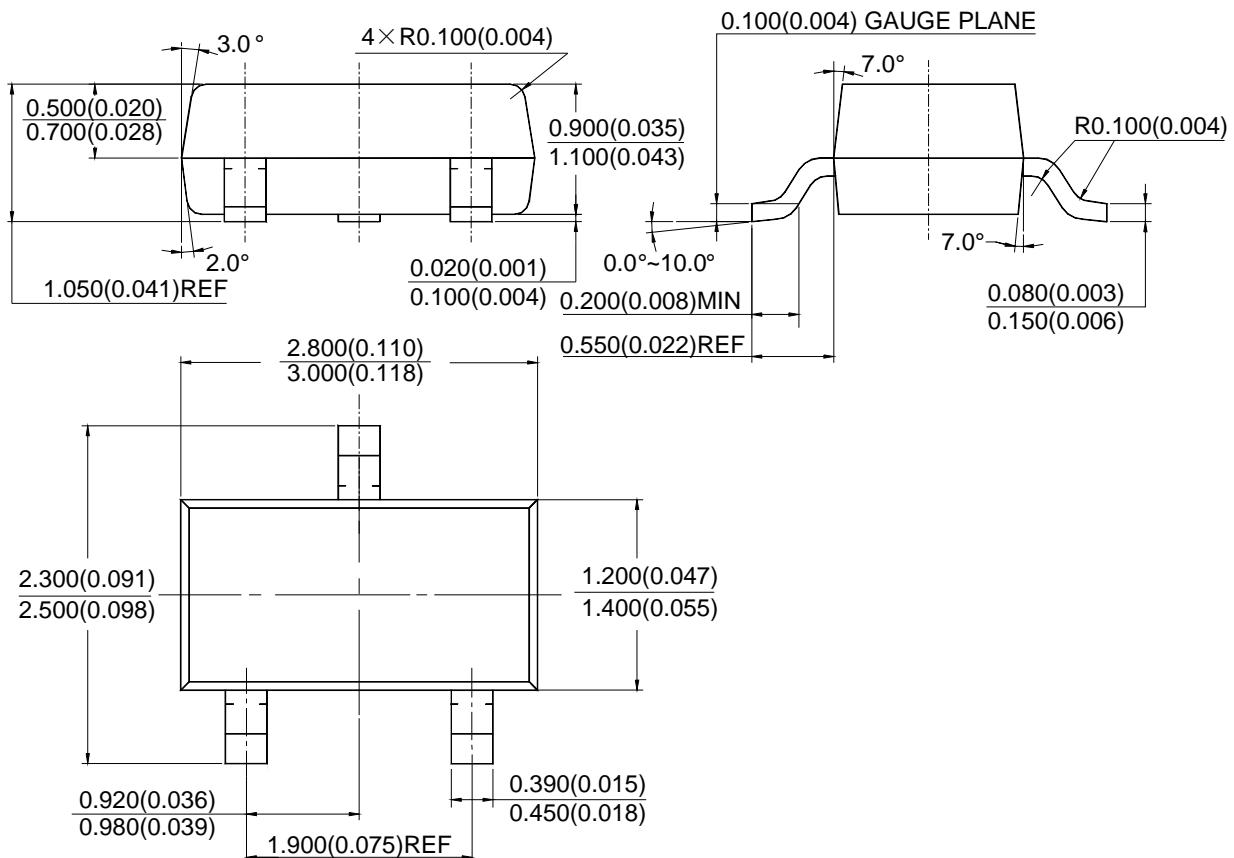
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Mechanical Dimensions

SOT-23-3

Unit: mm(inch)





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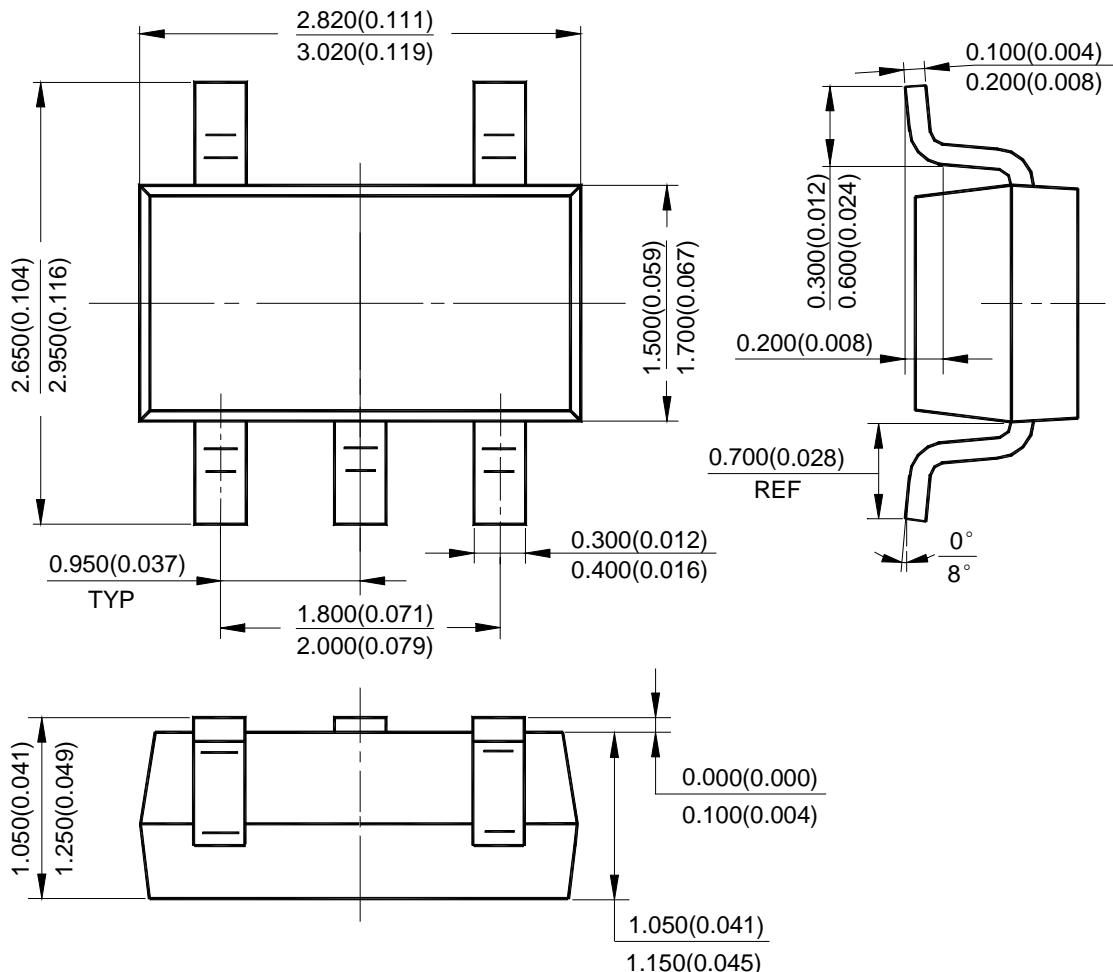
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Mechanical Dimensions (Continued)

SOT-23-5

Unit: mm(inch)





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