

HETERO JUNCTION FIELD EFFECT TRANSISTOR

NE3512S02

C TO Ku BAND SUPER LOW NOISE AMPLIFIER N-CHANNEL HJ-FET

FEATURES

- Super low noise figure and high associated gain
 NF = 0.35 dB TYP., G_a = 13.5 dB TYP. @ f = 12 GHz
- Micro-X plastic (S02) package

APPLICATIONS

- . C to Ku-band DBS LNB
- · Other C to Ku-band communication systems

ORDERING INFORMATION

Part Number	Order Number	Package	Quantity	Marking	Supplying Form
NE3512S02-T1C	NE3512S02-T1C-A	S02 (Pb-Free)	2 kpcs/reel	С	8 mm wide embossed taping
NE3512S02-T1D	NE3512S02-T1D-A		10 kpcs/reel		Pin 4 (Gate) faces the perforation side of the tape

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: NE3512S02

ABSOLUTE MAXIMUM RATINGS (TA = +25°C)

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	Vos	4	V
Gate to Source Voltage	Vgs	-3	V
Drain Current	lь	loss	mA
Gate Current	lg	100	μΑ
Total Power Dissipation	Ptot Note	165	mW
Channel Temperature	Tch	+125	°C
Storage Temperature	T _{stg}	-65 to +125	°C

Note Mounted on 1.08 cm² × 1.0 mm (t) glass epoxy PCB

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

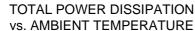
RECOMMENDED OPERATING CONDITIONS (Ta = +25°C)

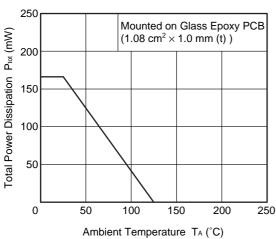
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	Vos	1	2	3	V
Drain Current	lσ	5	10	15	mA
Input Power	Pin	=	-	0	dBm

ELECTRICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)

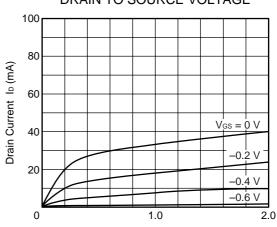
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Gate to Source Leak Current	Igso	Vgs = -3 V	ı	0.5	10	μА
Saturated Drain Current	IDSS	V _{DS} = 2 V, V _{GS} = 0 V	15	40	70	mA
Gate to Source Cutoff Voltage	VGS (off)	$V_{DS} = 2 \text{ V}, I_{D} = 100 \ \mu\text{A}$	-0.2	-0.7	-2.0	V
Transconductance	g m	V _{DS} = 2 V, I _D = 10 mA	40	55	-	mS
Noise Figure	NF	V _{DS} = 2 V, I _D = 10 mA, f = 12 GHz	_	0.35	0.5	dB
Associated Gain	Ga		12.5	13.5	=	dB

TYPICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)

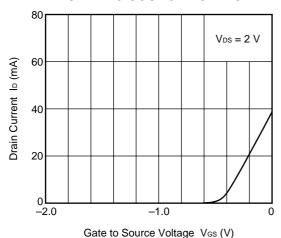




DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

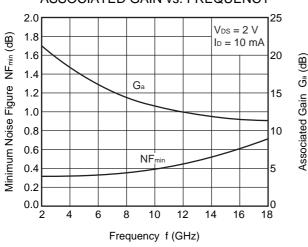


DRAIN CURRENT vs.
GATE TO SOURCE VOLTAGE

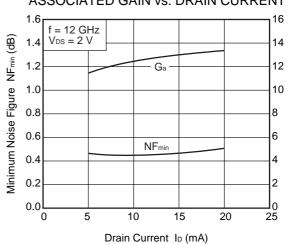


Drain to Source Voltage VDS (V)

MINIMUM NOISE FIGURE, ASSOCIATED GAIN vs. FREQUENCY



MINIMUM NOISE FIGURE, ASSOCIATED GAIN vs. DRAIN CURRENT



Remark The graphs indicate nominal characteristics.

Associated Gain Ga (dB)

S-PARAMETERS

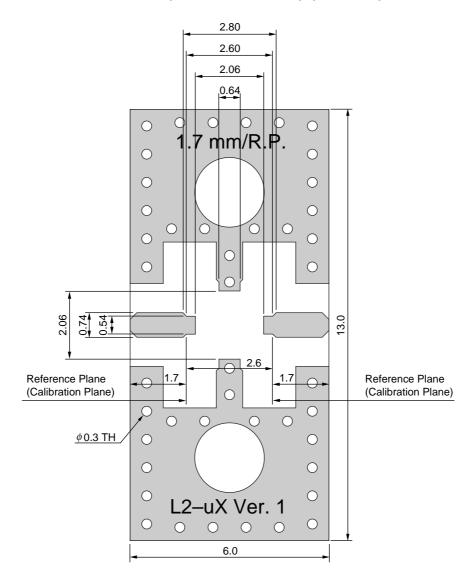
S-parameters/Noise parameters are provided on the NEC Compound Semiconductor Devices Web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

Click here to download S-parameters.

 $[\mathsf{RF} \ \mathsf{and} \ \mathsf{Microwave}] \to [\mathsf{Device} \ \mathsf{Parameters}]$

URL http://www.ncsd.necel.com/

RF MEASURING LAYOUT PATTERN (REFERENCE ONLY) (UNIT: mm)



RT/duroid 5880/ROGERS

t = 0.254 mm

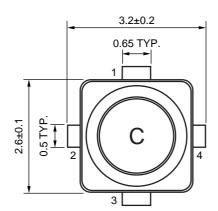
 $\epsilon r = 2.20$

tan delta = 0.0009 @10 GHz

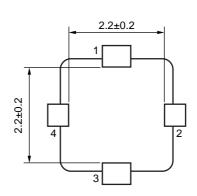
PACKAGE DIMENSIONS

S02 (UNIT: mm)

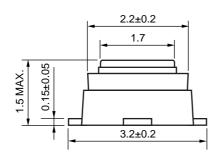
(Top View)



(Bottom View)



(Side View)



PIN CONNECTIONS

- 1. Source
- 2. Drain
- 3. Source
- 4. Gate

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).



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Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

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