NSDEMP11XV6T1, NSDEMP11XV6T5

Common Anode Quad Array Switching Diode

These Common Anode Epitaxial Planar QUAD Diodes are designed for use in ultra high speed switching applications. The NSDEMP11XV6T1 device is housed in the SOT–563 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Fast t_{rr}
- Low C_D
- These are Pb–Free Devices

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	80	Vdc
Peak Reverse Voltage	V _{RM}	80	Vdc
Forward Current	١ _F	100	mAdc
Peak Forward Current	I _{FM}	300	mAdc
Peak Forward Surge Current	I _{FSM} (Note 1)	2.0 _{www.Data}	Sheet Com

THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Мах	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	357 (Note 2) 2.9 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	350 (Note 2)	°C/W
Characteristic			
(Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	500 (Note 2) 4.0 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	250 (Note 2)	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

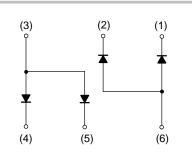
1. $t = 1 \mu S$

2. FR-4 @ Minimum Pad



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MARKING DIAGRAM



P9 = Device Code M = Date Code = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NSDEMP11XV6T1	SOT-563*	4000/Tape & Reel
NSDEMP11XV6T1G	SOT-563*	4000/Tape & Reel
NSDEMP11XV6T5	SOT-563*	8000/Tape & Reel
NSDEMP11XV6T5G	SOT-563*	8000/Tape & Reel

⁺For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*This package is inherently Pb-Free.

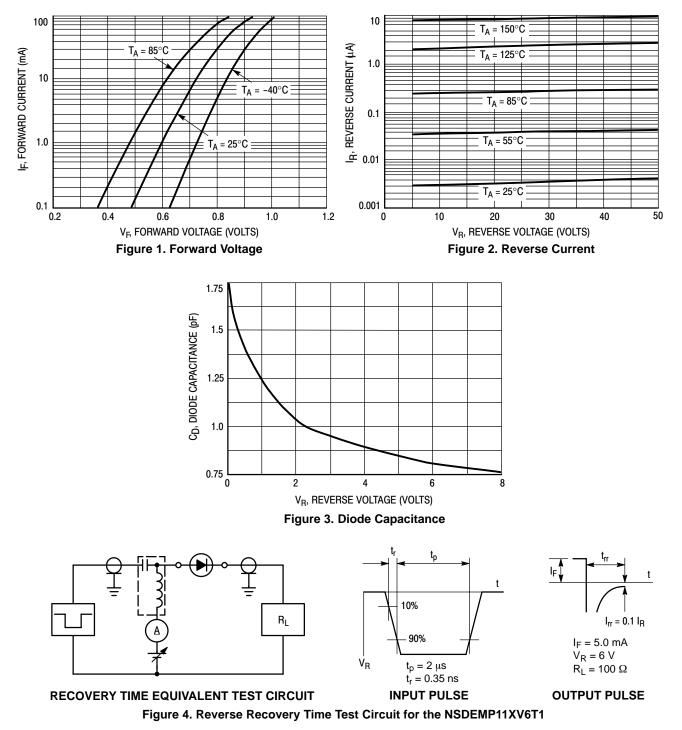
NSDEMP11XV6T1, NSDEMP11XV6T5

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

Characteristic	Symbol	Symbol Condition		Max	Unit
Reverse Voltage Leakage Current	I _R	V _R = 70 V	-	0.1	μAdc
Forward Voltage	V _F	I _F = 100 mA	-	1.2	Vdc
Reverse Breakdown Voltage	V _R	I _R = 100 μA	0	-	Vdc
Diode Capacitance	CD	V _R = 6.0 V, f = 1.0 MHz	-	3.5	pF
Reverse Recovery Time	t _{rr} (Note 3)	$I_F = 5.0 \text{ mA}, V_R = 6.0 \text{ V}, R_L = 100 \Omega, I_{rr} = 0.1 I_R$	-	4.0	ns

3. t_{rr} Test Circuit for NSDEMP11XV6T1 in Figure 4.

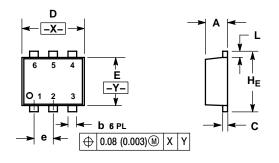
TYPICAL ELECTRICAL CHARACTERISTICS



NSDEMP11XV6T1, NSDEMP11XV6T5

PACKAGE DIMENSIONS

SOT-563, 6 LEAD CASE 463A-01 **ISSUE F**



NOTES

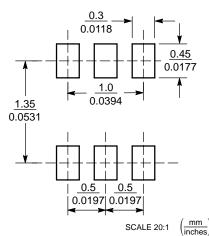
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETERS

3.

MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.50	0.55	0.60	0.020	0.021	0.023	
b	0.17	0.22	0.27	0.007	0.009	0.011	
С	0.08	0.12	0.18	0.003	0.005	0.007	
D	1.50	1.60	1.70	0.059	0.062	0.066	
Е	1.10	1.20	1.30	0.043	0.047	0.051	
e		0.5 BSC		0	0.02 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012	
HE	1.50	1.60	1.70	0.059	0.062	0.066	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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