

HRL0103C

Silicon Schottky Barrier Diode for Rectifying

REJ03G0367-0100

Rev.1.00

Aug 19, 2004

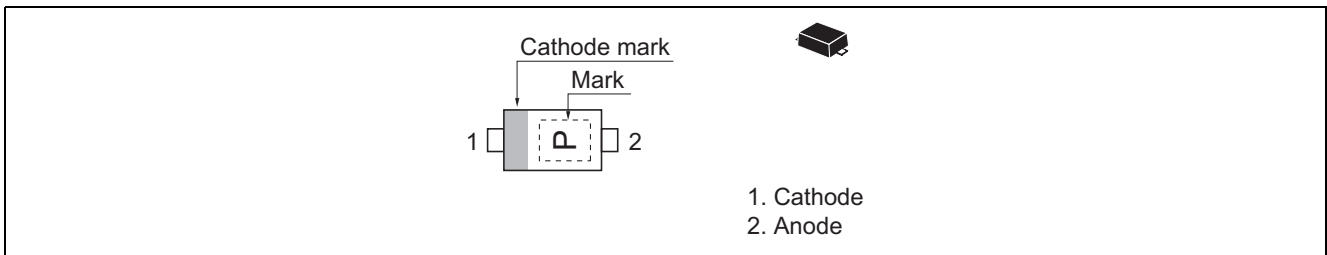
Features

- Low reverse voltage drop and suitable for high efficiency reverse current.
- Extremely small Flat Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HRL0103C	P	EFP

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}^{*1}	30	V
Reverse voltage	V_R	30	V
Average rectified current	I_O^{*1}	100	mA
Peak forward surge current	I_{FM}	300	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	1	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

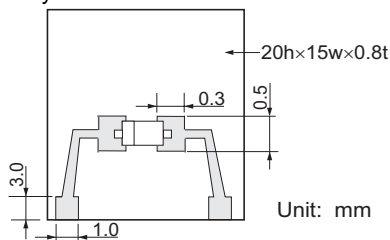
- Notes: 1. See from Fig.3 to Fig.5.
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.40	V	$I_F = 10 \text{ mA}$
	V_{F2}	—	—	0.60		$I_F = 100 \text{ mA}$
Reverse current	I_{R1}	—	—	0.1	μA	$V_R = 5 \text{ V}$
	I_{R2}	—	—	0.2		$V_R = 10 \text{ V}$
Capacitance	C	—	—	8.0	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
Thermal resistance	$R_{th(j-a)}$	—	800	—	°C/W	Polyimide board ^{*1}

- Notes: 1. Polyimide board



- Please do not use the soldering iron due to avoid high stress to the EFP package.
- The material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

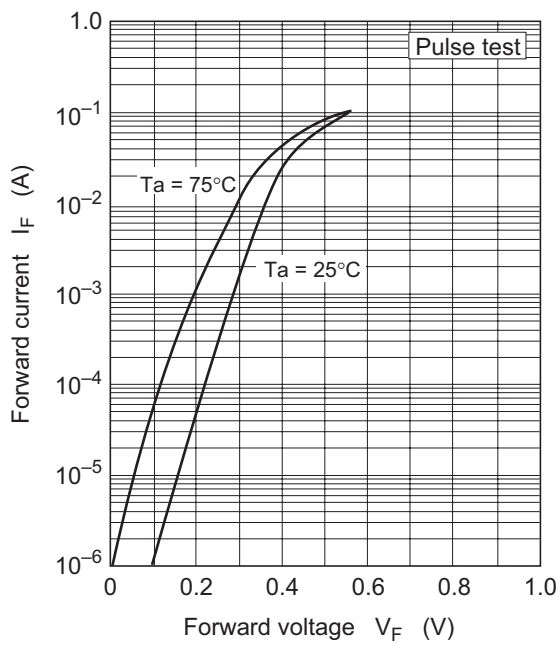


Fig.1 Forward current vs. Forward voltage

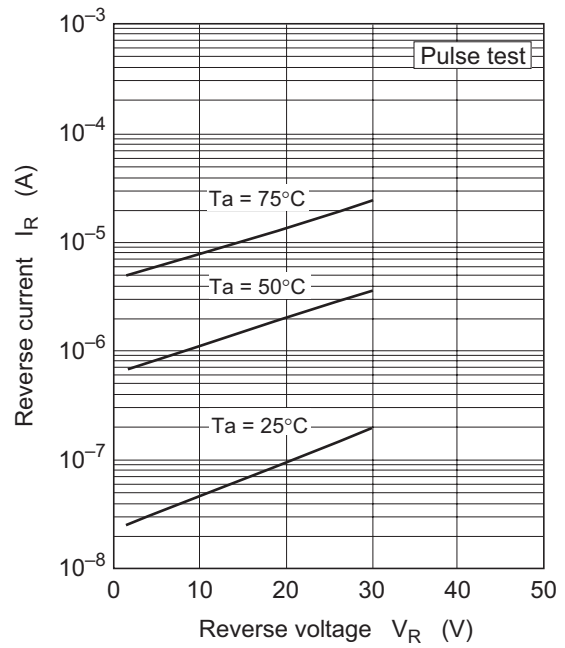


Fig.2 Reverse current vs. Reverse voltage

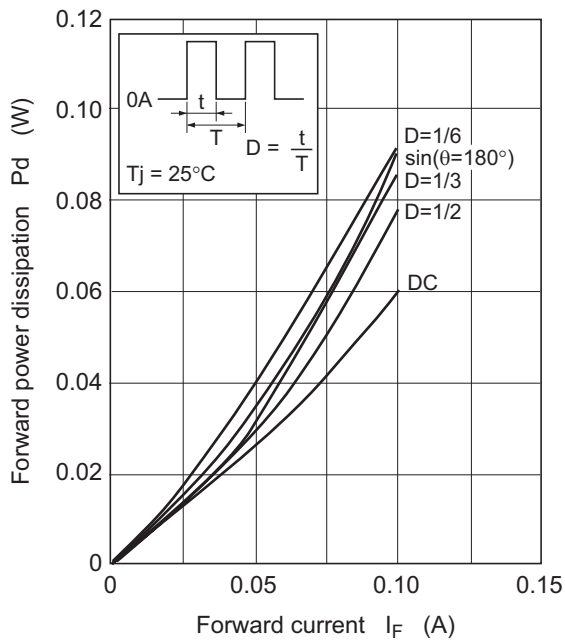


Fig3. Forward power dissipation vs. Forward current

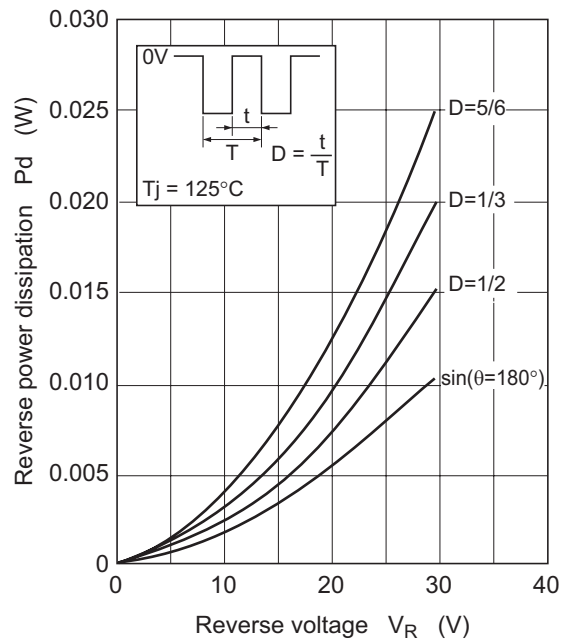


Fig4. Reverse power dissipation vs. Reverse voltage

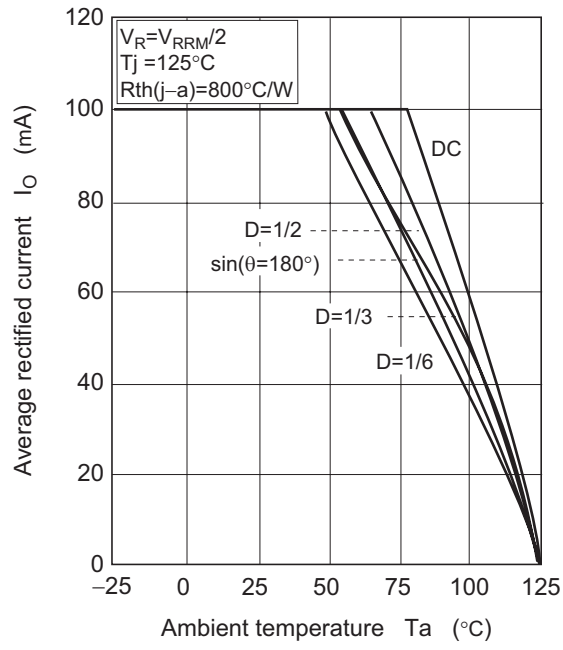
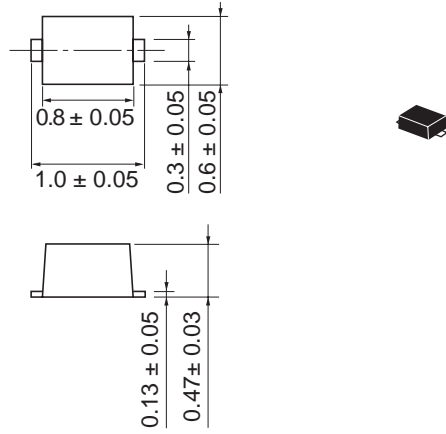


Fig.5 Average rectified current vs. Ambient temperature

Package Dimensions

As of January, 2003
Unit: mm



Package Code	EFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0007 g

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