NTMFS4899NF

Power MOSFET

30 V, 75 A, Single N-Channel, SO-8 FL

Features

- Integrated Schottky Diode
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Capacitance to Minimize Driver Losses
- Optimized Gate Charge to Minimize Switching Losses
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- CPU Power Delivery
- DC-DC Converters
- Low Side Switching

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Parameter			Symbol	Value	Unit
Drain-to-Source Vo	Itage		V_{DSS}	30	V
Gate-to-Source Voltage			V_{GS}	±20	V
Continuous Drain		T _A = 25°C	I _D	17.8	Α
Current R _{θJA} (Note 1)		T _A = 85°C		12.9	
Power Dissipation R _{θJA} (Note 1)		T _A = 25°C	P _D	2.70	W
Continuous Drain	1	T _A = 25°C	Ι _D	29.1	Α
Current R _{θJA} ≤ 10 sec		T _A = 85°C		21	
Power Dissipation $R_{\theta JA,} t \leq 10 \text{ sec}$	Steady State	T _A = 25°C	P _D	7.18	W
Continuous Drain		T _A = 25°C	Ι _D	10.4	Α
Current R _{θJA} (Note 2)		T _A = 85°C		7.5	
Power Dissipation R _{θJA} (Note 2)		T _A = 25°C	P _D	0.92	W
Continuous Drain		T _C = 25°C	Ι _D	75	Α
Current $R_{\theta JC}$ (Note 1)		T _C = 85°C		54	
Power Dissipation $R_{\theta JC}$ (Note 1)		T _C = 25°C	P _D	48	W
Pulsed Drain Current	t _p =10μs	T _A = 25°C	I _{DM}	188	Α
Current limited by pa	ackage	T _A = 25°C	I _{Dmaxpkg}	90	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	-55 to +150	°C
Source Current (Body Diode)			I _S	46	Α
Drain to Source dV/dt			dV/dt	6	V/ns
Single Pulse Drain-to-Source Avalanche Energy (V_{DD} = 50 V, V_{GS} = 10 V, I_L = 41 A_{pk} , L = 0.1 mH, R_G = 25 Ω)			EAS	84	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

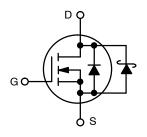


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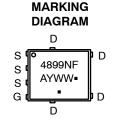
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V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX		
30 V	5.0 m Ω @ 10 V	7F A		
30 V	7.5 mΩ @ 4.5 V	75 A		

N-CHANNEL MOSFET







A = Assembly Location

Y = Year WW = Work Week • Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NTMFS4899NFT1G	SO-8FL (Pb-Free)	1500 / Tape & Reel
NTMFS4899NFT3G	SO-8FL (Pb-Free)	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case (Drain)	$R_{ heta JC}$	2.6	
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	46.3	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	136.2	*C/VV
Junction-to-Ambient - t ≤ 10 sec	$R_{ heta JA}$	17.4	

- Surface-mounted on FR4 board using 1 sq-in pad, 1 oz Cu.
 Surface-mounted on FR4 board using the minimum recommended pad size (50 mm², 1 oz Cu).

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS				ı			
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1.0 \text{ mA}$		30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /				27		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 24 V	T _J = 25 °C			500	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V				±100	nA
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D} = 1.0 \text{ mA}$		1.5		2.5	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				10		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 30 A		3.9	5.0	
			I _D = 15 A		3.8		
		V _{GS} = 4.5 V	I _D = 30 A		6.0	7.5	mΩ
			I _D = 15 A		5.8		1
Forward Transconductance	9FS	V _{DS} = 1.5 V, I _E) = 15 A		57		S
CHARGES AND CAPACITANCES				ı			
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 12 V			1600		pF
Output Capacitance	C _{OSS}				360		
Reverse Transfer Capacitance	C _{RSS}				165		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 4.5 V, V _{DS} = 15 V; I _D = 30 A			12.2		nC
Threshold Gate Charge	Q _{G(TH)}				1.6		
Gate-to-Source Charge	Q _{GS}				4.6		
Gate-to-Drain Charge	Q_{GD}				4.6		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V _{DS} = 15 V, I _D = 30 A			25		nC
SWITCHING CHARACTERISTICS (Note 4)	-			-	-	-	<u> </u>
Turn-On Delay Time	t _{d(ON)}	V_{GS} = 4.5 V, V_{DS} = 15 V, I_{D} = 15 A, R_{G} = 3.0 Ω			12.6		ns
Rise Time	t _r				20.3		
Turn-Off Delay Time	t _{d(OFF)}				20		
Fall Time	t _f				4.2		

- 3. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.
- 4. Switching characteristics are independent of operating junction temperatures.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS (N	ote 4)						
Turn-On Delay Time	t _{d(ON)}	V_{GS} = 10 V, V_{DS} = 15 V, I_{D} = 15 A, R_{G} = 3.0 Ω			8.8		ns
Rise Time	t _r				18.5		
Turn-Off Delay Time	t _{d(OFF)}				25.9		
Fall Time	t _f				2.5		
DRAIN-SOURCE DIODE CHARACTE	RISTICS						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _S = 2.0 A	T _J = 25°C		0.45	0.70	
			T _J = 125°C		0.43		V
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, } dl_{S}/dt = 100 \text{ A/}\mu\text{s,}$ $l_{S} = 30 \text{ A}$			19		ns
Charge Time	t _a				9.2		
Discharge Time	t _b				9.8		
Reverse Recovery Charge	Q _{RR}				5.7		nC
PACKAGE PARASITIC VALUES				-			
Source Inductance	L _S	T _A = 25°C			0.38		nΗ
Drain Inductance	L _D				0.005		1
Gate Inductance	L _G				1.84		
Gate Resistance	R_{G}				1.5	2.4	Ω

Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

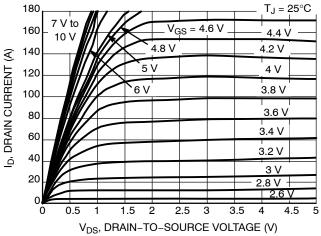


Figure 1. On-Region Characteristics

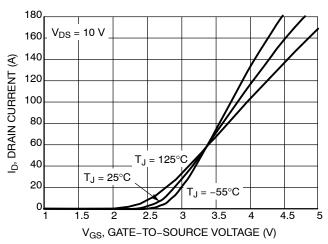


Figure 2. Transfer Characteristics

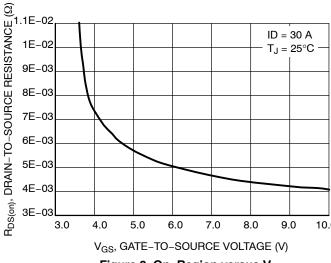


Figure 3. On-Region versus V_{GS}

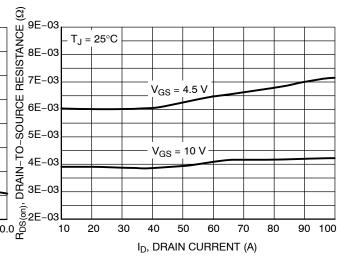


Figure 4. On-Resistance versus Drain Current and Gate Voltage

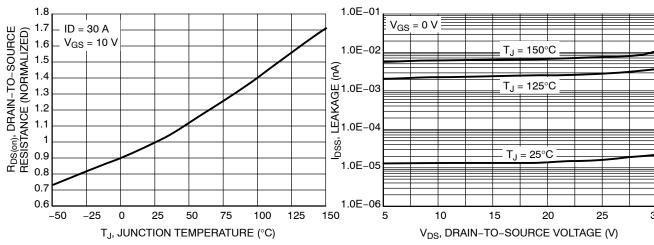


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS

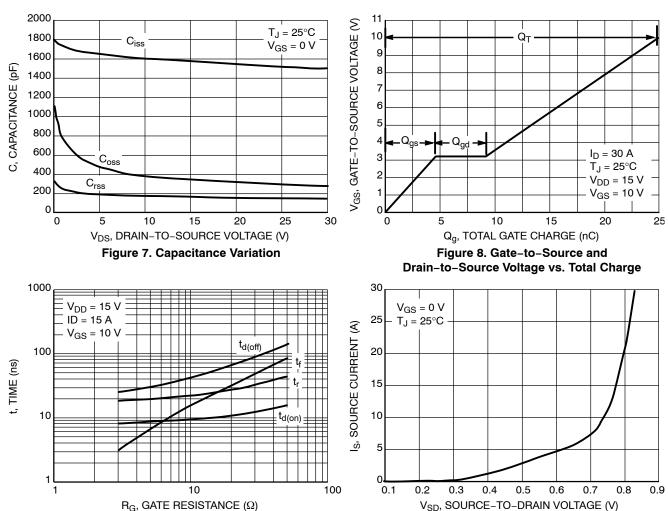


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

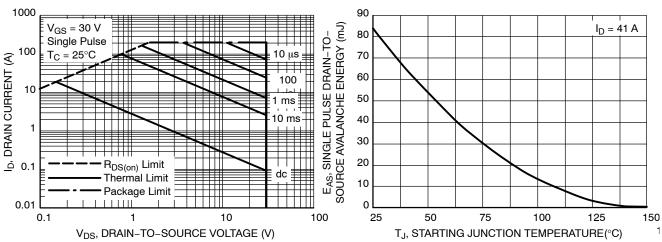
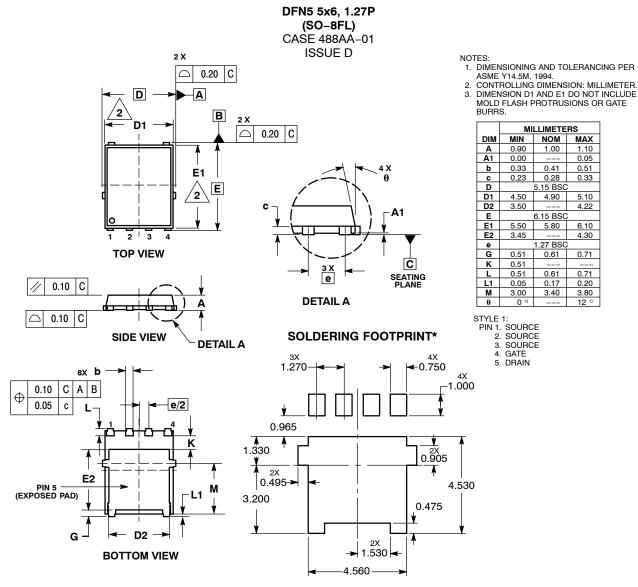


Figure 11. Maximum Rated Forward Biased Safe Operating Area

Figure 12. Maximum Avalanche Energy vs. Starting Junction Temperature

Figure 10. Diode Forward Voltage vs. Current

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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