

Silicon NPN Power Transistors

2SD728

DESCRIPTION

- With TO-3PN package
- Complement to type 2SB692

APPLICATIONS

- For low frequency power amplifier and power switching applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

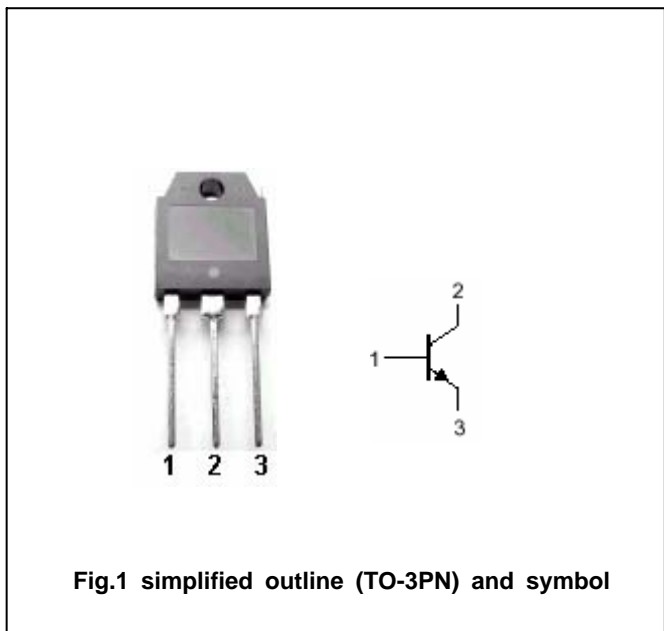


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings(Ta=25 )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	150	V
$V_{CEO}$	Collector-emitter voltage	Open base	150	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		6	A
$P_C$	Collector power dissipation	$T_C=25$	70	W
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-55~150	

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## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =50mA ; I <sub>B</sub> =0	150			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =1mA ; I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =5A ; I <sub>B</sub> =0.5A			2.0	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =150V; I <sub>E</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =5V; I <sub>C</sub> =0			0.1	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V	100			
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =5A ; V <sub>CE</sub> =5V	20			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V		7		MHz
C <sub>OB</sub>	Collector output capacitance	I <sub>E</sub> =0 ; f=1MHz; V <sub>CB</sub> =10V		160		pF

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PACKAGE OUTLINE

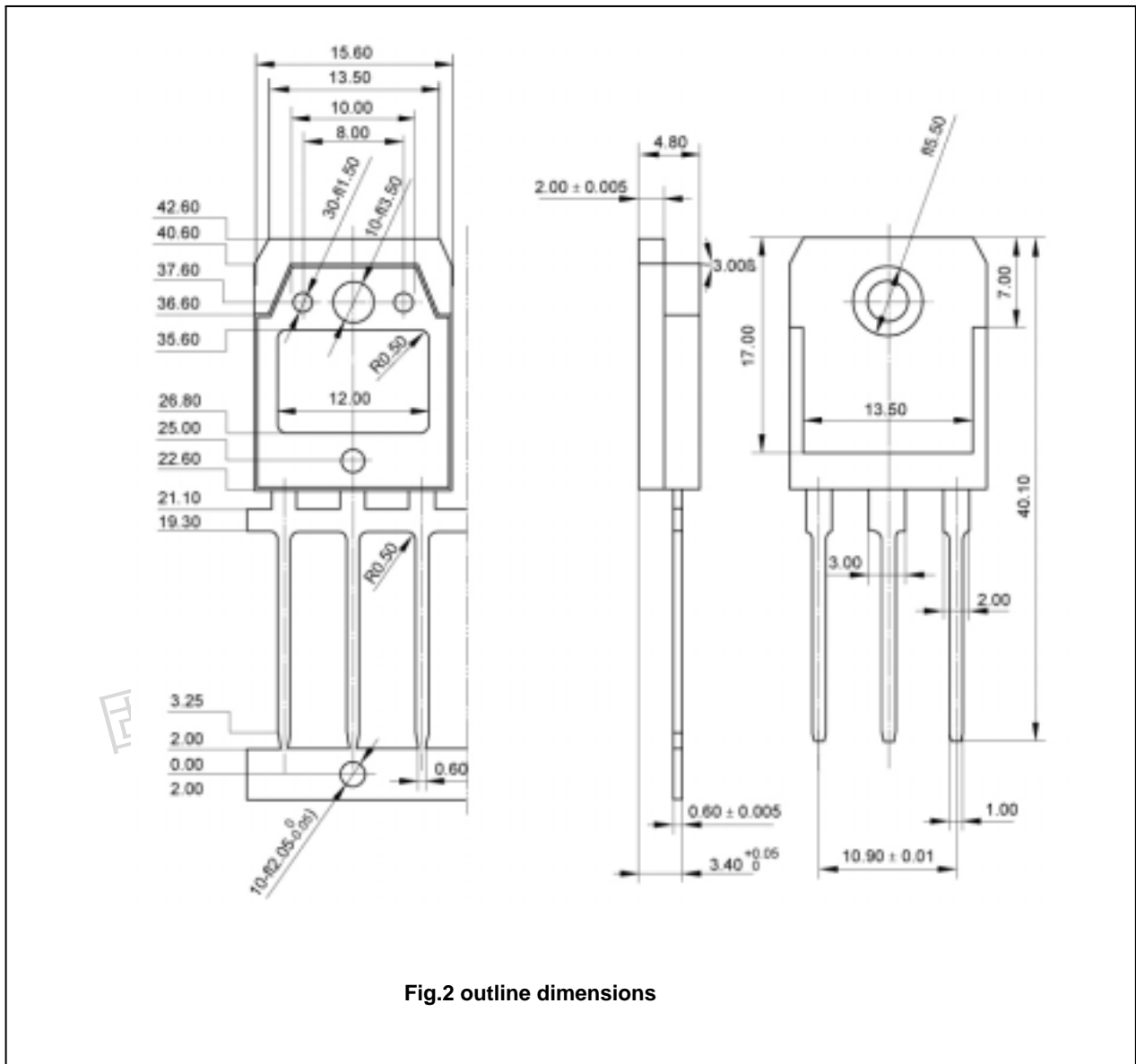


Fig.2 outline dimensions