

# PMEG3010EB

# 1 A very low V<sub>F</sub> MEGA Schottky barrier rectifier Rev. 01 — 1 December 2006 Pr

**Product data sheet** 

### **Product profile**

### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD523 (SC-79) ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

#### 1.2 Features

Forward current: I<sub>F</sub> ≤ 1 A

Reverse voltage: V<sub>R</sub> ≤ 30 V

Very low forward voltage

■ Ultra small and flat lead SMD plastic package

#### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F</sub>	forward current	$T_{sp} \le 55  ^{\circ}C$	-	-	1	Α
$V_R$	reverse voltage		-	-	30	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A	<u>[1]</u> _	610	680	mV

[1] Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 



## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Symbol
1	cathode	[1]	. 84
2	anode	1 2	1 🕂 2
			sym001

<sup>[1]</sup> The marking bar indicates the cathode.

## 3. Ordering information

Table 3. Ordering information

Type number	Package	ckage					
	Name	Description	Version				
PMEG3010EB	SC-79	plastic surface-mounted package; 2 leads	SOD523				

### 4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG3010EB	KA

# 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage		-	30	V
I <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 55 °C	-	1	Α
I <sub>FRM</sub>	repetitive peak forward current	$t_p \leq 1 \text{ ms}; \\ \delta \leq 0.25$	-	1	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	-	3	Α
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	<u>[1]</u> _	310	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		<del>-</del> 65	+150	°C
T <sub>stg</sub>	storage temperature		<del>-</del> 65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j\text{-}a)}$	thermal resistance from junction to ambient	in free air	[1][2]	-	400	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3] -	-	75	K/W

<sup>[1]</sup> For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.

### 7. Characteristics

Table 7. Characteristics

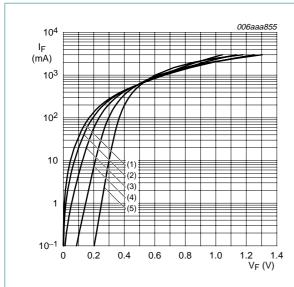
 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{F}$	forward voltage		<u>[1]</u>			
		$I_F = 0.1 \text{ mA}$	-	90	180	mV
		I <sub>F</sub> = 1 mA	-	150	200	mV
		I <sub>F</sub> = 10 mA	-	210	270	mV
		I <sub>F</sub> = 100 mA	-	295	360	mV
		I <sub>F</sub> = 500 mA	-	430	500	mV
		I <sub>F</sub> = 1 A	-	610	680	mV
$I_R$	reverse current	V <sub>R</sub> = 10 V	-	15	200	μΑ
		V <sub>R</sub> = 30 V	-	70	500	μΑ
$C_d$	diode capacitance	$V_R = 1 V; f = 1 MHz$	-	24	30	pF

<sup>[1]</sup> Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

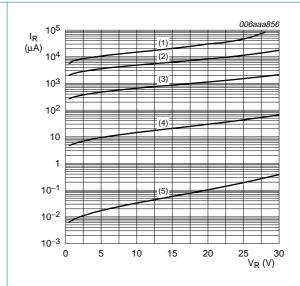
<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[3]</sup> Soldering point of cathode tab.



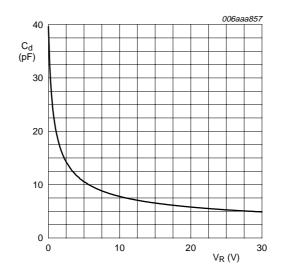
- (1)  $T_{amb} = 150 \, ^{\circ}C$
- (2)  $T_{amb} = 125 \, ^{\circ}C$
- (3)  $T_{amb} = 85 \, ^{\circ}C$
- (4)  $T_{amb} = 25 \, ^{\circ}C$
- (5)  $T_{amb} = -40 \, ^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1)  $T_{amb} = 150 \, ^{\circ}C$
- (2)  $T_{amb} = 125 \, ^{\circ}C$
- (3)  $T_{amb} = 85 \, ^{\circ}C$
- (4)  $T_{amb} = 25 \,^{\circ}C$
- (5)  $T_{amb} = -40 \, ^{\circ}C$

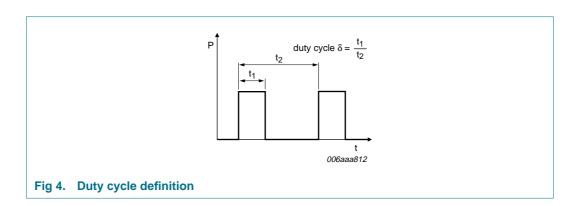
Fig 2. Reverse current as a function of reverse voltage; typical values



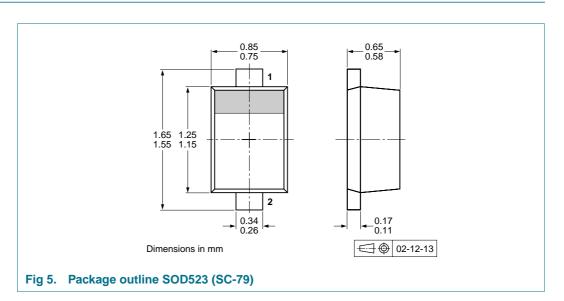
f = 1 MHz; T<sub>amb</sub> = 25 °C

Fig 3. Diode capacitance as a function of reverse voltage; typical values

### 8. Test information



## 9. Package outline



# 10. Packing information

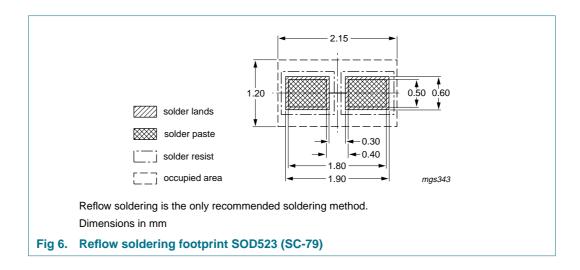
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packin	g quanti	ty
			3000	8000	10000
PMEG3010EB	SOD523	2 mm pitch, 8 mm tape and reel	-	-315	-
		4 mm pitch, 8 mm tape and reel	-115	-	-135

<sup>[1]</sup> For further information and the availability of packing methods, see Section 14.

## 11. Soldering



**PMEG3010EB** 

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# 12. Revision history

### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3010EB_1	20061201	Product data sheet	-	-

### 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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### 1 A very low V<sub>F</sub> MEGA Schottky barrier rectifier

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