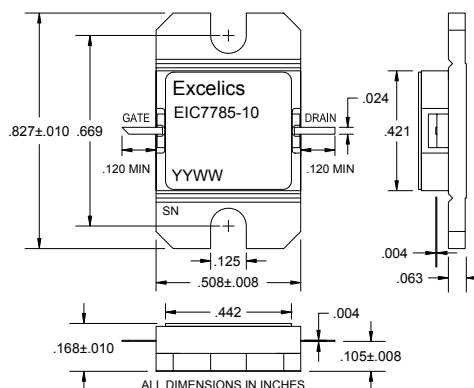


FEATURES

- 7.70– 8.50GHz Bandwidth
- Input/Output Impedance Matched to 50 Ohms
- +40.5 dBm Output Power at 1dB Compression
- 8.5 dB Power Gain at 1dB Compression
- 28% Power Added Efficiency
- -46 dBc IM3 at PO = 29.5 dBm SCL
- Hermetic Metal Flange Package
- 100% Tested for DC, RF, and R_{TH}



ELECTRICAL CHARACTERISTICS (T_a = 25°C)



Caution! ESD sensitive device.

SYMBOL	PARAMETERS/TEST CONDITIONS ¹	MIN	TYP	MAX	UNITS
P _{1dB}	Output Power at 1dB Compression V _{DS} = 10 V, I _{DSQ} ≈ 3200mA f = 7.70-8.50GHz	39.5	40.5		dBm
G _{1dB}	Gain at 1dB Compression V _{DS} = 10 V, I _{DSQ} ≈ 3200mA f = 7.70-8.50GHz	7.5	8.5		dB
ΔG	Gain Flatness V _{DS} = 10 V, I _{DSQ} ≈ 3200mA f = 7.70-8.50GHz			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression V _{DS} = 10 V, I _{DSQ} ≈ 3200mA f = 7.70-8.50GHz		28		%
I _{d1dB}	Drain Current at 1dB Compression f = 7.70-8.50GHz		3300	3700	mA
IM3	Output 3rd Order Intermodulation Distortion Δf = 10 MHz 2-Tone Test; P _{out} = 29.5 dBm S.C.L. ² V _{DS} = 10 V, I _{DSQ} ≈ 65% IDSS f = 8.50GHz	-43	-46		dBc
I _{DSS}	Saturated Drain Current V _{DS} = 3 V, V _{GS} = 0 V		5700	7100	mA
V _P	Pinch-off Voltage V _{DS} = 3 V, I _{DS} = 57 mA		-2.5	-4.0	V
R _{TH}	Thermal Resistance ³		2.5	3.0	°C/W

Note: 1) Tested with 50 Ohm gate resistor.

2) S.C.L. = Single Carrier Level.

3) Overall R_{th} depends on case mounting.

ABSOLUTE MAXIMUM RATING^{1,2}

SYMBOLS	PARAMETERS	ABSOLUTE ¹	CONTINUOUS ²
V _{ds}	Drain-Source Voltage	15	10V
V _{gs}	Gate-Source Voltage	-5	-4V
I _{gsf}	Forward Gate Current	104.4mA	34.8mA
I _{gsr}	Reserve Gate Current	-17.4mA	-5.8mA
P _{in}	Input Power	39.5dBm	@ 3dB Compression
T _{ch}	Channel Temperature	175 °C	175 °C
T _{stg}	Storage Temperature	-65 to +175 °C	-65 to +175 °C
P _t	Total Power Dissipation	50W	50W

Note: 1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.

Specifications are subject to change without notice.

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