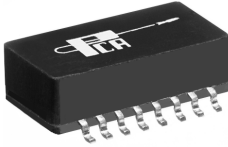


EPR1100S

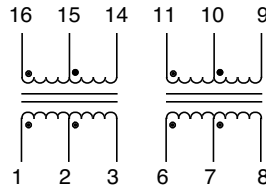


- Optimized for Leading Transceiver I.C.'s
- Meets or exceeds UL1459, UL1950 & EN60950 Standards
- Dual Surface Mount in a Small (SOIC) Package
- 1500 Vrms Isolation

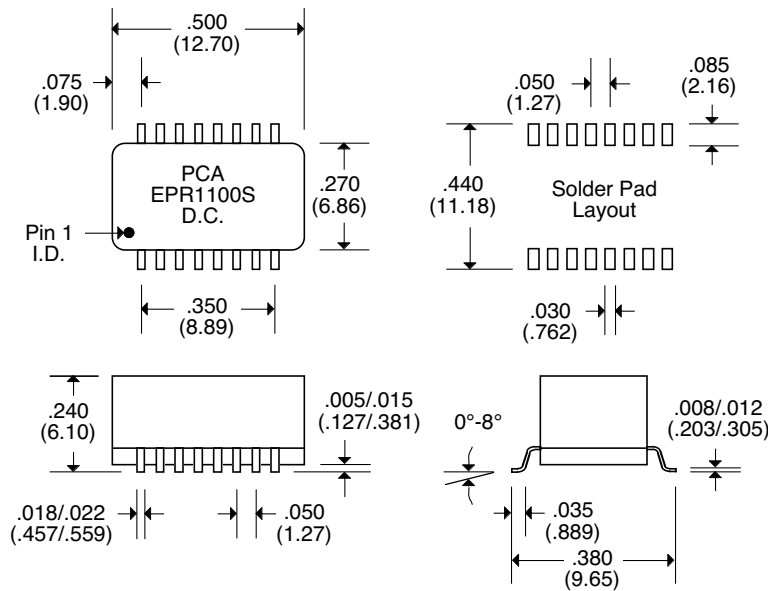
Electrical Parameters @ 25°C

Turns Ratio	OCL (mH Min.)	Cw/w (pF Max.)	LL (μ H Max.)	Pri. DCR (Ω Max.)	Sec. DCR (Ω Max.)	Primary Pins
1CT:2CT/1CT:1CT	1.2 / 1.2	30 / 30	0.6 / 0.6	0.7 / 0.7	0.7 / 1.0	16,14 / 6,8

Schematics



Package



Unless Otherwise Specified Dimensions are in Inches /mm \pm .010 / .25