

RKD703KK

Silicon Schottky Barrier Diode for High Speed Switching

REJ03G1833-0200

Rev.2.00

Nov 20, 2009

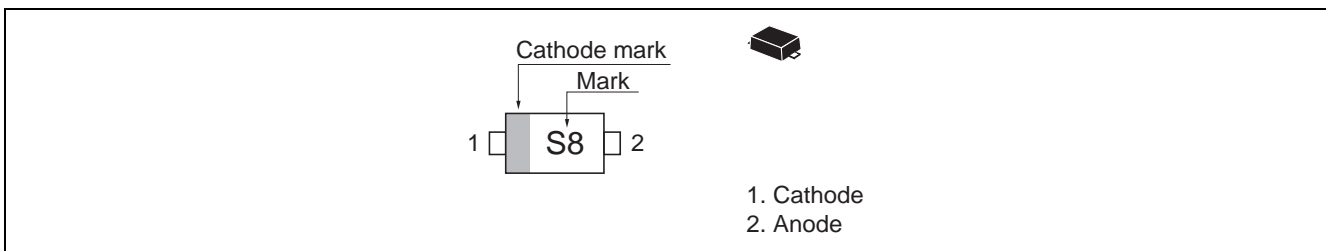
Features

- Low Power consumption (Low reverse leak current) and high speed (Low capacitance).
- We can support the lineup of environmental friendly halogen free type on your demand.
- Extremely small Flat Lead Package (SFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
RKD703KK R	S8	SFP	PUSF0002ZB-A	R (8,000 pcs / reel)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Average forward current	I_O^{*1}	100	mA
Non-Repetitive Peak forward surge current	I_{FSM}^{*2}	200	mA
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

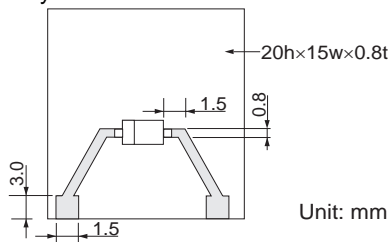
- Notes: 1. See from Fig.4 to Fig.6.
 2. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.25	V	$I_F = 1 \text{ mA}$
	V_{F2}	—	—	0.30	V	$I_F = 5 \text{ mA}$
	V_{F3}	—	—	0.35	V	$I_F = 20 \text{ mA}$
	V_{F4}	—	—	0.60	V	$I_F = 100 \text{ mA}$
Reverse current	I_{R1}	—	—	6	μA	$V_R = 10 \text{ V}$
	I_{R2}	—	—	50	μA	$V_R = 30 \text{ V}$
Capacitance	C	—	—	5	pF	$V_R = 1 \text{ V}, f = 1 \text{ MHz}$
Thermal resistance	$R_{th<j-a>}$	—	600	—	°C/W	Polyimide board ^{*1}

- Notes: 1. Polyimide board



2. In the SFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristics

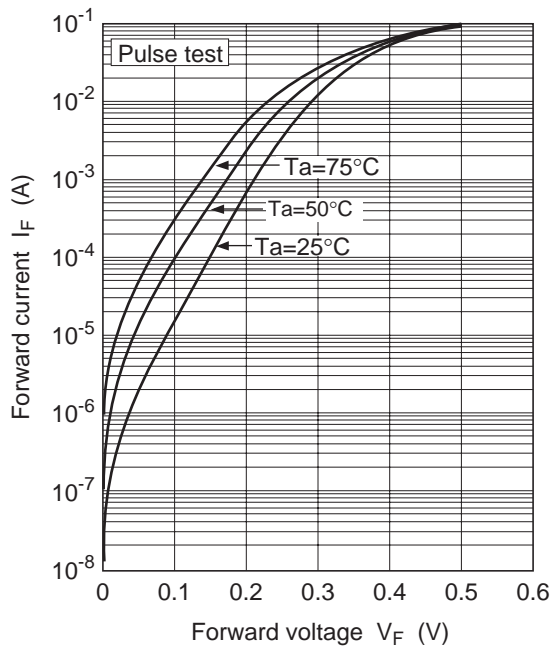


Fig.1 Forward current vs. Forward voltage

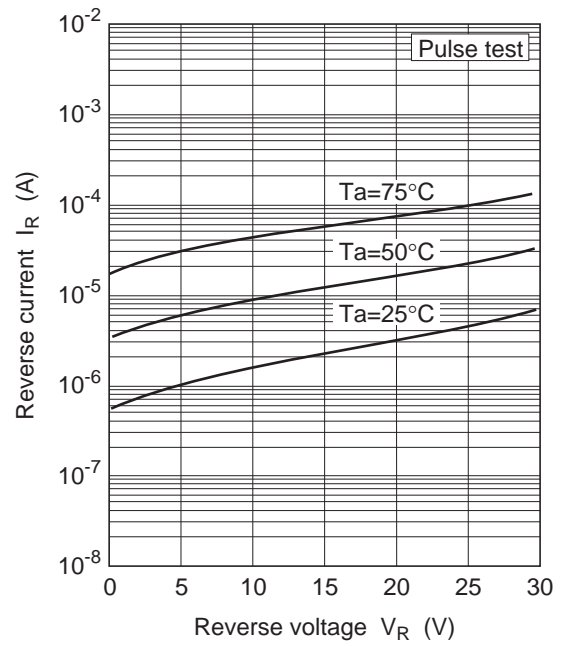


Fig.2 Reverse current vs. Reverse voltage

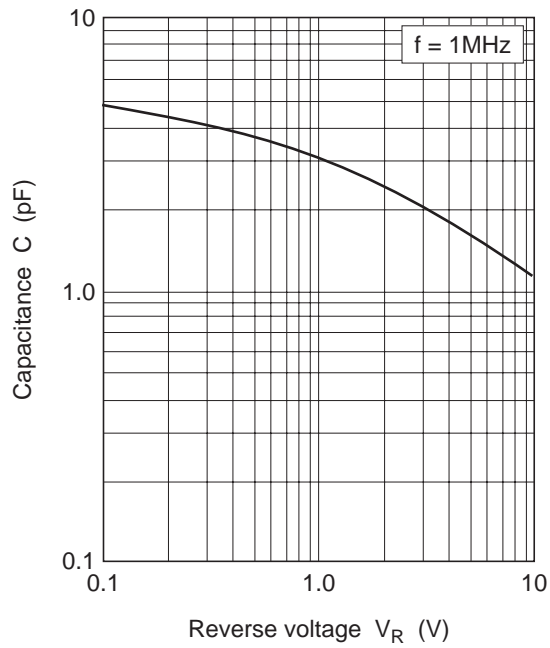


Fig.3 Capacitance vs. Reverse voltage

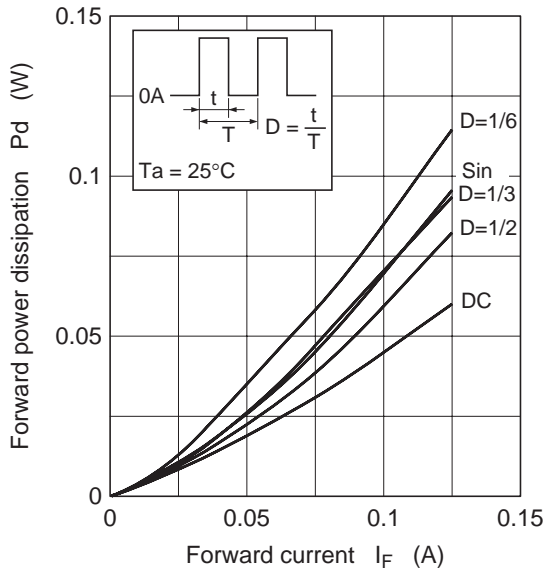


Fig.4 Forward power dissipation vs. Forward current

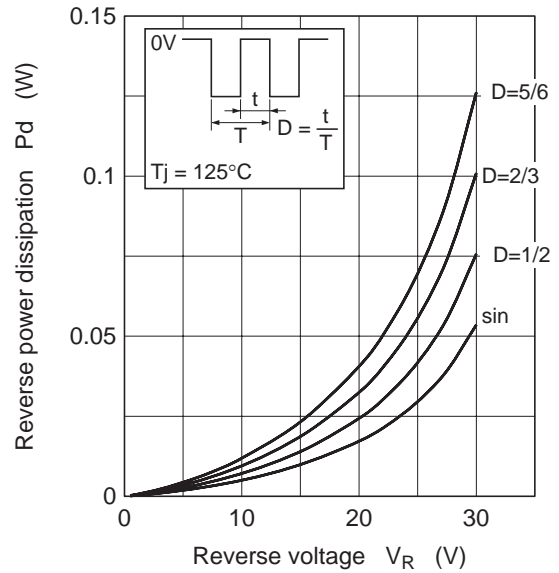


Fig.5 Reverse power dissipation vs. Reverse voltage

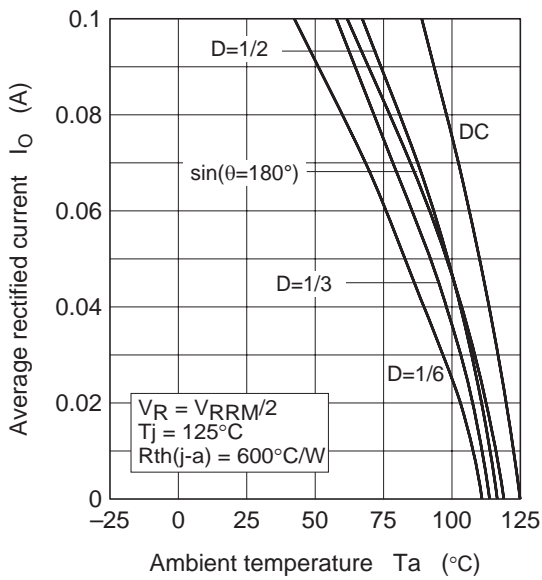
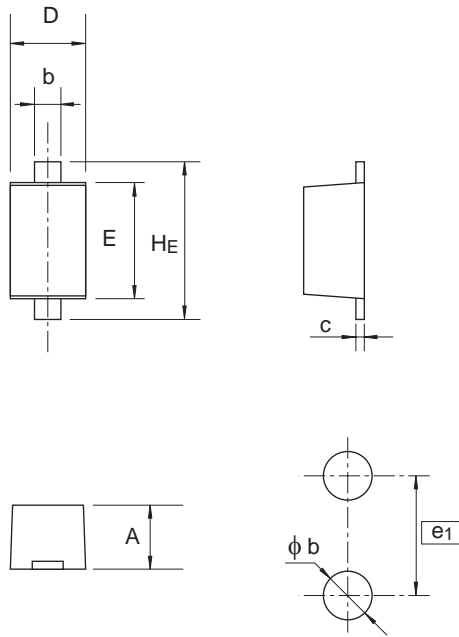


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
SFP	—	PUSF0002ZB-A	SFP / SFPV	0.0010g



Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	—	0.55
b	0.25	0.30	0.35
c	0.08	0.13	0.18
D	0.55	0.60	0.65
E	0.90	1.00	1.10
H_E	1.30	1.40	1.50
ϕb	—	0.50	—
e_1	—	1.40	—

Notes:

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450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510