CHENMKO ENTERPRISE CO.,LTD

CHM62A2PAPT

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 20 Volts CURRENT 48 Ampere

Lead free devices

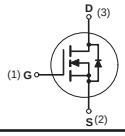
APPLICATION
* Servo motor control.
* Power MOSFET gate drivers.
* Other switching applications.

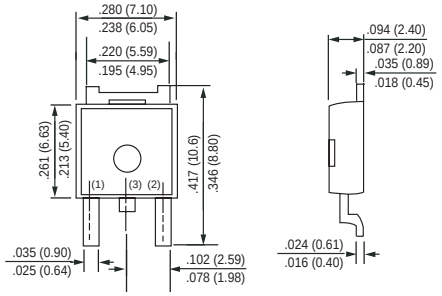
FEATURE
* Small package. (TO-252A)
* Super high dense cell design for extremely low $R_{DS(ON)}$.
* High power and current handing capability.

CONSTRUCTION
* N-Channel Enhancement

TO-252A

CIRCUIT





1 Gate
2 Source
3 Drain(Heat Sink)

Dimensions in inches and (millimeters)

TO-252A

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM62A2PAPT	Units
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	±12	V
I_D	Maximum Drain Current - Continuous	48	A
	- Pulsed (Note 3)	140	
P_D	Maximum Power Dissipation at Tc = 25°C	48	W
T_J	Operating Temperature Range	-55 to 150	°C
T_{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec
2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%
3. Repetitive Rating , Pulse width linited by maximum junction temperature
4. Guaranteed by design , not subject to production trsting

Thermal characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W

RATING CHARACTERISTIC CURVES (CHM62A2PAPT)

Electrical Characteristics T_A = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 µA	20			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 20 V, V_{GS} = 0 V			1	µA
I_{GSSF}	Gate-Body Leakage	V_{GS} = 12V,V_{DS} = 0 V			+100	nA
I_{GSSR}	Gate-Body Leakage	V_{GS} = -12V, V_{DS} = 0 V			-100	nA
ON CHARACTERISTICS (Note 2)						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 µA	0.5		1.2	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=4.5V, I_D=18A		10	12	mΩ
		V_{GS}=2.5V, I_D=9A		13	17	
g_{FS}	Forward Transconductance	V_{DS} =5V, I_D = 18A		10		S
SWITCHING CHARACTERISTICS (Note 4)						
Q_g	Total Gate Charge	V_{DS}=20V, I_D=18A V_{GS}=5V		35	45	nC
Q_{gs}	Gate-Source Charge			4		
Q_{gd}	Gate-Drain Charge			12		
t_{on}	Turn-On Time	V_{DD}= 10V I_D= 18A, V_{GS}= 5 V R_{GEN}= 3.3Ω		17	35	nS
t_r	Rise Time			12	25	
t_{off}	Turn-Off Time			55	110	
t_f	Fall Time			30	60	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
I_S	Drain-Source Diode Forward Current				45	A
V_{SD}	Drain-Source Diode Forward Voltage	I_S = 45A, V_{GS} = 0 V			1.3	V