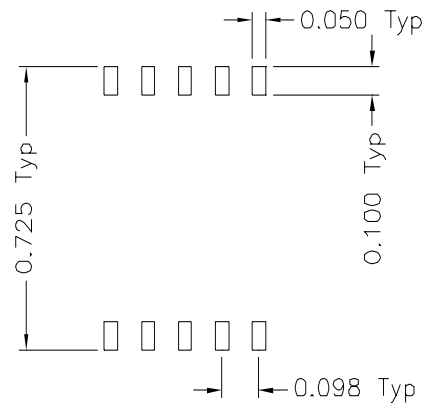
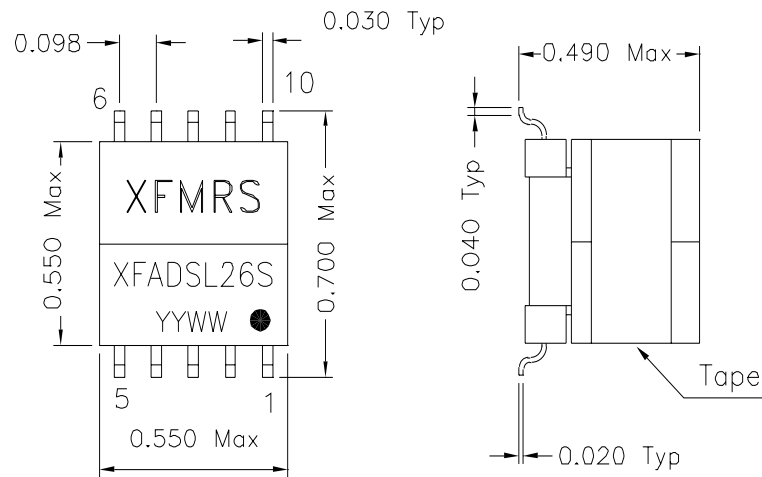
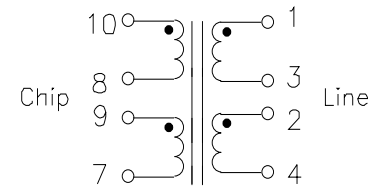


1. Dimensions:



Suggested PCB Layout

2.Schematic:



3.Electrical Specifications: @25°C

Turns Ratio: (P10-7):(P1-4) = 1CS:1CS±2% (TIE P3-2,P8-9)

PIN1-4 OCL: 480uH ±10% @10KHz 0.1V (TIE PIN2-3) Ls

PIN1-4 LL: 10uH Max @100KHz 0.1V

(CONNECT PIN2-3, Short 10-7) Ls

DC Res.: P10-8,P9-7 0.400 Ohms±15%

P1-3,2-4 0.500 Ohms ±15%

Insertion Loss: 1.0dB Max 100KHz & 1.1MHz

Long. Bal.: 40dB Min 30KHz-1.1MHz

HIPOT: 2000VAC Chip-Line

Designed to meet UL1950 requirements for Supplementary Insulation with 250 working volts.

DOC. REV A/1

XFMRS Inc	Title: TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH		P/N: XFADSL26S
SHEET 1 OF 1	DWN.	廖玉坤	Feb-29-00
	CHK.	李清儿	Feb-29-00
	APP.	Joe Huff	Feb-29-00