

Silicon NPN Power Transistors

2SD2333

**DESCRIPTION**

- With TO-3PML package
- High breakdown voltage
- Built-in damper diode

**APPLICATIONS**

- Color TV horizontal deflection output applications

**PINNING**

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

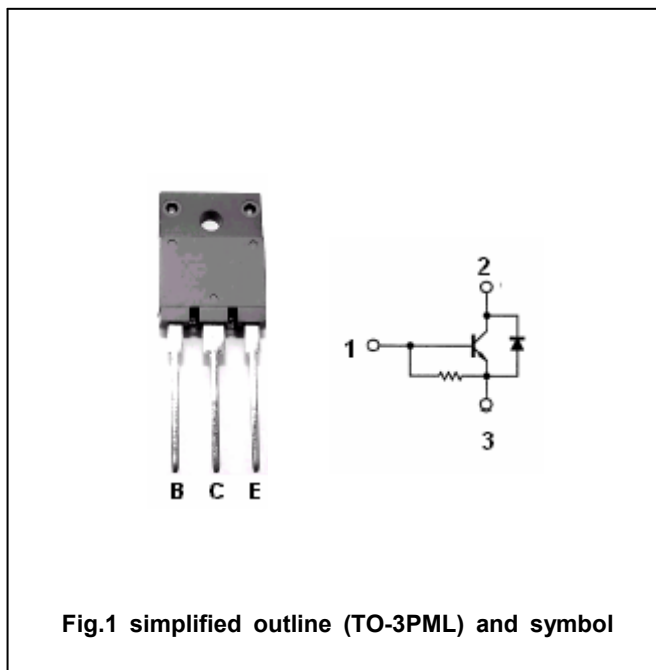


Fig.1 simplified outline (TO-3PML) and symbol

**Absolute maximum ratings(Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	1500	V
$V_{CEO}$	Collector-emitter voltage	Open base	600	V
$V_{EBO}$	Emitter-base voltage	Open collector	5	V
$I_C$	Collector current		5	A
$I_{BM}$	Base current-peak		2.5	A
$P_C$	Collector power dissipation	$T_C=25^\circ C$	80	W
$T_j$	Junction temperature		150	°C
$T_{stg}$	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =100mA; I <sub>B</sub> =0	800			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =200mA, I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8A			5.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =500V; I <sub>E</sub> =0			10	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =1A; V <sub>CE</sub> =5V	8			
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> =5A			2.0	V

