

SANYO Semiconductors DATA SHEET

SFT1101 — PNP Epitaxial Planar Silicon Transistor High-Voltage Switching Applications

Applications

· DC / DC converters, relay drivers, lamp drivers, motor drivers.

Features

- · Adoption of FBET, MBIT processes.
- · Large current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · High allowable power dissipation.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-120	V
Collector-to-Emitter Voltage	VCES		-120	V
Collector-to-Emitter Voltage	VCEO		-120	V
Emitter-to-Base Voltage	VEBO		-7	V
Collector Current	IC		-2.5	А
Collector Current (Pulse)	ICP		-4	Α
Base Current	ΙΒ		-500	mA
Collector Dissipation	De		1	W
	PC	Tc=25°C	15	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Marking: T1101

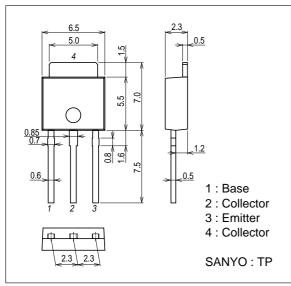
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Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Llmit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	VCB=-80V, IE=0A			-1	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =-5V, I _C =0A			-1	μΑ
DC Current Gain	hFE	V _{CE} =-5V, I _C =-100mA	200		560	
Gain-Bandwidth Product	fŢ	VCE=-10V, IC=-100mA		75		MHz
Output Capacitance	Cob	V _{CB} =-10V, f=1MHz		21		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =-1A, I _B =-100mA		-150	-270	mV
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C =-1A, I _B =-100mA		-0.85	-1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0A	-120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =-100μA, R _{BE} =0Ω	-120			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =-1mA, R _{BE} =∞	-120			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =-10μA, I _C =0A	-7			V
Turn-ON Time	ton	See specified Test Circuit.		55		ns
Storage Time	t _{stg}	See specified Test Circuit.		840		ns
Fall Time	tf	See specified Test Circuit.		40		ns

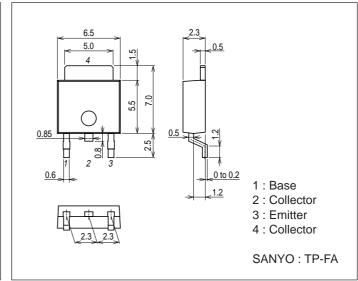
Package Dimensions

unit : mm (typ) 7518-003

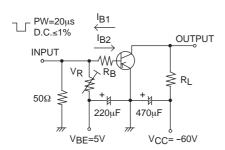


Package Dimensions

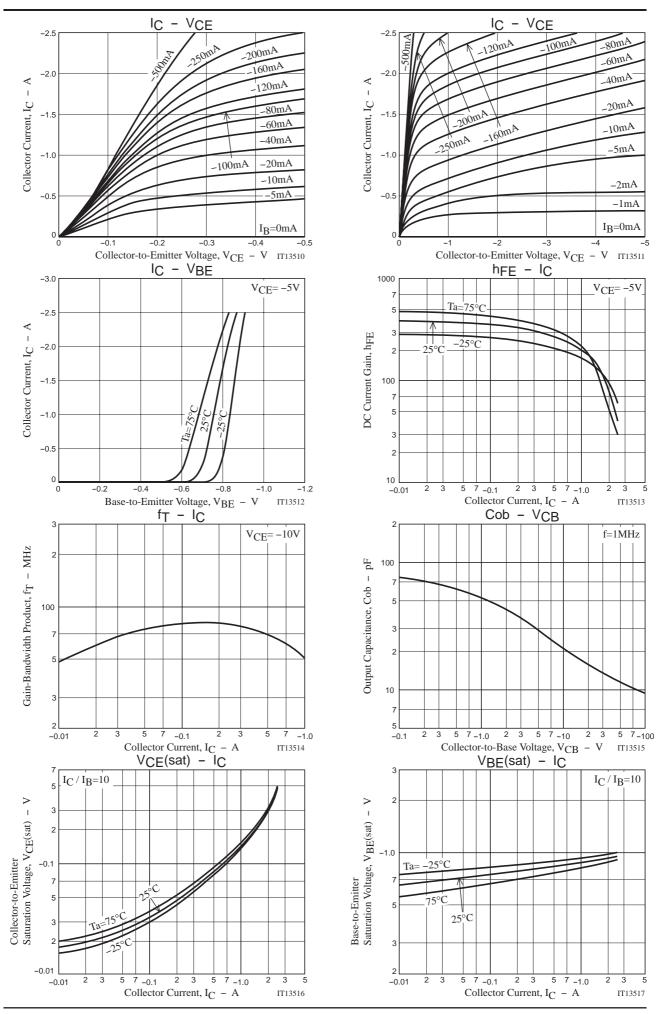
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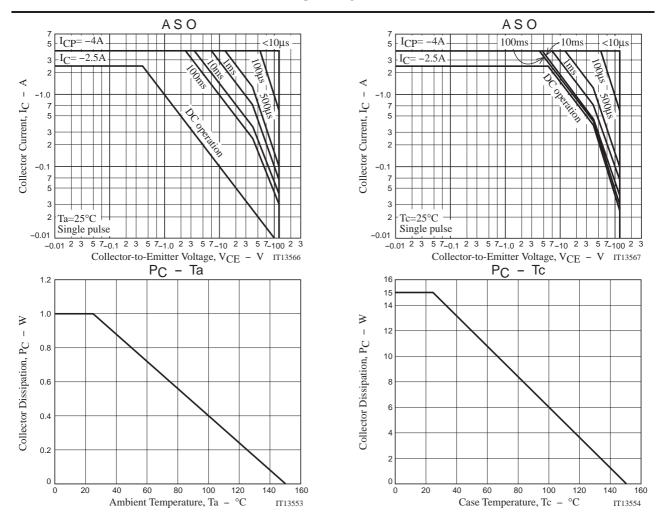


Switching Time Test Circuit



 $I_{C} = -10I_{B1} = 10I_{B2} = -0.7A$





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