

MBRS2H100T3G

Surface Mount Schottky Power Rectifier SMB Power Surface Mount Package

This device employs the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

Features

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guard-Ring for Overvoltage Protection
- Low Forward Voltage Drop
- This is a Pb-Free Device

Mechanical Characteristics

- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 95 mg (Approximately)
- Cathode Polarity Band
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- ESD Ratings: Machine Model = B, Human Body Model = 3B
- Device Meets MSL1 Requirements

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	100	V
Average Rectified Forward Current	I_O	2.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	130	A
Storage Temperature Range	T_{stg}	-65 to +175	°C
Operating Junction Temperature (Note 1)	T_J	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.



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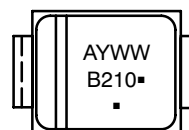
<http://onsemi.com>

SCHOTTKY BARRIER RECTIFIER 2.0 AMPERES, 100 VOLTS



SMB
CASE 403A
PLASTIC

MARKING DIAGRAM



B210 = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
MBRS2H100T3G	SMB (Pb-Free)	2500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MBRS2H100T3G

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	$R_{\theta JL}$	14	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient (Note 3)	$R_{\theta JA}$	71	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value		Unit
		$T_J = 25^{\circ}\text{C}$	$T_J = 125^{\circ}\text{C}$	
Maximum Instantaneous Forward Voltage (Note 4) ($I_F = 2.0 \text{ A}$)	V_F	0.79	0.65	V
Maximum Instantaneous Reverse Current (Note 4) ($V_R = 100 \text{ V}$)	I_R	0.008	1.5	mA

- Mounted with minimum recommended pad size, PC Board FR4.
- 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.
- Pulse Test: Pulse Width $\leq 250 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

TYPICAL CHARACTERISTICS

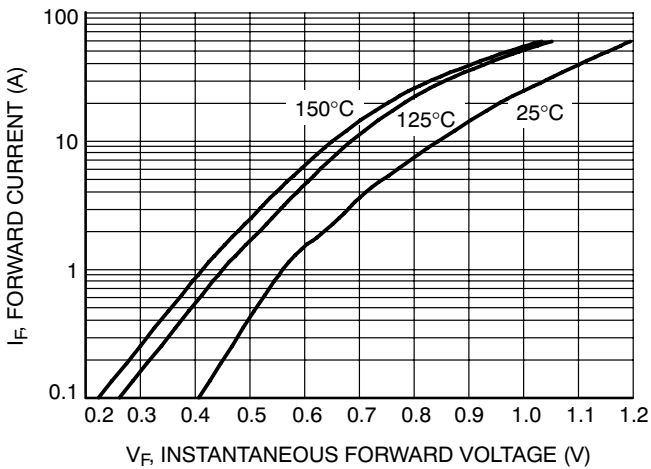


Figure 1. Typical Forward Voltage

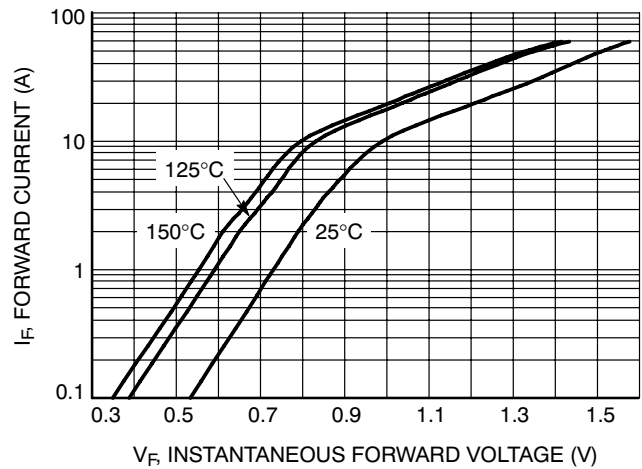


Figure 2. Maximum Forward Voltage

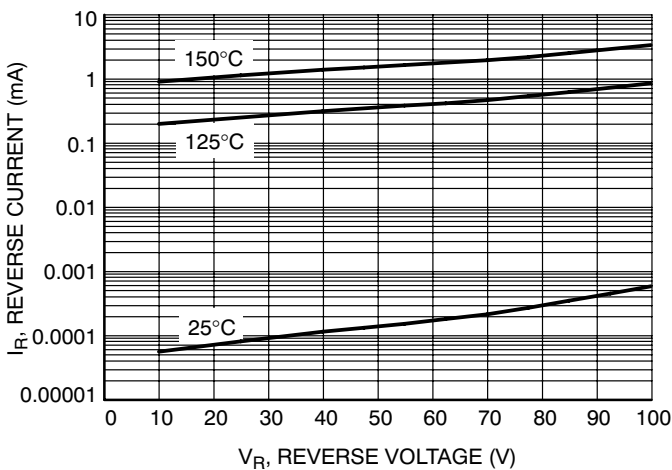


Figure 3. Typical Reverse Current

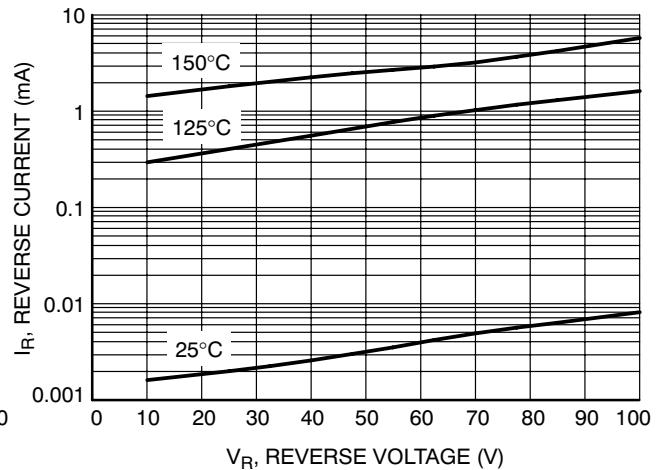


Figure 4. Maximum Reverse Current

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TYPICAL CHARACTERISTICS

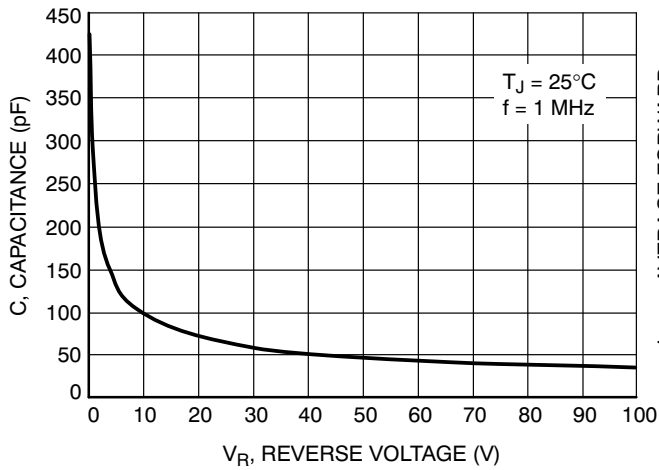


Figure 5. Typical Capacitance

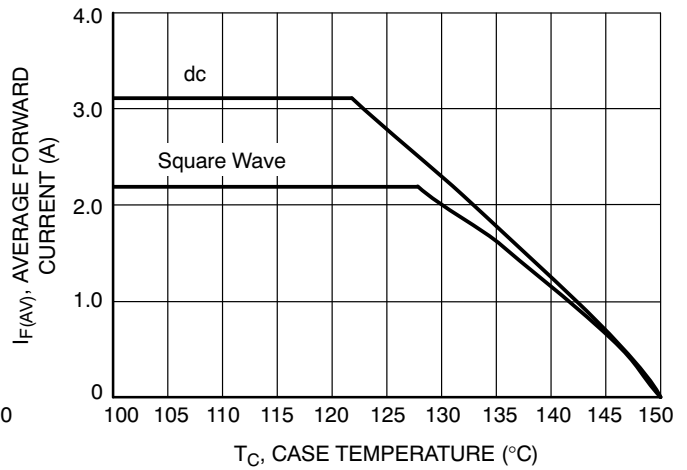


Figure 6. Current Derating - Lead

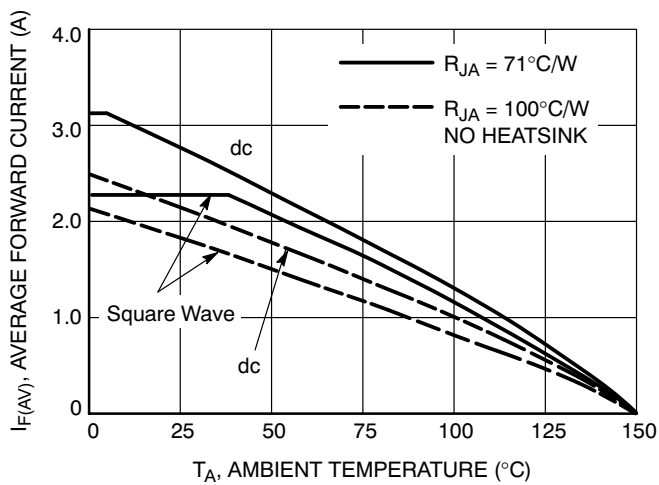


Figure 7. Current Derating, Ambient, Per Leg

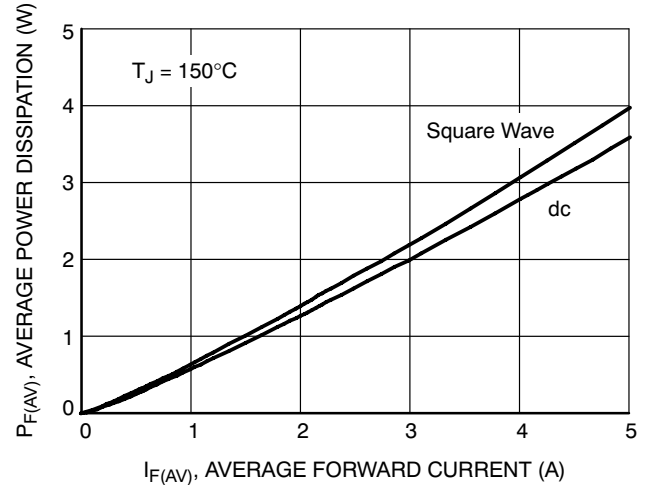


Figure 8. Maximum Forward Power Dissipation

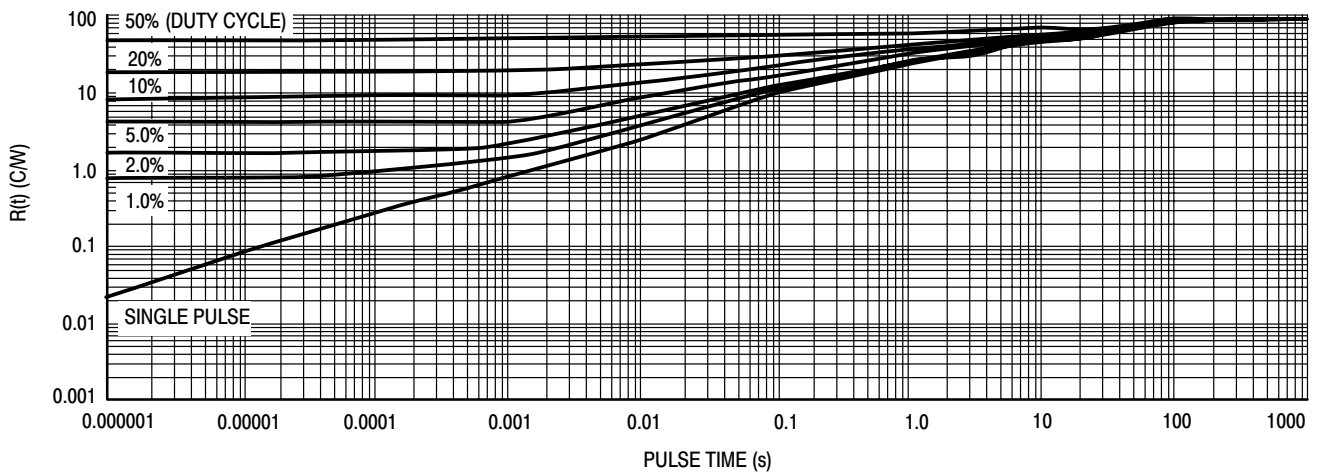
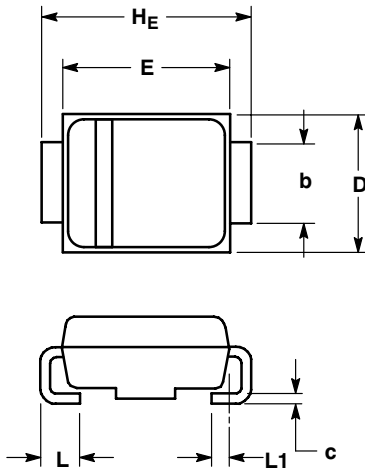


Figure 9. Thermal Response, Junction-to-Case

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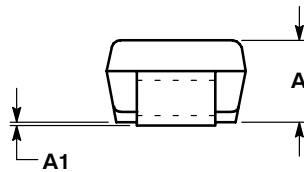
PACKAGE DIMENSIONS

SMB CASE 403A-03 ISSUE F

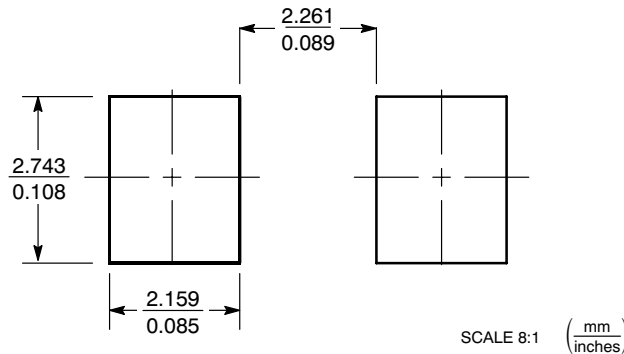


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.


DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.90	2.13	2.45	0.075	0.084	0.096
A1	0.05	0.10	0.20	0.002	0.004	0.008
b	1.96	2.03	2.20	0.077	0.080	0.087
c	0.15	0.23	0.31	0.006	0.009	0.012
D	3.30	3.56	3.95	0.130	0.140	0.156
E	4.06	4.32	4.60	0.160	0.170	0.181
HE	5.21	5.44	5.60	0.205	0.214	0.220
L	0.76	1.02	1.60	0.030	0.040	0.063
L1	0.51 REF			0.020 REF		



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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