

Hybride Impuls-Laserdiode mit integrierter Treiberstufe 70 W Spitzenleistung Hybrid Pulsed Laser Diode with Integrated Driver Stage 70 W Peak Power

Lead (Pb) Free Product - RoHS Compliant

SPL LL90_3



Besondere Merkmale

- Kleines kostengünstiges Plastik-Gehäuse
- Integriert sind ein FET und Kondensatoren zur Impulsansteuerung
- InAlGaAs/GaAs kompressiv verspannte Quantenfilmstruktur
- Hochleistungslaser mit „Large-Optical-Cavity“ (LOC) Struktur
- Nanostack Lasertechnologie beinhaltet mehrere epitaktisch integrierte Emitter
- Laserapertur 200 µm x 10 µm
- Schneller Betrieb (< 30 ns Impulsbreite)
- Niedrige Versorgungsspannung (< 20 V)

Anwendungen

- Entfernungsmessung
- Sicherheit, Überwachung
- Beleuchtung, Zündung
- Test- und Messsysteme

Sicherheitshinweise

Je nach Betriebsart emittieren diese Bauteile hochkonzentrierte, nicht sichtbare Infrarot-Strahlung, die gefährlich für das menschliche Auge sein kann. Produkte, die diese Bauteile enthalten, müssen gemäß den Sicherheitsrichtlinien der IEC-Norm 60825-1 behandelt werden.

Features

- Low cost, small size plastic package
- Integrated FET and capacitors for pulse control
- Strained InAlGaAs/GaAs QW-structures
- High power large-optical-cavity laser structure
- Nanostack laser technology including multiple epitaxially stacked emitters
- Laser aperture 200 µm x 10 µm
- High-speed operation (< 30 ns pulse width)
- Low supply voltage (< 20 V)

Applications

- Range finding
- Security, surveillance
- Illumination, ignition
- Testing and measurement

Safety advices

Depending on the mode of operation, these devices emit highly concentrated non visible infrared light which can be hazardous to the human eye. Products which incorporate these devices have to follow the safety precautions given in IEC 60825-1 "Safety of laser products".

Typ	Emitteranzahl	Opt. Spitzenausgangsleistung Opt. Peak Power	Wellenlänge	Bestellnummer
Type	Number of Emitters			Ordering Code
SPL LL90_3	3	70 W	905 nm	Q65110A1009

Grenzwerte (kurzzeitiger Betrieb) ($T_A = 25^\circ\text{C}$)**Maximum Ratings (short time operation)**

Parameter Parameter	Symbol Symbol	Werte Values		Einheit Unit
		min.	max.	
Spitzenausgangsleistung Peak output power	P_{opt}	–	80	W
Ladespannung ($V_G = 15 \text{ V}$) Charge voltage ($V_G = 15 \text{ V}$)	V_C		20	V
Gate-Spannung Gate voltage	V_G	– 20	+ 20	V
Tastverhältnis Duty cycle	<i>d.c.</i>	–	0.1	%
Betriebstemperatur Operating temperature	T_{op}	- 40	+ 85	$^\circ\text{C}$
Temperatur des pn-Übergangs ¹⁾ Junction temperature ¹⁾	T_j	–	+ 105	$^\circ\text{C}$
Lagertemperatur Storage temperature	T_{stg}	- 40	+ 100	$^\circ\text{C}$
Löttemperatur ($t_{\text{max}} = 10 \text{ s}$) Soldering temperature ($t_{\text{max}} = 10 \text{ s}$)	T_s	–	+ 260	$^\circ\text{C}$

¹⁾ limited due to plastic package, not due to laser chip

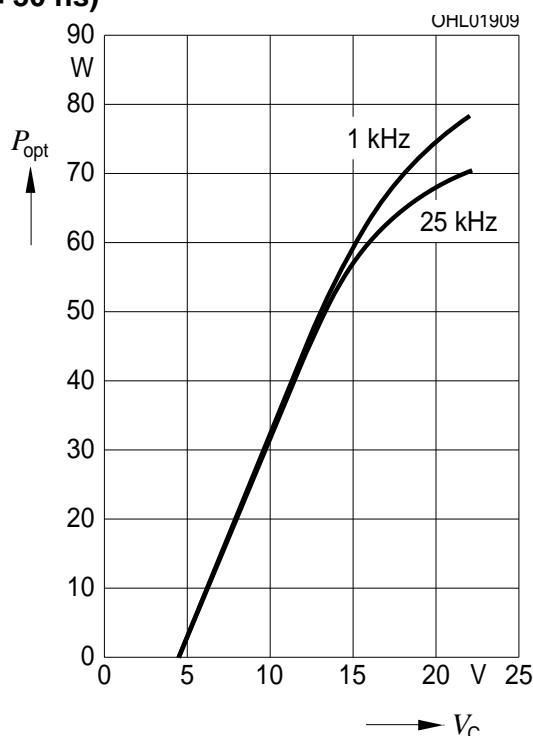
Optische Kennwerte ($T_A = 25^\circ\text{C}$)
Optical Characteristics

Parameter Parameter	Symbol Symbol	Werte Values			Einheit Unit
		min.	typ.	max.	
Zentrale Emissionswellenlänge ¹⁾ Emission wavelength ¹⁾	λ	895	905	915	nm
Spektralbreite (Halbwertsbreite) ¹⁾ Spectral width (FWHM) ¹⁾	$\Delta\lambda$	–	7	–	nm
Spitzenausgangsleistung ¹⁾ Peak output power ¹⁾	P_{opt}	60	70	80	W
Ladespannung an der Laserschwelle Charge Voltage at laser threshold	$U_{C, \text{th}}$	4.0	4.5	5.0	V
Pulsbreite (Halbwertsbreite) ^{1), 2)} Pulse width (FWHM) ^{1), 2)}	t_p	37	40	43	ns
Anstiegs- und Abfallzeit (10% ... 90%) ^{1), 2)} Rise and fall time (10% ... 90%) ^{1), 2)}	t_r, t_f	7 40	10 45	13 50	ns ns
Jitter (bzgl. Triggersignal und optischem Puls) Jitter (regarding trigger signal and optical pulse)	t_j		170	500	ps
Austrittsöffnung Aperture size	$w \times h$	–	200 × 10	–	μm^2
Strahldivergenz (Halbwertsbreite) parallel zum pn-Übergang ¹⁾ Beam divergence (FWHM) parallel to pn junction ¹⁾	$\theta_{ }$	12	15	18	Grad deg.
Strahldivergenz (Halbwertsbreite) senkrecht zum pn-Übergang ¹⁾ Beam divergence (FWHM) perpendicular to pn-junction ¹⁾	θ_{\perp}	27	30	33	Grad deg.
Temperaturkoeffizient der Wellenlänge Temperature coefficient of wavelength	$\partial\lambda / \partial T$	–	0.30	0.33	nm/K
Thermischer Widerstand Thermal resistance	R_{th}	–	200	–	K/W
Einschaltpunkt der Gate-Spannung Switch on gate voltage	$V_{G \text{ on}}$	–	5.0	–	V

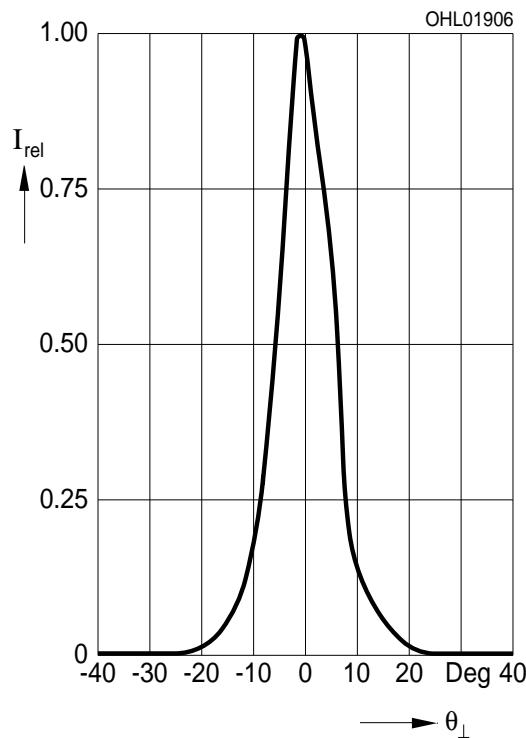
¹⁾ Werte beziehen sich auf folgende Standardbetriebsbedingung: >50 ns Pulsbreite, 1 kHz Pulswiederholrate, 18.5 V Ladespannung, 15 V Gate-Spannung und 25°C Umgebungstemperatur. Der Laser wird angesteuert mit dem MOSFET-Treiber Elantec EL7104C.
Values refer to the following standard operating conditions: >50 ns pulse width, 1 kHz pulse repetition rate, 18.5 V charge voltage, 15 V gate voltage and 25 °C ambient temperature. The laser is driven by the MOSFET driver Elantec EL7104C.

²⁾ Die Schaltgeschwindigkeit ist abhängig von Strom und Geschwindigkeit, mit der die Gate-Kapazität (typ. 300 pF) des internen Transistors geladen wird. Kürzere Pulsbreiten, Anstiegs- und Abfallzeiten erhält man bei Trigger-Pulsbreiten <50 ns. Dies bewirkt jedoch auch eine reduzierte optische Spitzenleistung.
Switching speed at gate depends on current and speed, charging the gate capacitance (typ. 300 pF) of the internal transistor. Reduced pulse widths, rise and fall times occur at trigger pulse widths <50 ns. This also reduces the optical peak power.

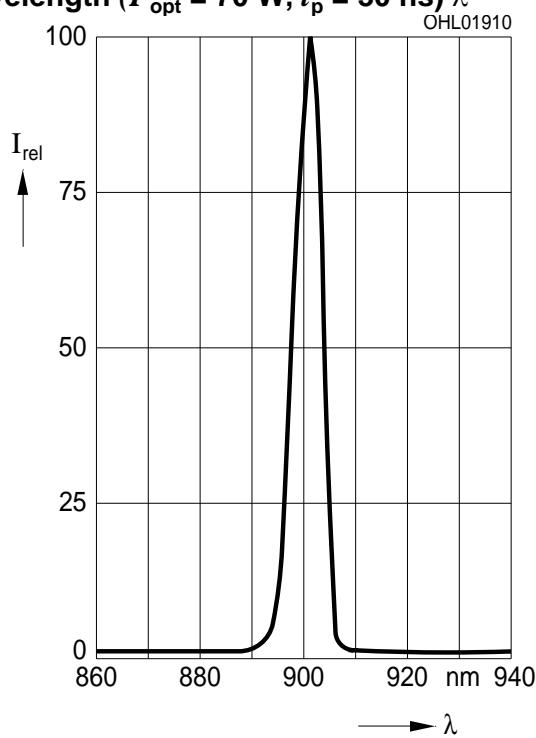
Optical output power P_{opt} vs charge voltage V_c
($t_p = 30 \text{ ns}$)



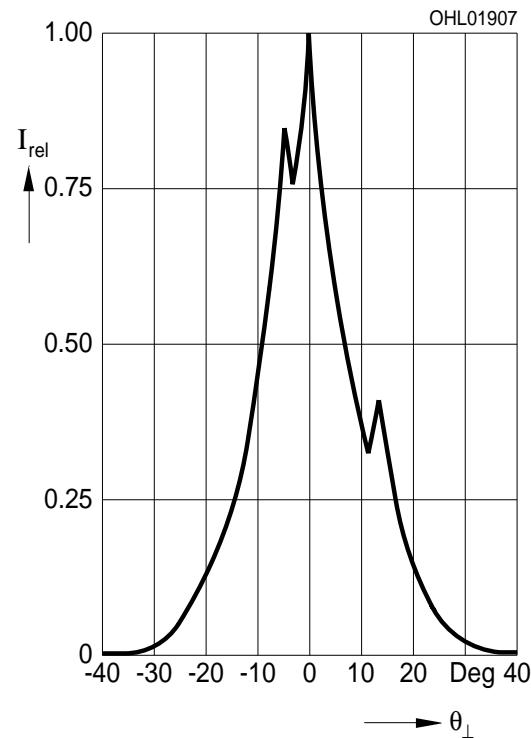
**Far-field distribution parallel to junction
 I_{rel} vs. angle $\theta_{||}$** ($P_{\text{opt}} = 70 \text{ W}$, $t_p = 30 \text{ ns}$)



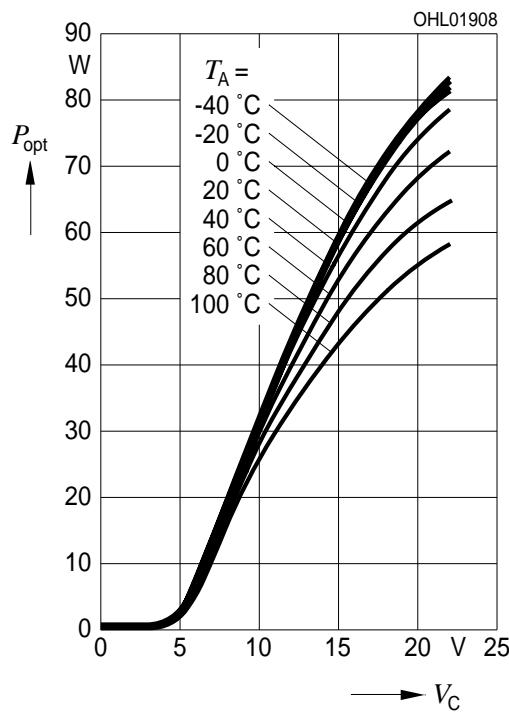
**Optical spectrum, relative intensity I_{rel} vs.
wavelength ($P_{\text{opt}} = 70 \text{ W}$, $t_p = 30 \text{ ns}$)**



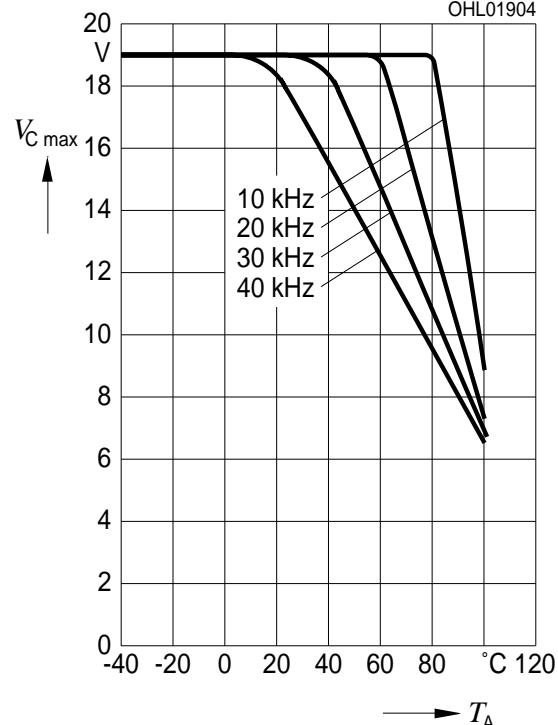
**Far-field distribution perpendicular to
junction I_{rel} vs. angle θ_{\perp}** ($P_{\text{opt}} = 70 \text{ W}$, $t_p = 30 \text{ ns}$)



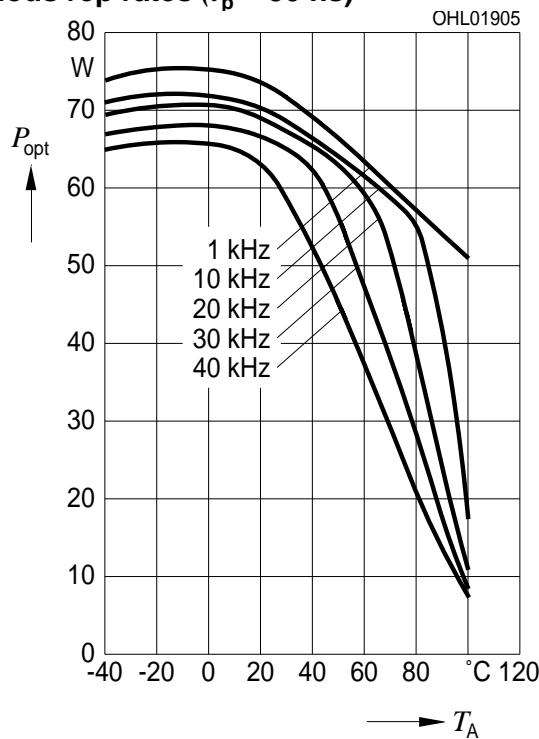
Optical output power P_{opt} vs charge voltage V_c
 $(t_p = 30 \text{ ns}, \text{PRF} = 1 \text{ kHz})$ at different ambient temperature T_{amb}



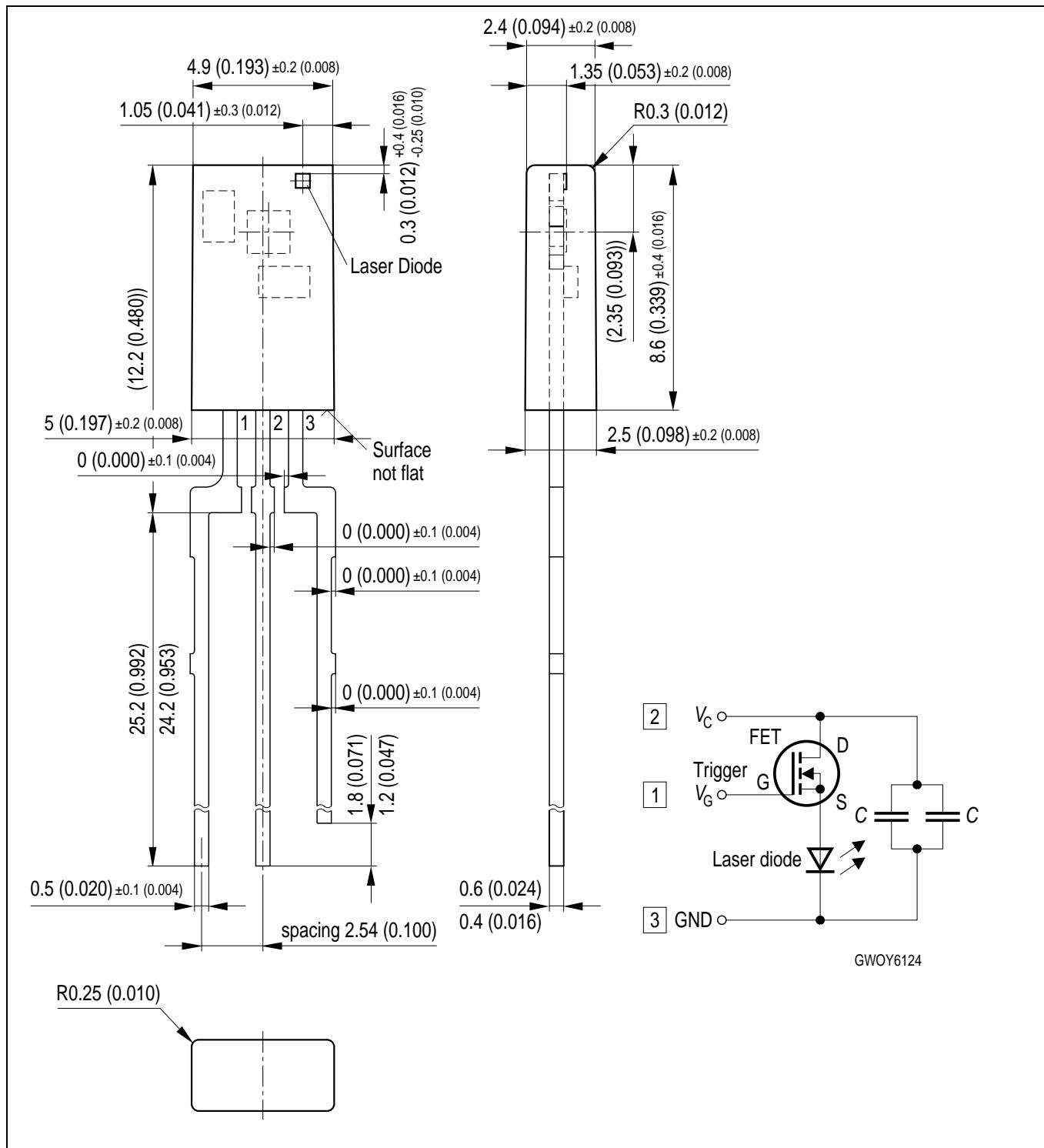
Maximum allowed charge voltage $V_{c,\text{max}}$ vs. ambient temperature T_{amb} at various rep rates
 $(t_p=30 \text{ ns}, V_c \leq 19 \text{ V}, \text{chip temperature} \leq 105^\circ \text{C})$



Peak output power at maximum charge voltage $V_{c,\text{max}}$ vs. ambient temperature T_{amb} at various rep rates ($t_p = 30 \text{ ns}$)



Maßzeichnung Package Outlines



Maße in mm (inch) / Dimensions in mm (inch).

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