







The B0011E75300A00 is a low profile sub-miniature balanced to unbalanced transformer designed for differential inputs and output locations on next generation analog and digital TV chipsets in an easy to use surface mount package. The B0011E75300A00 is ideal for high volume manufacturing and is higher performance than traditional wire wound baluns. The B0011E75300A00 has an unbalanced port impedance of 75 Ω and a 300Ω balanced port impedance. This transformation enables single ended signals to be applied to differential ports on modern semiconductors. The output ports have equal amplitude (-3dB) with 180 degree phase differential. The B0011E75300A00 is available on tape and reel for pick and place high volume manufacturing.

Detailed Electrical Specifications: Specifications subject to change without notice.

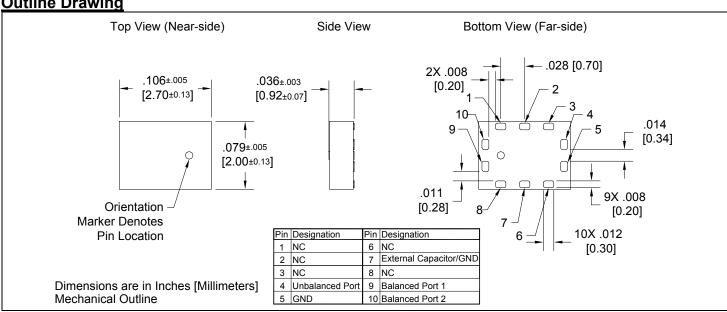
Features:

- 48 1080 MHz
- 1.0 mm Height Profile
- 75 Ohm to 2 x 150 Ohm
- DVB-T. DVB-H and DVB-C
- **Low Insertion Loss**
- **Surface Mountable**
- Tape & Reel
- **Non-conductive Surface**
- **RoHS Compliant**

	R			
Parameter	Min.	Тур.	Max	Unit
Frequency	48		1080	MHz
Unbalanced Port Impedance		75		Ω
Balanced Port Impedance		300		Ω
Return Loss	9	11		dB
Insertion Loss*		1.3	1.6	dB
Amplitude Balance		0.6	1.5	dB
Phase Balance		6	15	Degrees
CMRR		28		dB
Power Handling			TBD	Watts
Operating Temperature	-55		+85	°C

^{*} Insertion Loss stated at room temperature (Insertion Loss is approximately 0.15 dB higher at +85 °C). All performances stated for recommended operation with external circuitry.

Outline Drawing







Available on Tape and Reel for Pick and Place Manufacturing.

USA/Canada: Toll Free: Europe:



Typical Performance with Two External 82pF Capacitors (test/specification condition)

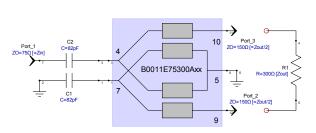
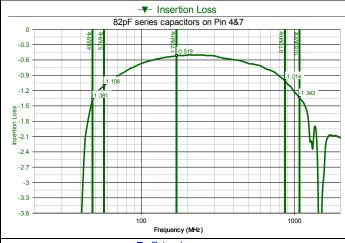
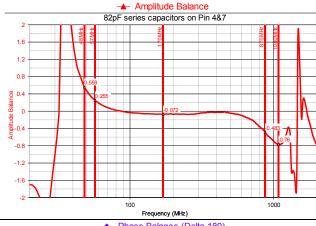
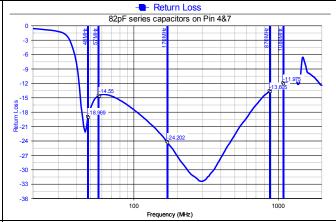
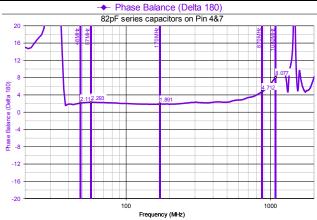


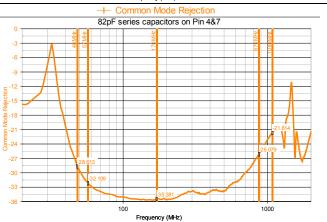
Figure 1 The Balun with external capacitors on both low impedance ports (nominal/test configuration). The two capacitors improve the insertion loss and return loss in the 40-200MHz range over that of the basic balun.













Typical Performance with One External 82pF Capacitor

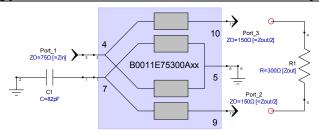
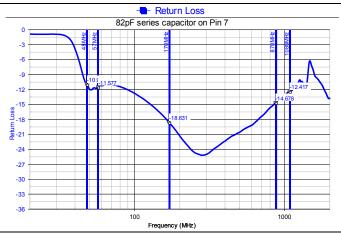
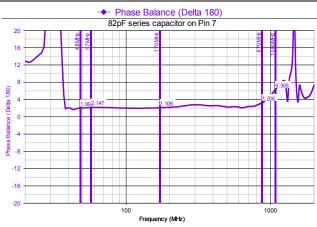


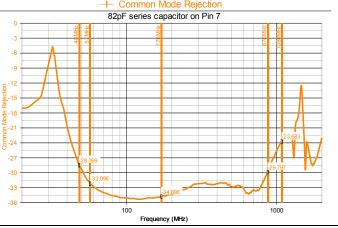
Figure 2 The Balun with an external capacitor on the low impedance ground pin. This improves insertion loss and return loss in the 40-200MHz range over that of the basic configuration. The capacitor value can be changed some to get a slightly different performance. A series capacitor with just pin 4 does NOT produce identical results as the one just in series with pin 7.







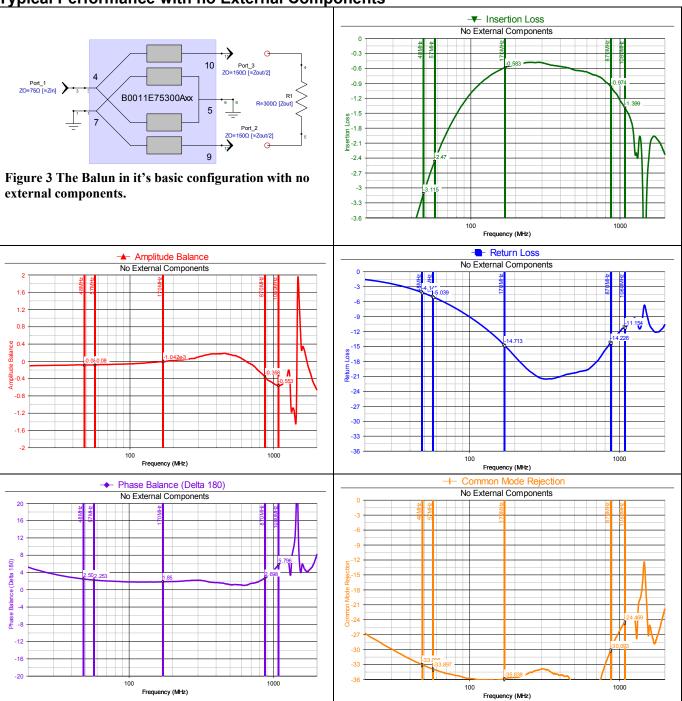








Typical Performance with no External Components







Additional Configurations

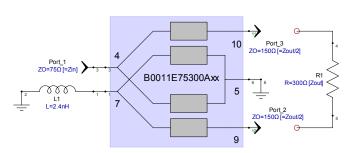


Figure 4 The balun with an external inductor in the low impedance ground path. This improves the insertion loss, return loss and balanced performance in the 870-1080MHz range. This inductance can be realized as a transmission line (dimensions will depend on the PCB material and ground plane distance). The added inductance can be used in conjunction with the external capacitors. When mounted with high groundplane spacing, part of this inductance comes automatically from the increased inductance of the typical via to ground.

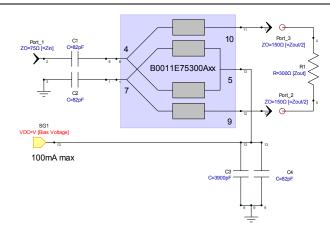


Figure 5 Biasing the balanced signals thru the balun. Dependign on the quality of "C3" the additional capacitor "C4" may not be needed. The DC-resistance from the bias point thru to each of the outputs are $10\pm4\Omega$. It is important to keep inductance to a minimum from pin 5 to ground or balanced performance will suffer. When used with high groundplane spacings it is recommended to have two or more via to ground.

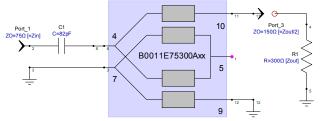


Figure 6 Using the balun as an single ended to single ended impedance transformer (Unun). Pin 5 must be open circuit. It is recommended to use the 82pF on the low impedance side to improve the insertion loss and return loss in the 50-200MHz range. Note that pins 4 & 7 can only be interchanged if pins 9 7 10 are also interchanged, otherwise performance will suffer.





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Distortion Considerations:

This balun does NOT contain any ferrite materials and are as such distortion free. Very, very low levels of distortion can arise from dissimilar metals on the contact pads of the part (Cu-Ni-Au) and from inter-metallic contaminations within the part.

Power Handling Considerations:

The RF-power handling capability of these baluns depends on the temperature at the mounted interface (base plate temperature), and whether or not DC-bias is feed through the part. The power handling specified is for a mounted temperature of 85°C and no DC-bias and is based on a maximum operating temperature of the part itself of 125°C (do not exceed this).

The maximum power handling relates to the dissipated power within the part and is therefore a function of insertion loss. The insertion loss of this device changes within the specified band. Thus if operated with a narrowband signal at either band edge (this is what is specified) it will have less power handling than if operated closer to the center of the band or if operated with a very wideband signal. If signals arrive at the balun that are out of the specified band, these must be included in the total dissipated power calculation. However the deteriorating return loss will eliminate some of this power.

The dissipated RF power can be estimated as;

$$P_{RF,diss} = P_{in} \left(1 - 10^{-\left(\frac{RL_{dB}}{10}\right)} \right) \left(1 - 10^{-\left(\frac{IL_{dB}}{10}\right)} \right) [W]$$

Where:

- P_{in} is the input power, either on the low impedance side, single ended or the combined differential power on the balanced side, depending on the feeding direction.
- RL_{dB} is the return loss in dB associated with the feeding port. I.e. if feed from the balanced side then the balanced return loss must be used (this however is very similar to the single ended return loss).
- IL_{dB} is the insertion loss at the operating temperature of the device.

Below the dissipated power is shown at a normalized 1W input power for the balun internal temperature of 125°C.

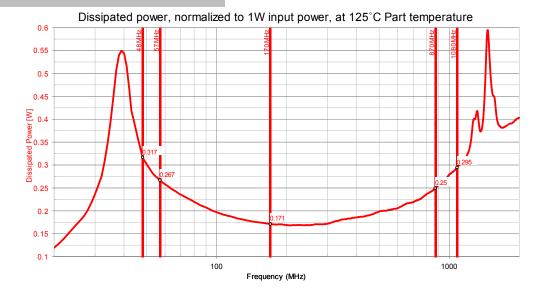
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Available on Tape and Reel for Pick and Place Manufacturing.









When used with DC-biasing the RF-power must be de-rated with the amount of DC power dissipated; $P_{dc,diss} = (R_{out1} || R_{out2})I_{dc}^2 \approx 7I_{dc}^2$ [W]

The total dissipated power cannot exceed TBD.

When the duty cycle is not 100% the input power can be averaged, however the instantaneous input power should newer exceed TBD.



Model B0011E75300A00

Rev B

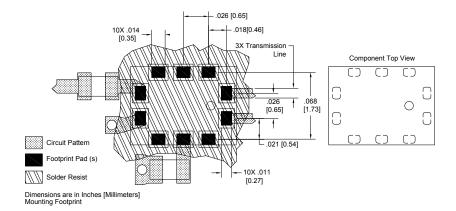


Mounting Configuration:

In order for Xinger surface mount components to work optimally, the proper impedance transmission lines must be used to connect to the RF ports. If this condition is not satisfied, insertion loss, Isolation and VSWR may not meet published specifications.

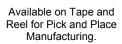
All of the Xinger components are constructed from softboard composites which possess excellent electrical and mechanical stability having X and Y thermal coefficient of expansion (CTE) of 19 ppm/°C.

An example of the PCB footprint used in the testing of these parts is shown below. In specific designs, the transmission line widths need to be adjusted to the unique dielectric coefficients and thicknesses as well as varying pick and place equipment tolerances.



The material used for testing is 8mil Rogers 4003. For a ground plane spacing much larger than this the single via at pin 5 should be replaced with at least 2 via and if possible a coplanar ground.

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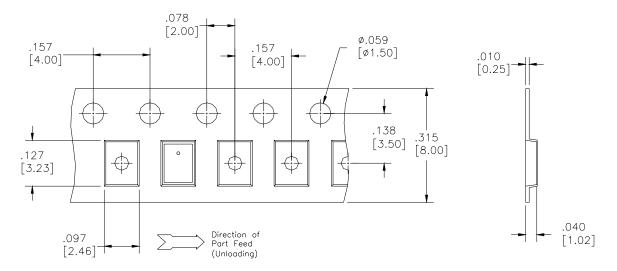




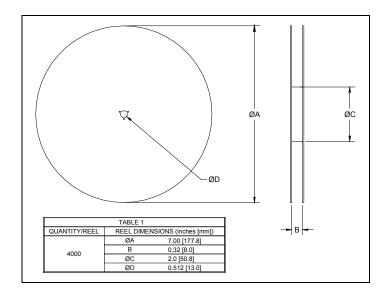


Packaging and Ordering Information

Parts are available in reel and are packaged per EIA 481-2. Parts are oriented in tape and reel as shown below. Minimum order quantities are 4000 per reel. See Model Numbers below for further ordering information.



Dimensions are in inches [mm]





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BD 2425 J 50 100 A 00

			_						
Function	on	Frequency	Package	Unbalar	nced Balan	Balanced Impedance Plat		Codes	
			Dimensions	Impeda	nce +	Coupling	Finish	Code	75
B = Balun	1	10 = 100 - 1000 MHz	A = 150 x 150 mils	50 = 50 Ohn		Ω Balanced	A = Gold		
BD = Balun + I	DC 08.	10 = 800 - 1000 MHz	(4mm × 4mm)	75 = 75 Ohn	n 30 = 30 s	Ω Balanced	P = Tin-Lead		
F = Filter	092	22 = 950 - 2150 MHz	C = 120 x 120 mils		50 = 50	Ω Balanced			
FB = Filter / Ba	alun 082	26 = 800 - 6200 MHz	(3mm x 3mm)		75 = 75	Ω Balanced			
C = 3dB Coupl	ler 122	22 = 1200 - 2200 MHz	E = 100 x 80 mils		100 = 10	0 Ω Balanced			
DC = Direction	nal 14°	16 = 1400 - 1600 MHz	(2.5mm x 2mm)		150 = 15	0 Ω Balanced			
J = RF Jumper	r 173	22 = 1700 - 2200 MHz	$J = 80 \times 50 \text{ mils}$		200 = 20	0 Ω Balanced			
X = RF cross o	over 23	26 = 2300 - 2600 MHz	(2mm x 1.25mm)		300 = 30	0 Ω Balanced			
	242	25 = 2400 - 2500 MHz	L = 60 x 30 mils		400 = 40	0 Ω Balanced			
	319	50 = 3100 - 5000 MHz	(1.5mm x 0.75mm)		03 = 3dE	3 Hybrid			
	343	36 = 3400 - 3600 MHz	N = 40 x 40 mils		10 = 10 a	IB Directional			
	485	59 = 4800 - 5900MHz	(1mm x 1mm)		20 = 20c	IB Directional			
	519	53 = 5100 - 5300 MHz							
	515	59 = 5100 - 5900 MHz							
	575	59 = 5700 - 5900 MHz							

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