Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

## SSM6K07FU

# DC-DC Converters High Speed Switching Applications

• Small package

• Low on resistance:  $R_{on} = 130 \text{ m}\Omega \text{ max} (@V_{GS} = 10 \text{ V})$ 

:  $R_{on} = 220 \text{ m}\Omega \text{ max} (@V_{GS} = 4 \text{ V})$ 

• Low input capacitance: C<sub>iss</sub> = 102 pF typ.

:  $C_{rss} = 22 pF typ$ .

### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DS}$	30	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC	ID	1.5	Α	
	Pulse	I <sub>DP</sub>	3.0	A	
Drain power dissipation		P <sub>D</sub> (Note 1)	300	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	<b>−55~150</b>	°C	



(25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu pad: 0.32 mm<sup>2</sup>  $\times$  6)

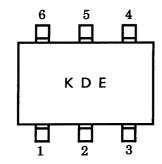
# 1, 2, 5, 6 : DRAIN 3 : GATE 4 : SOURCE US6 JEDEC — JEITA —

2-2J1D

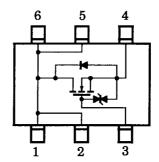
Weight: 6.8 mg (typ.)

**TOSHIBA** 

### Marking



### **Equivalent Circuit (top view)**



### **Handling Precaution**

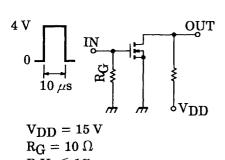
When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	e leakage current $I_{GSS}$ $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$		_	_	±1	μΑ	
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	30	_	_	V
Drain cut-off curre	cut-off current $I_{DSS}$ $V_{DS} = 30 \text{ V}, V_{GS} = 0$		_	_	1	μА	
Gate threshold vo	ltage	V <sub>th</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 0.1 mA	1.1	_	1.8	V
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_D = 0.75 \text{ A}$ (Note 2)	1.0	_	_	S
Drain-source ON resistance		R <sub>DS</sub> (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 2)	_	105	130	mΩ
		R <sub>DS</sub> (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 4 \text{ V}$ (Note 2)	_	170	220	
		R <sub>DS</sub> (ON)	$I_D = 0.75 \text{ A}, V_{GS} = 3.3 \text{ V}$ (Note 2)	_	230	500	
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1 MHz	_	102	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1 MHz	_	22	_	pF
Output capacitance		Coss	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0, f = 1 MHz	_	57	_	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = 15 \text{ V}, I_D = 0.75 \text{ A}, V_{GS} = 0~4 \text{ V}, R_G = 10 \Omega$	_	46	_	ns
	Turn-off time	t <sub>off</sub>		_	65	_	

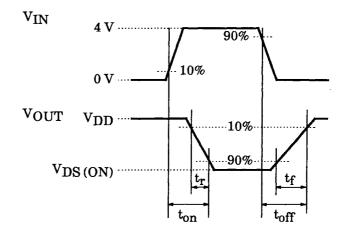
Note 2: Pulse test

### **Switching Time Test Circuit**



$$\begin{split} &D.U. \leqq 1\% \\ &V_{\mbox{\footnotesize{IN}}}: \, t_{\mbox{\footnotesize{r}}}, \, t_{\mbox{\footnotesize{f}}} < 5 \, \mbox{ns} \\ &\mbox{\footnotesize{COMMON SOURCE}} \end{split}$$

 $Ta = 25^{\circ}C$ 



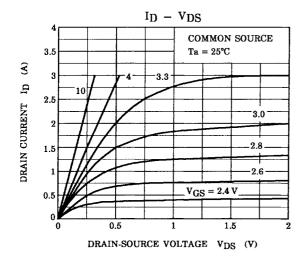
### **Precaution**

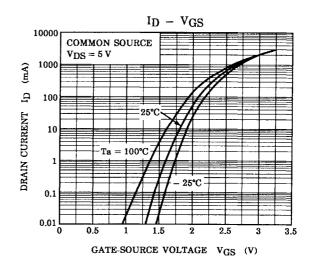
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is ID = 100  $\mu A$  for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ .

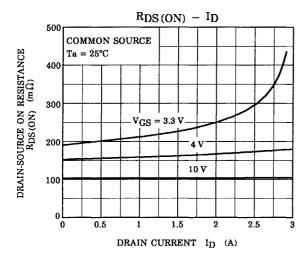
(Relationship can be established as follows:  $V_{GS}$  (off) <  $V_{th}$  <  $V_{GS}$  (on))

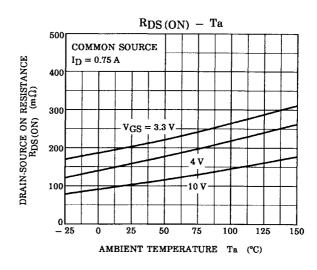
Please take this into consideration for using the device.

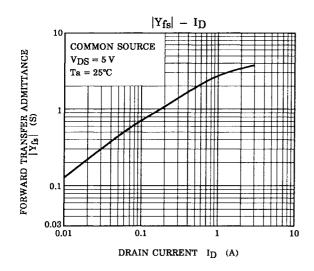
VGS recommended voltage of 4 V or higher to turn on this product.

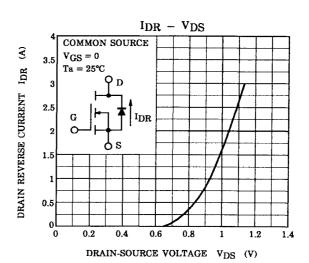




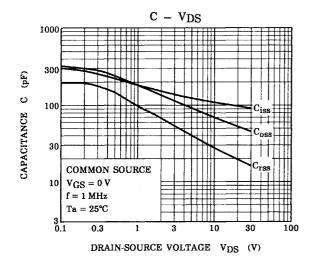


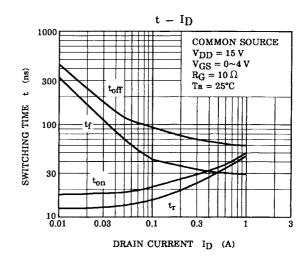


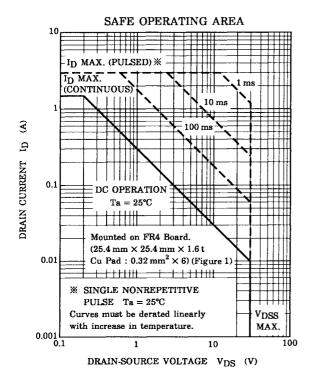


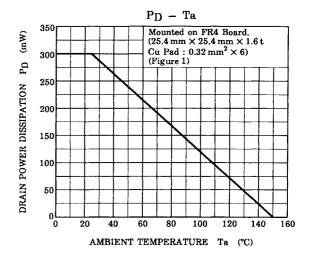


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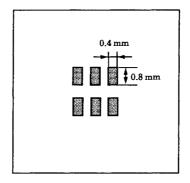


Figure 1 25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu Pad: 0.32 mm<sup>2</sup>  $\times$  6

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### **RESTRICTIONS ON PRODUCT USE**

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