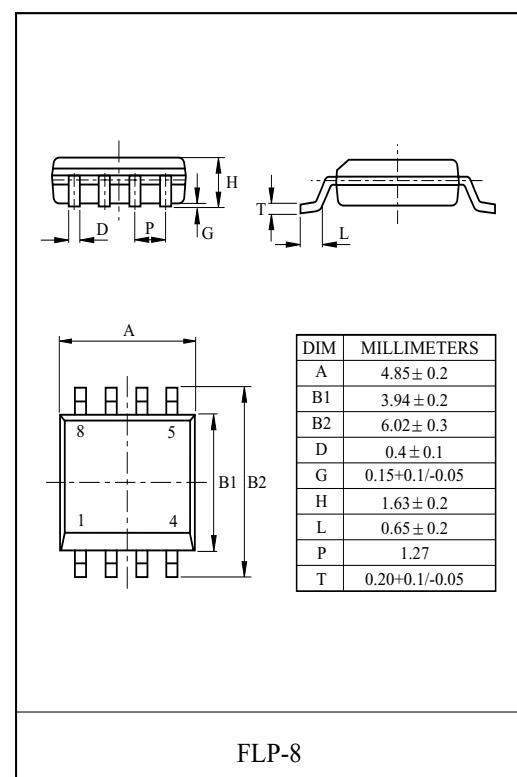


General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for portable equipment and SMPS.

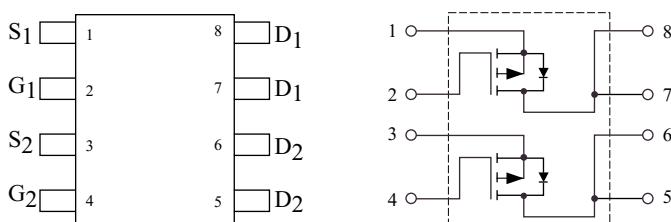
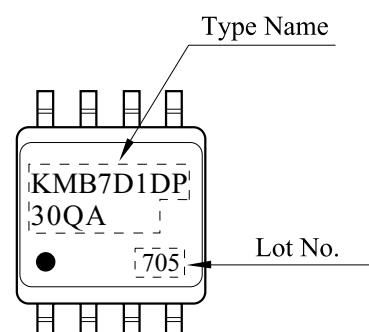
FEATURES

- $V_{DSS} = -30V$, $I_D = -7.1A$
- Drain-Source ON Resistance
 $R_{DS(ON)} = 25m\Omega$ (Max.) @ $V_{GS} = -10V$
- $R_{DS(ON)} = 41m\Omega$ (Max.) @ $V_{GS} = -4.5V$
- Super Hige Dense Cell Design

**MAXIMUM RATING (Ta=25 °C)**

CHARACTERISTIC		SYMBOL	N-Ch	UNIT
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	± 22	V
Drain Current	DC@ $T_A = 25^\circ C$	I_D	-7.1	A
	DC@ $T_A = 70^\circ C$		-5.7	
	Pulsed	I_{DP}	-40	
Drain-Source-Diode Forward Current		I_S	-1.7	A
Drain Power Dissipation	$T_A = 25^\circ C$	P_D	1.1	W
	$T_A = 70^\circ C$		0.7	
Maximum Junction Temperature		T_j	150	°C
Storage Temperature Range		T_{stg}	-55~150	°C
Thermal Resistance, Junction to Ambient		R_{thJA}	110	°C/W

Note : Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.

PIN CONNECTION (TOP VIEW)**Marking**

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ELECTRICAL CHARACTERISTICS (Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _{DS} =-10μA, V _{GS} =0V,	-30	-	-	V
Drain Cut-off Current	I _{DSS}	V _{GS} =0V, V _{DS} =-30V	-	-	-1	μA
		V _{GS} =0V, V _{DS} =-30V, T _j =55 °C	-	-	-25	
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =-250μA	-1	-	-3	V
Drain-Source ON Resistance	R _{DS(ON)*}	V _{GS} =-10V, I _D =-7.1A	-	20	25	mΩ
		V _{GS} =-4.5V, I _D =-5.5A	-	33	41	
Forward Transconductance	g _{fs} *	V _{DS} =-10V, I _D =-7.1A	-	20	-	S
Dynamic						
Input Capacitance	C _{iss}	V _{DS} =15V, f=1MHz, V _{GS} =0V	-	1550	-	pF
Output Capacitance	C _{oss}		-	420	-	
Reverse Transfer Capacitance	C _{rss}		-	380	-	
Total Gate Charge	Q _g *	V _{DS} =-15V, V _{GS} =-10V, I _D =-7.1A	-	33	50	nC
Gate-Source Charge	Q _{gs} *		-	5.4	-	
Gate-Drain Charge	Q _{gd} *		-	8.9	-	
Turn-On Delay Time	t _{d(on)*}	V _{DD} =-15V, V _{GS} =-10V I _D =-1A, R _G =6 Ω	-	9	15	ns
Turn-On Rise Time	t _r *		-	12	20	
Turn-On Delay Time	t _{d(off)*}		-	60	90	
Turn-On Fall Time	t _f *		-	34	50	
Source-Drain Diode Ratings						
Source-Drain Forward Voltage	V _{SDF*}	V _{GS} =0V, I _{DR} =-1.7A	-	-0.8	-1.2	V

* : Pulse Test : Pulse width <300μs , Duty cycle < 2%

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Fig1. I_D - V_{DS}

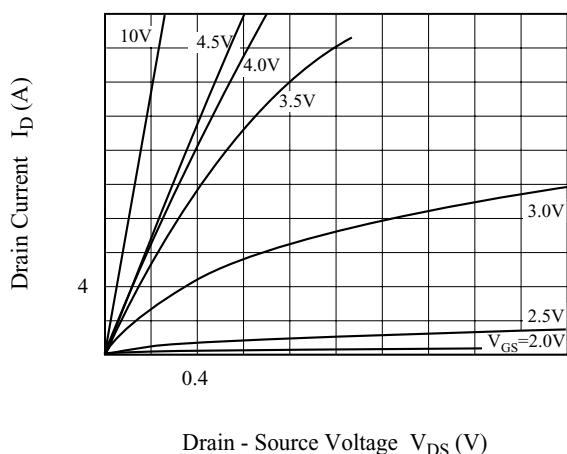


Fig2. $R_{DS(ON)}$ - I_D

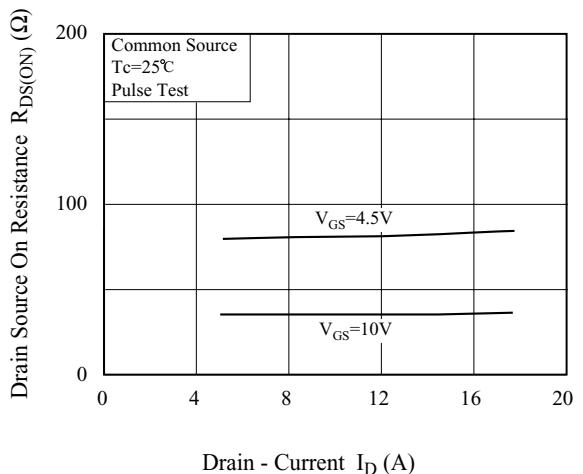


Fig3. I_D - V_{GS}

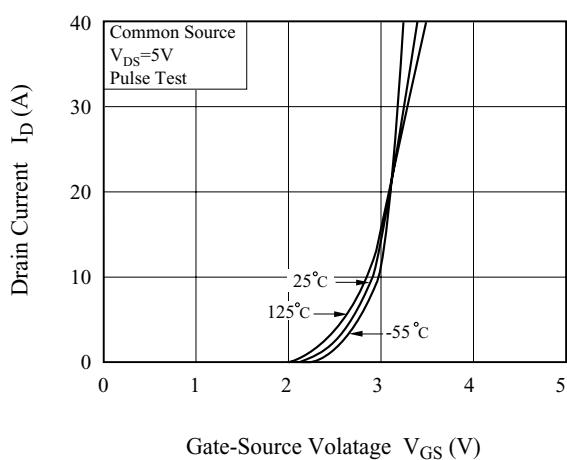


Fig4. $R_{DS(on)}$ - T_j

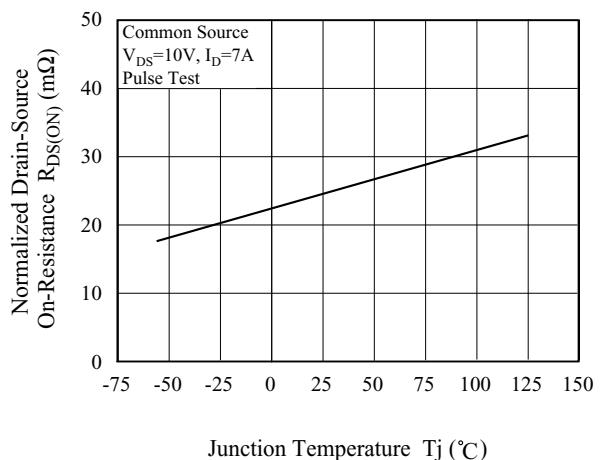


Fig5. V_{th} - T_j

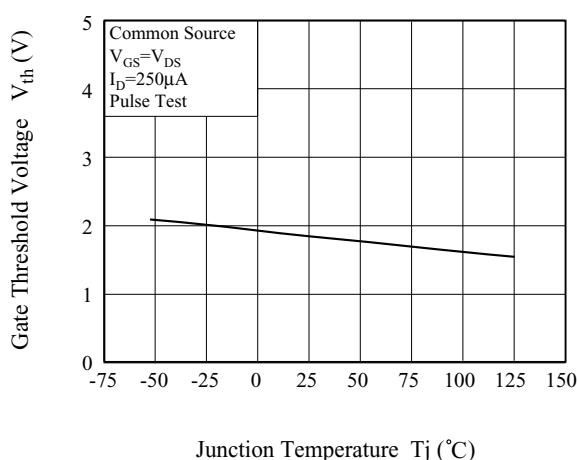
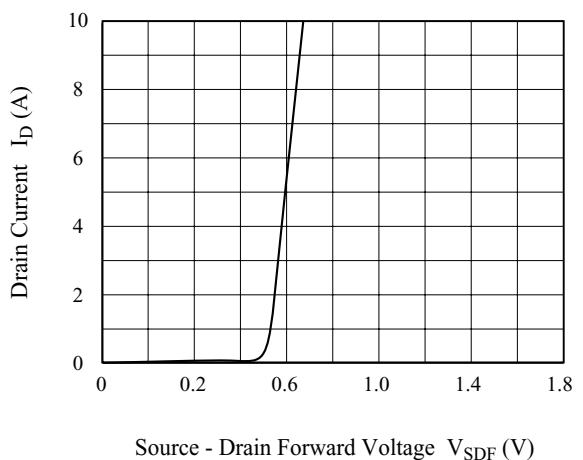


Fig6. I_S - V_{SDF}



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Fig7. Transient Thermal Response Curve

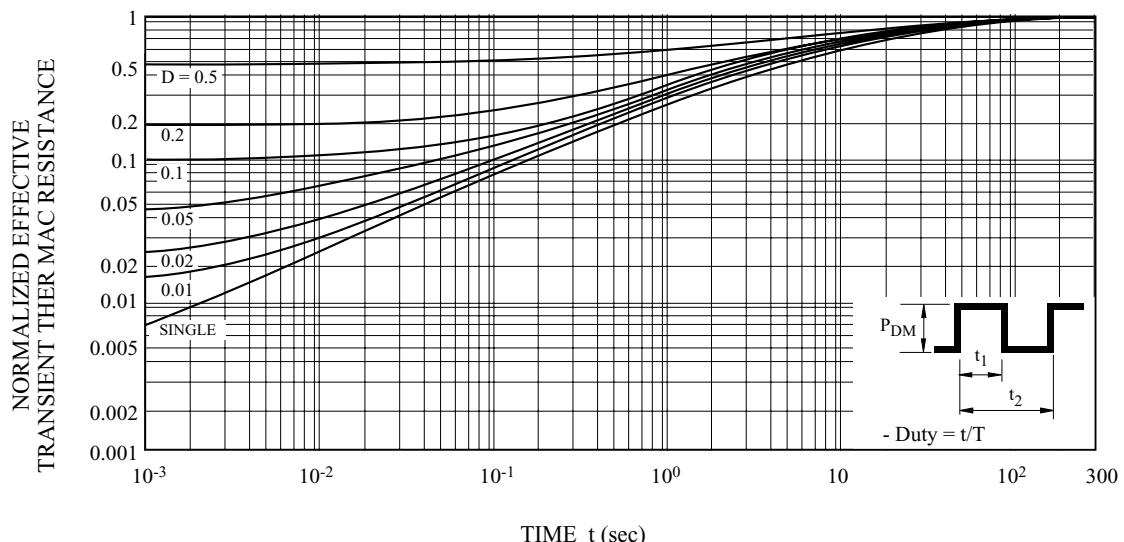
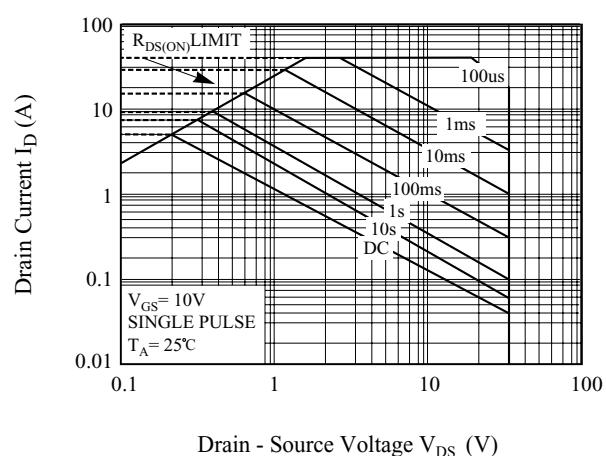


Fig8. Safe Operation Area



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Fig9. Gate Charge Circuit and Wave Form

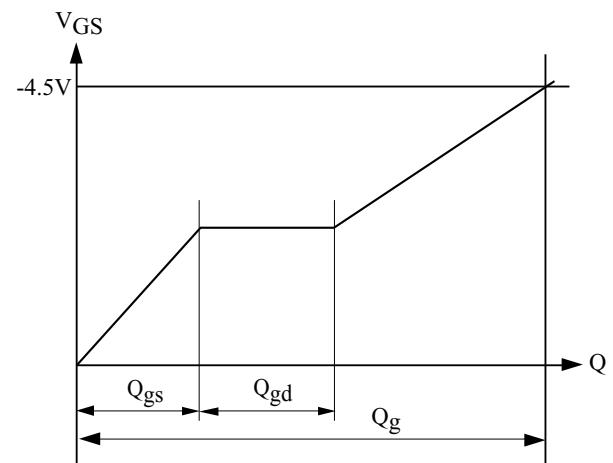
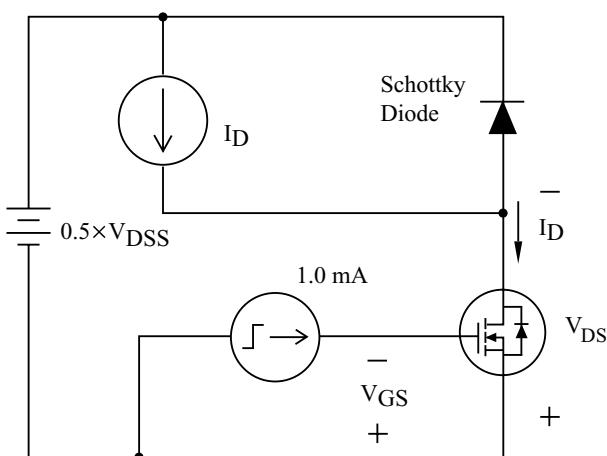


Fig10. Resistive Load Switching

